

1. Description

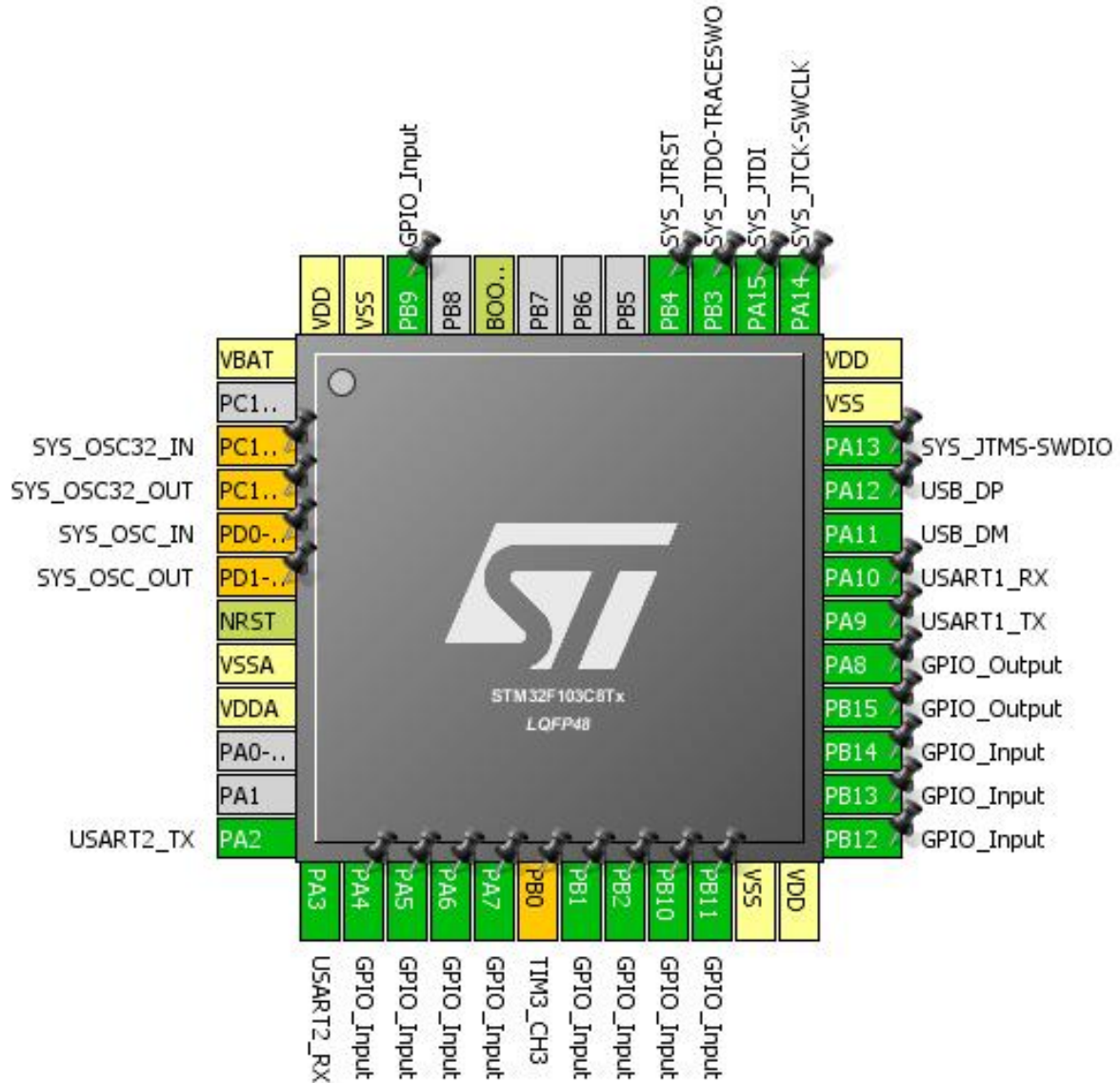
1.1. Project

Project Name	UAVRC_stm32f103c8t6
Generated with:	STM32CubeMX 4.4.0
Date	04/07/2015

1.2. MCU

MCU Serie	STM32F1
MCU Line	STM32F103
MCU name	STM32F103C8Tx
MCU Package	LQFP48
MCU Pin number	48

2. Pinout Configuration



3. IPs and Middlewares Configuration

IP	Mode	Fonction	Pin
SYS	Debug: JTAG(5-pin)	SYS_JTCK-SWCLK	PA14
		SYS_JTDI	PA15
		SYS_JTDO-TRACESWO	PB3
		SYS_JTMS-SWDIO	PA13
		SYS_JTRST	PB4
USART1	Mode: Asynchronous	USART1_RX	PA10
		USART1_TX	PA9
USART2	Mode: Asynchronous	USART2_RX	PA3
		USART2_TX	PA2
USB	Device (FS)	USB_DM	PA11
		USB_DP	PA12

4. Pins Configuration

Pin	Pos	Function(s)	Label
PC14-OSC32_IN *	3	SYS_OSC32_IN	
PC15-OSC32_OUT *	4	SYS_OSC32_OUT	
PD0-OSC_IN *	5	SYS_OSC_IN	
PD1-OSC_OUT *	6	SYS_OSC_OUT	
PA2	12	USART2_TX	
PA3	13	USART2_RX	
PA4 **	14	GPIO_Input	
PA5 **	15	GPIO_Input	
PA6 **	16	GPIO_Input	
PA7 **	17	GPIO_Input	
PB0 *	18	TIM3_CH3	
PB1 **	19	GPIO_Input	
PB2 **	20	GPIO_Input	
PB10 **	21	GPIO_Input	
PB11 **	22	GPIO_Input	
PB12 **	25	GPIO_Input	
PB13 **	26	GPIO_Input	
PB14 **	27	GPIO_Input	
PB15 **	28	GPIO_Output	
PA8 **	29	GPIO_Output	
PA9	30	USART1_TX	
PA10	31	USART1_RX	
PA11	32	USB_DM	
PA12	33	USB_DP	
PA13	34	SYS_JTMS-SWDIO	
PA14	37	SYS_JTCK-SWCLK	
PA15	38	SYS_JTDI	
PB3	39	SYS_JTDO-TRACESWO	
PB4	40	SYS_JTRST	
PB9 **	46	GPIO_Input	

** The pin is affected with an I/O function

* The pin is affected with a peripheral function but no peripheral mode is activated

5. Power Plugin report