

CENTRAL PROCESSING UNIT

System_Clk

Reset

Int_ack

Int_req

Mem_addr

Bus_data

32

32

I_data

I_addr

32

32

I/O

IOcs_

IOrd_

IOwr_

dc_

drd_

dwr_

Data Memory

32X1024

ics_

ird_

iwr_

Instruction Memory

32X1024

Top Level Module

