

# A Quantum Algorithm for the Bottleneck Travelling Salesman Problem

by

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# Abstract

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# Acknowledgements

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# Chapter 1

## Introduction

Continuous advancements in quantum computing have opened new ways to solve complex computational problems that were previously considered intractable using classical methods. Well known examples of these advancements include Shor's [1] and Grover's [2] algorithm for factoring and unstructured search respectively. The Bottleneck Traveling Salesman Problem (BTSP) serves as a challenging optimization problem in the field of logistics and operations research. Its practical applications range from optimizing vehicle routes to circuit design. Consequently, achieving an efficient solution is highly sought after. However, the BTSP is classified as an NP-hard problem, indicating it is at least as difficult as the hardest problems in the NP class. For reference, an NP problem must satisfy two conditions: no known solution in polynomial time, and a solution can be verified in polynomial time. An NP-hard problem does not need to satisfy the verification condition. To address this problem, many heuristic approaches are explored [3][4]. However, inherent to such methods is a trade-off between precision and efficiency.

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# Chapter 2

## Theory

### 2.1 The Bottleneck Travelling Salesman Problem

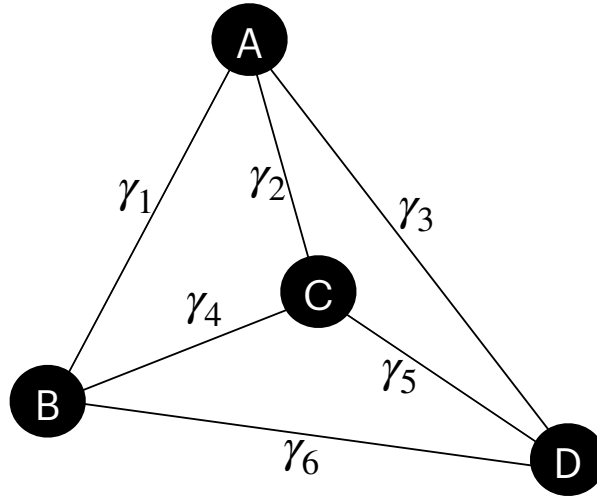


Figure 2.1: An undirected weighted graph representation of a symmetric 4-city system. The vertices represent cities and the edge weights represent the cost of travel.

The BTSP can be represented as a graph problem. We start with a graph, whose vertices are labelled A through D, representing a 4-city system. We define movement from one vertex to another as a walk, done through the edges connecting our vertices. We are interested in a particular walk known as a Hamiltonian cycle that contains every vertex exactly once before returning to the start. Our graph also includes edge weights, we define as  $\gamma_i$ . The BTSP is to find the Hamiltonian cycle in a graph, where the largest edge weight (bottleneck) is minimized. This is distinct from the Travelling Salesman Problem (TSP) where the combined edge weights in a given cycle is minimized. The total possible Hamiltonian cycles is given by  $(N - 1)!$ , where N is the number of nodes. We present a symmetric case in FIG .2.1,  $N_k \rightarrow N_{k+1} = N_{k+1} \rightarrow N_k = \gamma_i$ . Thus the total possible cycles is  $(N - 1)!/2$ . It is important to note that a solution to either BTSP or TSP is not unique. BTSP solutions also do not necessarily equate to the TSP solutions. We can illustrate an example below. Consider all the Hamiltonian cycles for a symmetric 4-city system:

$$\begin{aligned} A &\rightarrow B \rightarrow C \rightarrow D \rightarrow A \\ A &\rightarrow B \rightarrow D \rightarrow C \rightarrow A \\ A &\rightarrow C \rightarrow B \rightarrow D \rightarrow A \end{aligned}$$

Assigning some arbitrary weights, we can see the total costs of the cycles below. The first cycle is the solution to BTSP as its largest edge weight at 5 is the smallest among all three. The last cycle is a solution to the TSP as its combined edge weight is the smallest.

$$\begin{aligned}\gamma_1 + \gamma_4 + \gamma_5 + \gamma_3 &= 4 + 4 + 5 + 4 = 17 \\ \gamma_1 + \gamma_6 + \gamma_5 + \gamma_2 &= 4 + 6 + 5 + 2 = 17 \\ \gamma_2 + \gamma_4 + \gamma_6 + \gamma_3 &= 2 + 4 + 6 + 4 = 16\end{aligned}$$

By simply changing the weight of  $\gamma_6$  to 5, we can illustrate all cycles are solutions to the BTSP.

$$\begin{aligned}\gamma_1 + \gamma_4 + \gamma_5 + \gamma_3 &= 4 + 4 + 5 + 4 = 17 \\ \gamma_1 + \gamma_6 + \gamma_5 + \gamma_2 &= 4 + 5 + 5 + 2 = 16 \\ \gamma_2 + \gamma_4 + \gamma_6 + \gamma_3 &= 2 + 4 + 5 + 4 = 15\end{aligned}$$

The computational complexity of the BTSP is known to be NP-hard. Implying there is no algorithm for a solution in polynomial time. A brute-force approach would imply that we can run an algorithm in  $O((N-1)!)$  time.

## 2.2 Quantum Computing Basics

### 2.2.1 Frequently Used Notation

Single qubit states:

$$|0\rangle = \begin{bmatrix} 1 \\ 0 \end{bmatrix}, |1\rangle = \begin{bmatrix} 0 \\ 1 \end{bmatrix}$$

2-qubit states:

$$|00\rangle = |0\rangle \otimes |0\rangle = \begin{bmatrix} 1 \\ 0 \\ 0 \\ 0 \end{bmatrix}, |01\rangle = |0\rangle \otimes |1\rangle = \begin{bmatrix} 0 \\ 1 \\ 0 \\ 0 \end{bmatrix}$$

$$|10\rangle = |1\rangle \otimes |0\rangle = \begin{bmatrix} 0 \\ 0 \\ 1 \\ 0 \end{bmatrix}, |11\rangle = |1\rangle \otimes |1\rangle = \begin{bmatrix} 0 \\ 0 \\ 0 \\ 1 \end{bmatrix}$$

Multiple qubit states are achieved through tensor products of single states. An  $n$ -qubit system will span a Hilbert space  $N = 2^n$ . We can see with the 2-qubit system we have 4 states. Quantum logic gates serve as the quantum computing counterparts to classical logic gates, performing operations on qubits instead of classical bits. Represented as unitary matrices, these operations must be reversible. Below we can see the Hadamard gate and the result of its application to single qubit states, frequently employed to establish a uniform superposition.

$$H = \frac{1}{\sqrt{2}} \begin{bmatrix} 1 & 1 \\ 1 & -1 \end{bmatrix}$$

$$H|0\rangle = \frac{1}{\sqrt{2}}(|0\rangle + |1\rangle) = |+\rangle$$

$$H|1\rangle = \frac{1}{\sqrt{2}}(|0\rangle - |1\rangle) = |-\rangle$$

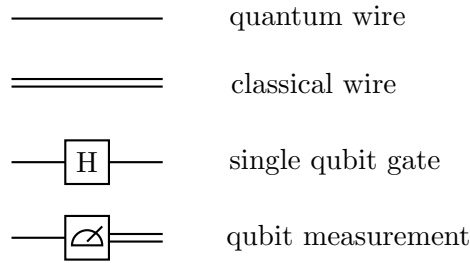


We can refer to equation 1 to see the result of Hadamard on an  $n$ -qubit system. Let's have a look at the Hadamard gate applied to  $|00\rangle$ :

$$\begin{aligned}
 H^{\otimes 2}|00\rangle &= H|0\rangle H|0\rangle \\
 &= |+\rangle|+\rangle \\
 &= \left(\frac{1}{\sqrt{2}}(|0\rangle + |1\rangle)\right)\left(\frac{1}{\sqrt{2}}(|0\rangle + |1\rangle)\right) \\
 &= \frac{1}{2}(|0\rangle + |1\rangle)(|0\rangle + |1\rangle) \\
 &= \frac{1}{2}(|00\rangle + |01\rangle + |10\rangle + |11\rangle)
 \end{aligned}$$

Here we can see why Dirac notation is powerful as it simplifies tensor products and removes need for matrix representation.

### 2.2.2 Quantum Circuit Components



### 2.2.3 Controlled Gates and Phase Kickback

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1. We will describe the use of control gates.
2. We will describe the emergence of phase kickback with the use of hadamard gates discussed in the previous section. This will be important to know going into the next section.

## 2.3 Quantum Phase Estimation

The phase estimation algorithm initially proposed by Alexey Kitaev [6] plays an important role as a subroutine for the more widely known factoring algorithm by Peter Shor [1]. We first must briefly discuss the Quantum Fourier Transform (QFT) as it is key to understanding phase estimation [5]. Given a computational basis state  $|x\rangle$ , applying the QFT ( $F_N$ ) results in:

$$F_N|x\rangle = \frac{1}{\sqrt{N}} \sum_{k=0}^{N-1} e^{2\pi i x k N^{-1}} |k\rangle$$

Let's represent this in binary notation and decompose it into a tensor product. We can represent  $|x\rangle$  as a string of bits, and the QFT as a tensor product of single qubit basis states:

$$|x\rangle = |x_1 x_2 \dots x_n\rangle = |x_1\rangle \otimes |x_2\rangle \otimes \dots \otimes |x_n\rangle$$

$$\begin{aligned}
 F_N|x\rangle &= \frac{1}{\sqrt{2^n}} \bigotimes_{j=1}^n (|0\rangle + e^{2\pi i x 2^{-j}} |1\rangle) \\
 &= \frac{1}{\sqrt{2^n}} ((|0\rangle + \omega_1 |1\rangle) \otimes (|0\rangle + \omega_2 |1\rangle) \otimes \dots \otimes (|0\rangle + \omega_n |1\rangle)) \\
 \omega_1 &= e^{2\pi i x 2^{-1}} = e^{2\pi i (0.x_n)} \\
 \omega_2 &= e^{2\pi i x 2^{-2}} = e^{2\pi i (0.x_{n-1} x_n)} \\
 &\dots \\
 \omega_n &= e^{2\pi i x 2^{-n}} = e^{2\pi i (0.x_1 \dots x_n)}
 \end{aligned} \tag{2.1}$$

An important characteristic of the  $w_j$  is the bit shift occurring in the exponent. If we look at  $w_1$ , the exponent has a factor  $x2^{-1}$ , which is equivalent to one right bit shift:  $x_1 \dots x_{n-1}.x_n$ . Integer multiples of the exponent would imply full rotations returning to the same point thus we can ignore all the values on the left of the decimal and what remains is  $0.x_n$ .

Let's discuss the phase estimation problem. Given an eigenstate  $|\lambda\rangle$  of a unitary operator  $U$ , we want to calculate a good approximation for  $\phi \in [0, 1)$  satisfying:

$$U|\lambda\rangle = e^{2\pi i \phi} |\lambda\rangle \tag{2.2}$$

The phase estimation algorithm uses two registers of qubits. The first one will be a set of  $n$  control qubits that determine the precision of our approximation. The second register will be a set of  $m$  qubits initialized to an eigenstate  $|\lambda\rangle$ .

Let's walk through the quantum circuit in FIG. 2.2, to understand the inner workings of this algorithm. Our initialized state is  $|0^{\otimes n}\lambda\rangle$ . From here we perform the same operation we find in equation 1, where all the qubits in the first register are set to a uniform superposition on all states  $2^n$ . The next portion of the algorithm involves applying controlled gates based on the unitary operator  $U$ . The function of these  $CU$  gates is to apply the operator  $U$  on  $|\lambda\rangle$  if the control qubit is in the state  $|1\rangle$ . We can have a look at the effect on the  $n^{\text{th}}$  qubit, after it has been prepared in a superposition by the Hadamard gate:

$$\frac{1}{\sqrt{2}}(|0\rangle + |1\rangle) \otimes |\lambda\rangle = |0\lambda\rangle + |1\lambda\rangle$$

Applying  $CU$  and factoring out the eigenstate:

$$\begin{aligned}
 CU \frac{1}{\sqrt{2}}(|0\lambda\rangle + |1\lambda\rangle) &= \frac{1}{\sqrt{2}}(CU|0\lambda\rangle + CU|1\lambda\rangle) \\
 &= \frac{1}{\sqrt{2}}(|0\lambda\rangle + e^{2\pi i \phi} |1\lambda\rangle) \\
 &= \frac{1}{\sqrt{2}}(|0\rangle + e^{2\pi i \phi} |1\rangle) \otimes |\lambda\rangle
 \end{aligned}$$

We can see the eigenstate after the  $CU$  operation is left unchanged. the phase has been encoded into the control qubit instead, a result is due to the phase kickback. Thus, we can

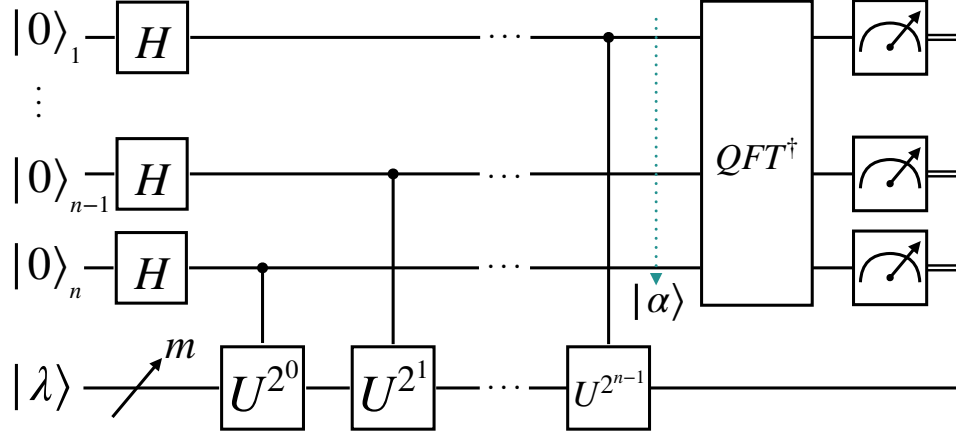


Figure 2.2: A quantum circuit representation of the phase estimation algorithm. Given,  $U|\lambda\rangle = e^{2\pi i\phi}|\lambda\rangle$ , this algorithm allows us to generate an approximation for  $\phi \in [0, 1)$ . The circuit consists of two registers of qubits, the first  $n$ -qubits are initialized to  $|0\rangle$  and contribute to the precision of the  $\phi$  value obtained. The second register of  $m$ -qubits is initialized to the eigenstate of  $U$ . The Hadamard gates,  $H$ , are used to create a uniform superposition in the first register. The control gates based on  $U$  are responsible for encoding phase to the qubits in the first register. Finally, a  $QFT^\dagger$  is performed on the first register to extract the encoded phase. Each subsequent qubit in the first register would require double the control gates. Thus, with a large  $n$  we obtain a more precise value for  $\phi$ , but also exponentially increase our computation time.

reuse our eigenstate for the next qubit. Consecutive qubits have double the amount of  $CU$  operators as the previous, thus squaring the eigenvalue each time:

$$\begin{aligned} \text{qubit}_{n-1} &: |0\rangle + e^{2\pi i\phi^2}|1\rangle \\ &\dots \\ \text{qubit}_1 &: |0\rangle + e^{2\pi i\phi^{2^n}}|1\rangle \end{aligned}$$

We know that the value of  $\phi < 1$ . We can represent this in binary notation in the form  $0.\phi_1\phi_2\dots\phi_n$ :

$$\phi = \sum_{j=1}^n \phi_j 2^{-j}$$

If we have another look at the control qubits using binary notation for  $\phi$  instead, we can see the result of the multiple  $CU$  operations simply results in right bit shifts:

$$\begin{aligned} \text{qubit}_n &: |0\rangle + e^{2\pi i(0.\phi_1\phi_2\dots\phi_n)}|1\rangle \\ \text{qubit}_{n-1} &: |0\rangle + e^{2\pi i(0.\phi_2\dots\phi_n)}|1\rangle \\ &\dots \\ \text{qubit}_1 &: |0\rangle + e^{2\pi i(0.\phi_n)}|1\rangle \end{aligned}$$

If we look at the form of first register after all the  $CU$  operations in the state  $|\alpha\rangle$ , it will resemble the result of performing the QFT we saw in equation 5. Where our  $\omega_j$  are:

$$\begin{aligned}
 \omega_1 &= e^{2\pi i x 2^{-1}} = e^{2\pi i (0.\phi_n)} \\
 \omega_2 &= e^{2\pi i x 2^{-2}} = e^{2\pi i (0.\phi_{n-1}\phi_n)} \\
 &\dots \\
 \omega_n &= e^{2\pi i x 2^{-n}} = e^{2\pi i (0.\phi_1 \dots \phi_n)}
 \end{aligned}$$

simply performing the inverse QFT will give us  $|\phi\rangle = |\phi_1\phi_2\dots\phi_n\rangle$ . We can immediately see the approximation is limited by the number of qubits in the first register. A simple strategy would be to increase the number of qubits; however this would also increase computational cost as we double our use of  $CU$  gates for each additional qubit.

## Chapter 3

# Algorithm for the Constraint Problem

The constrained version of the BTSP asks whether there exists a Hamiltonian cycle in which the weight of every edge is less than a specified threshold  $\alpha$ . In such a cycle, if we denote the weight of any edge as  $\gamma_i$ , then it must satisfy the condition:

$$\gamma_i < \alpha$$

We will construct the algorithm in the following steps:

1. Normalize edge weights so no single hamiltonian cycle is greater than or equal to 1.
2. Construct a unitary operator that holds information regarding the hamiltonian cycles as phases in the diagonal.
3. Set all edgeweights  $> \alpha$  to zero and construct a secondary unitary operator similiar to step 2.
4. Create controlled gates using the unitary operators constructed in Step 2 and 3.
5. Identify all the eigenstates of the unitary operators that map to the phases associated with the hamiltonian cycles.
6. Perform phase estimation twice with an eigenstate using the control gates to evaluate the hamiltonian cycle before and after the edgeweights  $> \alpha$  are set to zero.
7. Compare the two phases achieved. If they are equal, the corresponding hamiltonian cycle is a solution that satisfies the constraint.

### 3.1 Normalize Edge Weights

A hamiltonian cycle of a complete graph with  $N$  nodes requires  $N$  edge-weights to complete the cycle. A single graph consists a total of  $N(N - 1)$  edgeweights in a directed graph. we can choose the largest  $N$  and use these to normalize the edge weights. Let  $w$  describe our edge-weights and will be a list of  $m = N(N - 1)$  elements:

$$w = \{w_1, w_2, \dots, w_m\}$$

Sort  $w$  in descending order to obtain:

$$w' = w'_1, w'_2, \dots, w'_m$$

Where:  $w'_1 \geq w'_2 \geq \dots \geq w'_m$

The sum  $S$  of the largest  $N$  items in  $w$  can be described as:

$$S = \sum_{i=1}^N w'_i$$

We can now perform the normalization. Let  $\tilde{w}$  describe our normalized edge-weights:

$$\tilde{w} = (S + \epsilon)^{-1} w$$

Where:  $\epsilon > 0$ . The purpose of  $\epsilon$  is to make sure if any normalized hamiltonian cycle is exactly equal to  $S$  then we do not have a zero phase.

## 3.2 Unitary Operator for Hamiltonian Cycles

We start by constructing diagonal matrices  $U_j$ , one for each node in a complete graph and describe the matrix elements:

$$[U_j]_{kk} = e^{2\pi i \gamma_{jk}(1-\delta_{jk})}$$

Where:  $1 \leq j, k \leq N$   $N$  denotes the total number of nodes.  $\gamma_{jk}$  represents the edgeweight connecting node  $j \rightarrow k$

Then we construct  $U$ , a tensor product of all the diagonal matrices:

$$U = \bigotimes_j^N U_j$$

$U$  will be a  $N^N \times N^N$  matrix with only the diagonal elements populated. Because the diagonal elements will entirely consist of phases  $[U]_{kk} = e^{i\alpha_{kk}}$ . We can confirm the unitary operator condition is satisfied:  $U^\dagger U = \mathbb{1}$ .

## 3.3 Identifying The Eigenstates

**section not complete.**

1. Here I will discuss how we determine the eigenstates associated with the hamiltonian cycles.

## Chapter 4

# Constraint Solution for a 4-City Problem

### 4.1 Algorithm Construction

for our constraint test, we simply need a tensor product of 4 matrices:

$$U = U_1 \otimes U_2 \otimes U_3 \otimes U_4$$

lets have a closer look at the matrix  $U_1$

$$\begin{bmatrix} 1 & 0 & 0 & 0 \\ 0 & e^{i\gamma_{12}} & 0 & 0 \\ 0 & 0 & e^{i\gamma_{13}} & 0 \\ 0 & 0 & 0 & e^{i\gamma_{14}} \end{bmatrix}$$

section not complete.

1. In this section I will work through the steps for algorithm construction mentioned in the previous chapter for the 4-city problem.
2. a circuit figure will be included as well.

### 4.2 Results: Simulations with Qiskit

section not complete.

**progress report:** The algorithm has been constructed and tested with simulators in Qiskit for the 4-city problem. The full solution has been explored but given time constrained may not be solved and will be briefly discussed in the following section. The simulator does generate graphs we intend to include in this section. the presentation style of the graphs is still being explored. A constraint problem simulation with 5-cities might also be explored, again if time permitting. Otherwise things are fairly on track.

next steps:

1. complete the sections presented in this thesis, particularly regarding theory and algorithm construction section

2. complete this chapter of the constraint solution with circuit figures and graphs from Qiskit Simulation
3. complete other sections (discussion, intro, abstract...) including appendix regarding the code used in python.
4. time permitting: construct and present 5-city simulation as well



## Chapter 5

# Discussion

1. this section will dicuss the advantages and drawbacks of the algorithm proposed.

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## Appendix A

# Code for the 4-city constraint problem

1. this section will include any code used to produce graphs in the simulated section.