## Introduction

This network contains a code in pytorch for training few basic semantic segmentation networks. It includes an implementation of training/evaluation on the given subsample of cityscapes data, with DICE and IoU losses. It contains the code for UNet, ENet, ESnet and DeepLabV3 architectures. Furthermore simple visualization on prediction of these networks looks like when we prioritze differently CrossEntropy and DICE loss (a sweep of  $\beta \in [0,1]$  for loss  $\beta \cdot C \, rossEntropy \, (Y_{true}, Y_{pred}) + (1-\beta) \cdot D \, ICE \, (Y_{true}, Y_{pred})$ ). The input to the network had to be rescaled because of the memory and GPU hours limitations from (256, 256,3) to (128,128, 3).

# Install extra dependencies

```
!pip install -q torchinfo accelerate tqdm

WARNING: Running pip as the 'root' user can result in broken permissions and conflicting behaviour with the system package manager. It is recommended to use a virtual environment instead: https://pip.pypa.io/warnings/venv
```

# **Imports**

```
import os
import pandas as pd # data processing, CSV file I/O (e.g. pd.read csv)
import numpy as np # linear algebra
# pytorch dependency
import torch
import torchvision
import torch.nn as nn
import torch.nn.functional as F
import torch.optim as optim
from torch import Tensor
# HuggingFace accelerate library
from accelerate import Accelerator # (easy support for multiple GPU's,
TPU, floating point 16s, which makes training much faster)
# displaying the pytorch architecture (makes prototyping the network
easier, as it shows shapes)
from torchinfo import summary
# plotting the results
import matplotlib.pyplot as plt
```

```
import seaborn as sns

# creatation and transformations for the dataset
from torch.utils.data import Dataset
from torch.utils.data import DataLoader
import torchvision.transforms as transforms

from PIL import Image
from collections import defaultdict
from IPython.display import clear_output

# show nice progress bar
from tqdm.auto import tqdm

from time import time
```

# Config

```
class CONFIG:
    # use the 16 bit floating point arithmetic (should speeds up
training/inference)
    USE MIXED PRECISION = "fp16" # other values possible, "fp16" or
None
    # set to true to read the dataset from kaggle, false when doing
locally
    USE KAGGLE = True
    # downscaling the images : to make the inference on kaggle faster
and keep within reason on GPU there,
                               I set it to 2 (so the image is scaled
from (256,256) to (128, 128)), None
                               keeps the original shape
    DOWNSCALE = 2
    # Imagenet channelwise mean
    MEAN = [0.485, 0.456, 0.406]
    # imagenet, channelwise standard deviation
    STD = [0.229, 0.224, 0.225]
    # epsilon for DICE, IoU losses (now 1e-6, however in some papers
set to 1)
    EXTRA LOSS EPS = 1e-6
    # style of plots, I find darkgrid nice for regular plots
    SNS STYLE = "darkgrid"
```

```
BATCH_SIZE = 8

SINGLE_NETWORK_TRAINING_EPOCHS = 15

CE_VS_DICE_EVAL_EPOCHS = 15

DELTA_BETA = 0.2

cfg = CONFIG()
```

## get filepaths

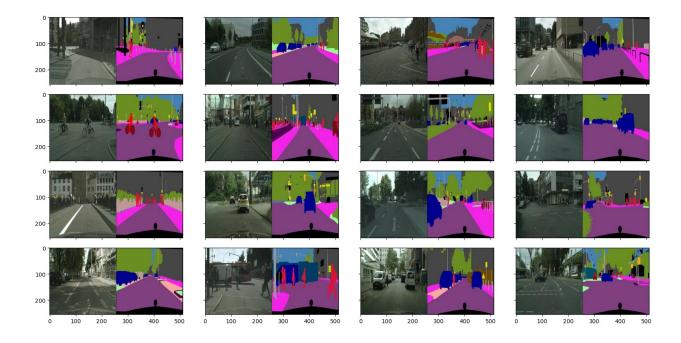
```
# if on kaggle, use the kaggle datapath
if cfg.USE KAGGLE:
    datapath = os.path.join("/kaggle", "input", "cityscapes-image-
pairs", "cityscapes_data")
# otherwise locally
else:
    datapath = os.path.join("dataset", "cityscapes data")
# setting up the datapaths
train_datapath = os.path.join(datapath, "train")
val_datapath = os.path.join(datapath, "val")
train cs datapath = os.path.join(datapath, "cityscapes data", "train")
val cs datapath = os.path.join(datapath, "cityscapes data", "val")
# list all, full datapaths for training and validation images and save
them in these two variables
training images paths = [os.path.join(train datapath, f) for f in
os.listdir(train datapath)]
validation images paths = [os.path.join(val datapath, f) for f in
os.listdir(val datapath)]
# sanity check, how many images
print(f"size of training : {len(training images paths)}")
print(f"size of cityscapes training :
{len(os.listdir(train cs datapath))}")
print(f"size of validation : {len(validation images paths)}")
print(f"size of cityscapes validation :
{len(os.listdir(val cs datapath))}")
global step = 0
size of training: 2975
size of cityscapes training: 2975
size of validation : 500
size of cityscapes validation: 500
```

```
# Create a HuggingFace Accelerate accelerator. This allows using
multiple GPUs, TPUs or
# mixed precision (like brain-float16 or 16bit floating points) which
should make training/inference
# faster
if cfg.USE_MIXED_PRECISION is not None:
    accelerator = Accelerator(mixed_precision=cfg.USE_MIXED_PRECISION)
else:
    accelerator = Accelerator()
```

## Data visualization

Firstly, inspect the dataset to see what the data looks like. Below few random images from the dataset are plotted Unfortunately in this dataset, we are not provided with separate mask data, but data is a single image which is a concatenation of input image and RGB mask representation. This makes it trickier to work with

```
# how many images (total width * height)
width = 4
height = 4
vis batch size = width * height
# get vis batch size unique, random indices
indexes = np.arange(len(training images paths))
indexes = np.random.permutation(indexes)[:vis batch size]
# create the plot
fig, axs = plt.subplots(height, width, sharex=True, sharey=True,
figsize=(16, 8)
for i in range(vis batch size):
    # read the image
    img = torchvision.io.read image(training images paths[indexes[i]])
    # pytorch reads it as (c, h, w), reshape it to (h, w, c) which is
the shape matplotlib wants
    img = img.permute(1, 2, 0)
    # calculate the indexes for plots and set the image data
    y, x = i // width, i % width
    axs[y, x].imshow(img.numpy())
plt.tight layout()
```



# Data preprocessing

#### labels

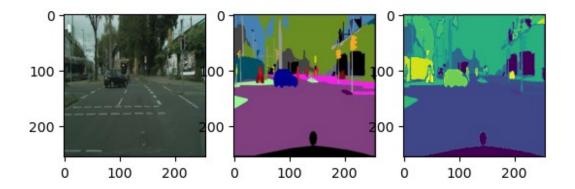
To restore the categories information from a single jpg image, we firstly get the names and categories from the cited cityscapes repository. We get the color representation of each of these classes (I decided to work with categories rather than names), and save it to <code>idx\_to\_color</code>. For each pixel then of an input jpg image, we find a closest color, and put there the index of category which represent this color. this will give us the required (height, width, num\_of\_classes) representation. This is achieved by code below

```
# reference
# link :
https://github.com/mcordts/cityscapesScripts/blob/master/cityscapesscr
ipts/helpers/labels.py
idx_to_name = [ 'unlabeled','ego vehicle','rectification border', 'out
of roi', 'static', 'dynamic','ground', 'road', 'sidewalk', 'parking',
'rail track', 'building', 'wall', 'fence','guard rail',
'bridge','tunnel','pole', 'polegroup', 'traffic light', 'traffic sign','vegetation', 'terrain', 'sky', 'person', 'rider',
'car','truck','bus', 'caravan','trailer', 'train',
'motorcycle','bicycle','license plate']
idx_to_category = ["void", "flat", "construction", "object", "nature",
"sky", "human", "vehicle"]

idx_to_color = [[ 0,  0,  0], [ 0,  0,  0], [  0,  0,  0], [  0,  0,  0],
[ 0,  0,  0],[111, 74,  0],[81,  0, 81],[128, 64,128],[244,
35,232],
```

```
[250, 170, 160], [230, 150, 140], [70, 70, 70],
[102,102,156],[190,153,153],[180,165,180],[150,100,100],[150,120, 90],
[153, 153, 153],
                [153,153,153],[250,170, 30],[220,220, 0],[107,142,
35],[152,251,152],[70,130,180],[220, 20, 60],[255, 0, 0],[0,
0,142],
                [ 0, 0, 70], [ 0, 60, 100], [ 0, 0, 90], [ 0, 0, 110], [
0, 80, 100], [0, 0, 230], [119, 11, 32], [0, 0, 142]]
idx to color np = np.array(idx to color)
name to category = \{0: 0, 1: 0, 2: 0, 3: 0, 4: 0, 5: 0, 6: 0,
7: 1, 8: 1, 9: 1, 10: 1, 11: 2, 12: 2, 13: 2, 14: 2, 15: 2, 16
: 2,
                    17 : 3, 18 : 3, 19 : 3, 20 : 3, 21 : 4, 22 : 4,
23 : 5, 24 : 6, 25 : 6, 26 : 7, 27 : 7, 28 : 7, 29 : 7, 30 : 7, 31 :
7, 32: 7, 33 : 7, 34 : 7}
from typing import Tuple
# vectorize the operation of getting the name to category for numpy
(just a lookup in name to category dictionary)
name to category mapping = lambda x: name to category[x]
vectorized cat mapping = np.vectorize(name to category mapping)
# vectorize the operation of mapping the name to color for numpy (just
a lookup in idx to color dictionary)
name to col mapping = lambda x: idx to color[x]
vectorized col mapping = np.vectorize(name to col mapping)
def preprocess image(path : str, sparse mapping=True,
downscale factor=None) -> Tuple[np.ndarray, np.ndarray, np.ndarray]:
        Read the .jpeg image from *path*. Return the input image (256)
x \ 256 \ x \ 3), mask (256 x \ 256 \ x \ 3) read from the ipeg
        and conversion to categories or names (if sparse mapping is
true) representation (256 \times 256 \times (|categories| or |names|)
    # Read the image from path and dowscale if downscale factor is not
None.
    img = Image.open(path)
    width, height = img.size
    if downscale factor:
        width, height = width // downscale factor,
height//downscale factor
        img = img.resize(( width, height ))
    # then split the image into two images (in the middle of width) :
```

```
input image and color mask (each represented by 3 channels)
    img = np.asarray(img)
    raw, mask = img[:, :width//2, :], img[:, width//2:, :]
    height, width, channels = mask.shape
    # compute then the sum of squared distances for each pixel to the
colors (L2 between the color and pixel data) :
    # the value which will be the minimal is the category name we will
use for that pixel, and we will get it using argmin
    distances = np.sum((mask.reshape(-1, channels)[:, np.newaxis, :] -
idx to color np)**2, axis=2)
    classes = np.argmin(distances, axis=1).reshape(height, width)
    # if we want to operate on names, map the categories to names
    if sparse mapping:
        classes = vectorized cat mapping(classes)
    return raw, mask, classes
x, mask raw, classes =
preprocess image(training images paths[indexes[i]],
sparse mapping=False, downscale factor=None)
# sanity checks and print the data
print("size of input : ", x.shape)
print("size of mask raw : ", mask_raw.shape)
print("size of classes : ", classes.shape)
plt.subplot(1, 3, 1)
plt.imshow(x)
plt.subplot(1, 3, 2)
plt.imshow(mask raw)
plt.subplot(1, 3, 3)
plt.imshow(classes)
plt.show()
size of input : (256, 256, 3)
size of mask raw : (256, 256, 3)
size of classes : (256, 256)
```



# Load data and Pytorch dataset

In the section below, we will create and check the Pytorch dataset for this images

### Load dataset

There aren't that many images for training/validation and the size of them is pretty small, therefore to speed up the computation, we can just load them into RAM. This will be achieved by preprocessing as above all paths, and appending the input images and proper masks to python arrays The images themselves are scaled to [0, 1] and converted to PyTorch Tensors (with the (c, h, w) convention)

```
train images to use = -1
# for kaggle
downscale factor=cfg.DOWNSCALE
X train, Y train = [], []
X \text{ val}, Y \text{ val} = [], []
for path in tqdm(training images paths[:]):
    X, _, Y = preprocess image(path,
downscale factor=downscale factor)
    X train.append(torch.Tensor(X / 255.).permute(2, 0, 1))
    Y train.append(torch.Tensor(Y))
for path in tqdm(validation_images_paths):
    X, , Y = preprocess image(path,
downscale_factor=downscale factor)
    X_{val.append(torch.Tensor(X / 255.).permute(2, 0, 1))}
    Y val.append(torch.Tensor(Y))
{"model id": "0a37dbbf8f314ee692cadfacc047504f", "version major": 2, "vers
ion minor":0}
```

```
{"model id": "56e9836c2cfa4493ab1dc70ecf4c1c12", "version major": 2, "vers
ion minor":0}
print(f"size of X train : {len(X train)} ; Y train {len(Y train)}")
print(f"size of X val : {len(X val)} ; Y val {len(Y val)}")
size of X train : 2975 ; Y train 2975
size of X val : 500 ; Y val 500
total size in B = 0
for i in range(len(X train)):
    total size in B += X train[i].element size() *
X train[i].nelement()
    total size in B += Y train[i].element size() *
Y train[i].nelement()
for i in range(len(X val)):
    total size in B += X val[i].element size() * X val[i].nelement()
    total_size_in_B += Y_val[i].element_size() * Y_val[i].nelement()
print(f"The total size of data in RAM is {round(total size in B / 1000
/ 1000 / 1000, 3)} GB")
The total size of data in RAM is 0.911 GB
```

## Pytorch Dataset

Create a PyTorch dataset from which we will get the data. It's very simple, just get the image from the python arrays, and apply some preprocessing. Here only the normalization with  $\mu$  and  $\sigma$  calculated from ImageNet is applied. It could be done in loop above, however I used transforms just to keep code somehow more maintanable (and the resulting speed benefit is very small)

```
# Pytorch Dataset
class CityScapesDataset(Dataset):
    def __init__(self, X, Y, transform=None, target_transform=None):
        self.X = X
        self.Y = Y
        self.transform = transform
        self.target_transform = target_transform

def __len__(self):
        return len(self.X)

def __getitem__(self, idx):
        x, y = self.X[idx], self.Y[idx]

if self.transform:
        x = self.transform(x)
```

```
if self.target_transform:
    y = self.target_transform(y)
    return x , y

# just normalize the data
preprocess = transforms.Compose([
    transforms.Normalize(mean=cfg.MEAN, std=cfg.STD),
])

# create the Datasets
train_ds = CityScapesDataset(X_train, Y_train, transform=preprocess)
val_ds = CityScapesDataset(X_val, Y_val, transform=preprocess)
# create the dataloaders
train_dataloader = DataLoader(train_ds, batch_size=cfg.BATCH_SIZE, shuffle=True)
val_dataloader = DataLoader(val_ds, batch_size=cfg.BATCH_SIZE, shuffle=True)
```

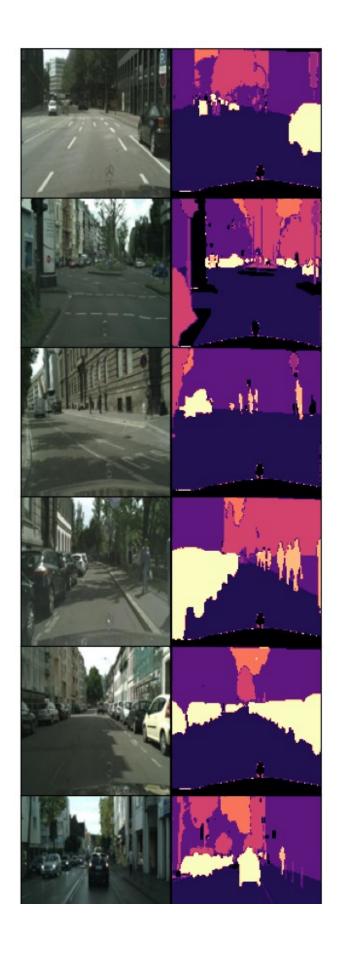
#### Test dataset

Just a sanity check whether the data is loaded correctly. Some artifacts are visible, like in image below. I believe this is fine, given it's only a notebook for fun and playing with some basic dataset (this wouldn't be like this if we have a mask data)

```
fig, axes = plt.subplots(cfg.BATCH_SIZE, 2, figsize=(4,
2.*cfg.BATCH_SIZE), squeeze=True)
fig.subplots_adjust(hspace=0.0, wspace=0.0)

for i in range(cfg.BATCH_SIZE):
    img, mask = X_train[i], Y_train[i]
    #print(img.shape, mask.shape)
    axes[i, 0].imshow(img.permute(1,2, 0))
    axes[i, 0].set_xticks([])
    axes[i, 0].set_yticks([])

axes[i, 1].imshow(mask, cmap='magma')
    axes[i, 1].set_xticks([])
    axes[i, 1].set_yticks([])
```



# Save a batch from test dataloader for later evaluation

```
eval batch data = next(iter(val dataloader))
# the images coming from the dataset are now preprocessed :
# images are normalized using means and standard deviations coming
from ImageNet (x' = (x - \mbox{\ mu}) / \mbox{\ std})
# to decode, multiply by standard deviation, and add mean (x = x' * )
std + \mbox{\mbox{}} mu)
def decode image(img : torch.Tensor) -> torch.Tensor:
    return img * torch.Tensor(cfg.STD) + torch.Tensor(cfg.MEAN)
print(eval batch data[0].shape, eval batch data[1].shape)
batch size = eval batch data[0].shape[0]
fig, axes = plt.subplots(batch size, 2, figsize=(4, 2.*batch size),
squeeze=True)
fig.subplots_adjust(hspace=0.0, wspace=0.0)
for i in range(batch size):
    img, mask = eval batch data[0][i], eval batch data[1][i]
    #print(img.shape, mask.shape)
    axes[i, 0].imshow(decode image(img.permute(1,2, 0)))
    axes[i,0].set xticks([])
    axes[i,0].set yticks([])
    axes[i, 1].imshow(mask, cmap='magma')
    axes[i,1].set xticks([])
    axes[i,1].set_yticks([])
torch.Size([8, 3, 128, 128]) torch.Size([8, 128, 128])
```



#### Extra Losses

#### **Brief Description**

Because our task is pixelwise classification, initially we could think that we could just use crossentropy to predict the class of the pixel. This is used, however there exists also many other losses, which are often either used or combined, to get much better results. Other popular metrics include:

• DICE coefficient (or Sørensen–Dice coefficient). This loss is used to compare the pixelwise agreement between the predicted segmentation and mask, and is represented as:

$$DICE(X, X_{truth}) = \frac{2|X \cap X_{truth}|}{|X| + |X_{truth}|}$$

This metrics takes values from 0 (no overlap at all) and 1 (full overlap). This loss his widely adapted to calculate the simillarity of two images [2]. DICE loss has a large benefit in comparison to cross entropy, that it considers the loss information both locally and globally (in comparison to crossentropy which cares only about local). It is consider much better loss for semantic segmenation, especially in inbalanced classes problems.

Intersection over Union (or Jaccard coefficient). Simillarly to DICE, This loss is also used
to compare the pixelwise agreement between the predicted segmenation and mask. It's
quite simillar to DICE in it's formulation, and it's given by

$$IoU(X, X_{truth}) = \frac{|X \cap X_{truth}|}{|X \cup X_{truth}|} = \frac{|X \cap X_{truth}|}{|X| + |X_{truth}| - |X \cap X_{truth}|}$$

This metric is simple, intuitive (devide the Area of Overlap over Area of Union: this achieves a score from 0 (no overlap) to 1 (we perfectly classified everything, and intersection of truth/prediction and union of them are the same)). It provides the same benefits as DICE: it is concerned with both local and global alignment, providing a better value to optimize then cross entropy. Both metrics are used for semantic segmentaion and overall are quite simillar however there is at least one important different I know of: IoU gives more weight to intersection than IoU (because of the factor 2, which I believe means that some small errors are tolerated in DICE).

There exists many more losses which I will explore in other notebooks, many of which are mentioned in really nice survey [2].

#### **Multiclass**

Important consideration of each of this losses is that most often they are only mentioned for binary classes. Most likely however, we will deal with multiclass prediction problem. A way to deal with it, is to one hot encode our predictions, and calculate DICE or IoU for each of the classes. Then calculate the mean of them (or weighted mean) to get the final score.

### great articles / references:

- [1]https://towardsdatascience.com/metrics-to-evaluate-your-semantic-segmentation-model-6bcb99639aa2
- [2]https://arxiv.org/pdf/2006.14822.pdf

- [3]https://medium.com/ai-salon/understanding-dice-loss-for-crisp-boundary-detection-bb30c2e5f62b
- [4]https://stats.stackexchange.com/questions/273537/f1-dice-score-vs-iou/ 276144#276144

```
# dice loss
# awesome implementation for DICE can be found here
https://github.com/milesial/Pytorch-UNet/blob/master/utils/dice score.
def dice coeff(inp : Tensor, tqt : Tensor, eps=cfg.EXTRA LOSS EPS):
    sum dim = (-1, -2, -3)
    # calculation of intersection
    inter = 2 *(inp * tgt).sum(dim=sum dim)
    # calculate the sum of |inp| + |tgt|
    sets_sum = inp.sum(dim=sum_dim) + tgt.sum(dim=sum_dim)
    sets sum = torch.where(sets sum == 0, inter, sets sum)
    # calcaute the dice
    dice = (inter + eps) / (sets sum + eps)
    # average the dice of classwise
    return dice.mean()
def multiclass dice coeff(input: Tensor, target: Tensor, eps: float =
cfg.EXTRA LOSS EPS):
    return dice_coeff(input.flatten(0, 1), target.flatten(0, 1), eps)
def dice loss(input: Tensor, target: Tensor):
    # Dice loss (objective to minimize) between 0 and 1
    return 1 - multiclass dice coeff(input, target)
def IoU coeff(inp : Tensor, tgt : Tensor, eps = 1e-6):
    sum dim = (-1, -2, -3)
    # Intersection term | A ^ B |
    inter = (inp * tgt).sum(dim=sum_dim)
    \# \ sum \ of \ |A| + |B|
    sets sum = inp.sum(dim=sum dim) + tgt.sum(dim=sum dim)
    sets sum = torch.where(sets sum == 0, inter, sets sum)
    \# IoU = |A \land B| / |A \setminus B| = |A \land B| / (|A| + |B| - |A \land B|)
    return (inter + eps) / (sets sum - inter + eps)
def IoU loss(inp : Tensor, tgt : Tensor):
    return 1 - IoU_coeff(inp.flatten(0,1), tgt.flatten(0,1))
```

#### Evaluate the model

```
def evaluate_model(model, val_dataloader, epoch, epochs, criterion,
                   with dice loss=True, with IoU loss=False):
    global epoch to fig
    val loss = 0
    val dice = 0
    val IoU = 0
    with tgdm(val dataloader, desc=f"Epoch {epoch}/{epochs}; val Loss
O") as pbar:
        model.eval()
        examples so far = 0
        for i, batch in enumerate(val dataloader):
            images, true masks = batch[0], batch[1]
            images = images.to(device)
            true masks = true masks.to(device).long()
            # predictions
            masks pred = model(images)
            loss = criterion(masks_pred, true_masks)
            val loss += loss.item() * images.shape[0]
            examples so far += images.shape[0]
            dice = dice_loss(F.softmax(masks_pred, dim=1).float(),
                             F.one_hot(true masks.long(),
number of classes).permute(0, 3, 1, 2).float())
            if with dice loss:
                loss += dice
            val dice += images.shape[0] * (1.-dice.item())
            IoU = IoU loss(F.softmax(masks pred, dim=1).float(),
                           F.one hot(true masks.long(),
number of classes).permute(0, 3, 1, 2).float())
            if with IoU loss:
                loss += IoU
            val IoU += images.shape[0] * (1.-IoU.item())
            pbar.update(1)
            descr = f"Epoch {epoch}/{epochs} ; val Loss
{round(val_loss / examples_so_far ,3)}, val IoU : {round(val_IoU /
examples_so_far ,3)}, val Dice : {round(val_dice /
examples so far ,3)}"
            pbar.set description(descr)
    eval summary = {}
```

```
eval_summary["validation_loss"] = val_loss / examples_so_far
  eval_summary["validation_DICE_coefficient"] = val_dice /
examples_so_far
  eval_summary["validation_IoU_coefficient"] = val_IoU /
examples_so_far
return eval_summary
```

## Training the model

```
def train model(model, device, train dataloader,
                val dataloader, epochs=10, lr=1e-4,
update pb every batch = 1,
                with dice loss=True, with IoU loss=False):
    global global step
    # setup the optimizer, loss, learning rate scheduler
    optimizer = optim.Adam(model.parameters(), lr=lr)
    criterion = nn.CrossEntropyLoss()
    model, optimizer, train dataloader = accelerator.prepare(model,
optimizer, train dataloader)
    returned data = []
    for epoch in range(1, epochs + 1):
        training loss = 0
        training_dice = 0
        training IoU = 0
        with tqdm(train dataloader, desc=f"Epoch {epoch}/{epochs};
training Loss {round(training loss,3)}") as pbar:
            model.train()
            optimizer.zero grad()
            examples so far = 0
            for i,batch in enumerate(train_dataloader):
                optimizer.zero grad()
                images, true_masks = batch[0], batch[1]
                images = images.to(device)
                true masks = true masks.to(device).long()
                masks pred = model(images)
                loss = criterion(masks pred, true masks)
                if with dice loss:
                    dice = dice loss(F.softmax(masks pred,
dim=1).float(),
```

```
F.one hot(true masks.long(),
number of classes).permute(0, 3, 1, 2).float())
                    loss += dice
                    training dice += images.shape[0] * (1.-
dice.item())
                if with IoU loss:
                    IoU = IoU loss(F.softmax(masks pred,
dim=1).float(),
                              F.one hot(true masks.long(),
number of classes).permute(0, 3, 1, 2).float())
                    loss += IoU
                    training IoU += images.shape[0] * (1.-IoU.item())
                # Backward and optimize
                #loss.backward()
                accelerator.backward(loss)
                optimizer.step()
                qlobal step += 1
                training loss += images.shape[0] * loss.item()
                examples so far += images.shape[0]
                pbar.update(1)
                if i % update_pb_every_batch == 0:
                    descr = f"Epoch {epoch}/{epochs} ; training Loss
{round(training loss / examples so far ,3)}"
                    if with dice loss:
                        descr = f"Epoch {epoch}/{epochs} ; training
Loss {round(training_loss / examples_so_far ,3)}, avg DICE :
{round(training dice / examples so far ,3)}"
                    elif with IoU loss:
                        descr = f"Epoch {epoch}/{epochs} ; training
Loss {round(training_loss / examples_so_far ,3)}, avg IoU :
{round(training IoU / examples so far ,3)}"
                    pbar.set description(descr)
        epoch summary = {}
        epoch summary["training loss"] = training loss /
examples_so_far
        if with dice loss:
            epoch summary["training DICE coefficient"] = training dice
/ examples so far
        if with IoU loss:
            epoch summary["training IoU coefficient"] = training IoU /
examples so far
        val summary = evaluate model(model, val dataloader, epoch,
```

## Show the outputs

```
def show inference(batch, predictions):
    batch size = batch[0].shape[0]
    fig, axes = plt.subplots(batch size, 3, figsize=(6,
2.*batch size), squeeze=True, sharey=True, sharex=True)
    fig.subplots adjust(hspace=0.05, wspace=0)
    for i in range(batch size):
        img, mask = batch[0][i], batch[1][i]
        axes[i, 0].imshow(decode image(img.permute(1,2, 0)))
        axes[i,0].set xticks([])
        axes[i,0].set yticks([])
        if i == 0:
            axes[i, 0].set_title("Input Image")
        axes[i, 1].imshow(mask, cmap='magma')
        axes[i,1].set xticks([])
        axes[i,1].set yticks([])
        if i == 0:
            axes[i, 1].set title("True Mask")
        predicted = predictions[i]
        predicted = predicted.permute(1, 2, 0)
        predicted = torch.argmax(predicted, dim=2)
        axes[i, 2].imshow(predicted.cpu(), cmap='magma')
        axes[i, 2].set xticks([])
        axes[i, 2].set yticks([])
        if i == 0:
            axes[i, 2].set title("Predicted Mask")
```

## plot the losses/coefficients

```
def plot_losses_coeffs(training_val_summary):
    if cfg.SNS_STYLE is not None:
        sns.set(style=cfg.SNS_STYLE)
    training = defaultdict(list)
```

```
validation = defaultdict(list)
    m = 0
    for epoch summary in training val summary:
        keys = list(epoch summary.keys())
        training_keys = [k for k in keys if k.startswith("training")]
        for k in training keys:
            training[k[len("training")+1:]].append(epoch summary[k])
        val keys = [k for k in keys if k.startswith("validation")]
        for k in val keys:
            validation[k[len("validation")
+1:]].append(epoch summary[k])
    fig, axes = plt.subplots(len(validation.keys()), 1, figsize=(10,
10), sharex=True)
    for i,k in enumerate(validation.keys()):
        if k in training:
            axes[i].plot(training[k], marker='o', linestyle='--',
label="training", linewidth=3)
        if validation[k][-1] > validation[k][0]:
            best idx = np.argmax(validation[k])
        else:
            best idx = np.argmin(validation[k])
        best = validation[k][best idx]
        axes[i].set_title(f"{k} (validation best : {round(best, 4)})",
fontsize=14, fontweight="bold")
        axes[i].plot(validation[k], label="validation", marker='o',
linestyle='--', linewidth=3)
        axes[i].plot([0, best idx], [best, best], linewidth=2,
linestyle="--", color='black', alpha=0.5)
        m = max(m, len(validation[k]))
        axes[i].legend()
    axes[-1].set xticks(list(range(0, m)))
    axes[-1].set xticklabels(list(range(1, m + 1)))
    sns.set(style="white")
```

#### Show the CE vs DICE eval

## Training / Eval for CE vs DICE comparison

```
def train model CE DICE(model, device, train dataloader,
                val dataloader, Beta=0., epochs=10, lr=1e-4,
update pb every batch = 1):
    global global step
    # setup the optimizer, loss, learning rate scheduler
    optimizer = optim.Adam(model.parameters(), lr=lr)
    cross entropy = nn.CrossEntropyLoss()
    model, optimizer, train dataloader = accelerator.prepare(model,
optimizer, train_dataloader)
    returned data = []
    for epoch in range(1, epochs + 1):
        training loss = 0
        training dice = 0
        training CE = 0
        model.train()
        optimizer.zero grad()
        examples so far = 0
        for i,batch in enumerate(train dataloader):
            optimizer.zero grad()
            images, true_masks = batch[0], batch[1]
            images = images.to(device)
            true masks = true masks.to(device).long()
            masks pred = model(images)
                 = cross entropy(masks pred, true masks)
            dice = dice loss(F.softmax(masks pred, dim=1).float(),
                              F.one hot(true masks.long(),
number of classes).permute(0, 3, 1, 2).float())
            loss = Beta * CE + (1. - Beta) * dice
            training dice += images.shape[0] * (1.-dice.item())
            training CE += images.shape[0] * CE.item()
            # Backward and optimize
            #loss.backward()
            accelerator.backward(loss)
            optimizer.step()
```

```
qlobal step += 1
            training loss += images.shape[0] * loss.item()
            examples so far += images.shape[0]
        epoch summary = \{\}
        epoch summary["training loss"] = training loss /
examples so far
        epoch summary["training CE"] = training CE / examples so far
        epoch summary["training DICE coefficient"] = training dice /
examples so far
        val summary = evaluate model CE DICE(model, val dataloader,
epoch, epochs)
        epoch summary = {**epoch summary, **val summary}
        returned data.append(epoch summary)
    return returned data
def evaluate model CE DICE(model, val dataloader, epoch, epochs):
    global epoch to fig
    val CE = 0
    val dice = 0
    val IoU = 0
    cross entropy = nn.CrossEntropyLoss()
    model.eval()
    examples so far = 0
    for i, batch in enumerate(val dataloader):
        images, true masks = batch[0], batch[1]
        images = images.to(device)
        true masks = true masks.to(device).long()
        # predictions
        masks_pred = model(images)
        loss = cross entropy(masks pred, true masks)
        val CE += loss.item() * images.shape[0]
        examples so far += images.shape[0]
        dice = dice loss(F.softmax(masks pred, dim=1).float(),
                             F.one hot(true masks.long(),
number of classes).permute(0, 3, 1, 2).float())
        val dice += images.shape[0] * (1.-dice.item())
        IoU = IoU loss(F.softmax(masks pred, dim=1).float(),
                           F.one hot(true masks.long(),
number of classes).permute(0, 3, 1, 2).float())
```

```
val_IoU += images.shape[0] * (1.-IoU.item())

eval_summary = {}
  eval_summary["validation_CE"] = val_CE / examples_so_far
  eval_summary["validation_DICE_coefficient"] = val_dice /
examples_so_far
  eval_summary["validation_IoU_coefficient"] = val_IoU /
examples_so_far
  return eval_summary
```

### CE vs DICE comparison plotting/training utils

```
def plot summaries preds(summaries, preds):
    true val = eval batch data[1].permute(1, 2, 0)
    images = [true val] + [*preds]
    batch size = images[0].shape[-1]
    fig, axes = plt.subplots(batch size, len(images), figsize=(18,
2.*batch size), squeeze=True, sharey=True, sharex=True)
    fig.subplots adjust(hspace=0.0, wspace=0)
    for i in range(len(images)):
        for j in range(batch size):
            axes[j, i].imshow(images[i][:,:,j].cpu())
            axes[j,i].set xticks([])
            axes[j,i].set yticks([])
            if i == 0 and j == 0:
                summary = summaries[i]
                axes[0,0].set title(f"True Mask")
            elif i == 1 and j == 0:
                axes[j,i].set title(f"Sum of DICE+CE\
nCE={round(summary['VAL LOSS'],2)},DICE={round(summary['VAL DICE'],2)}
            elif j == 0:
                summary = summaries[i-1]
                axes[j,i].set_title(f"Beta={round(summary['BETA'],
2)}\
nCE={round(summary['VAL CE'],2)},DICE={round(summary['VAL DICE'],2)}")
def get_summaries_preds_for_comparison(delta_Beta, model_ref,
model params, model mixed, summary mixed, epochs=25, lr=3e-4):
    summaries = []
    predictions = []
```

```
device = "cuda"
    d = defaultdict(list)
    for x in summary mixed:
        for k,v in x.items():
            d[k].append(v)
    LOSS, DICE, V LOSS, V DICE, V IOU = d["training loss"][-1],
d["training_DICE_coefficient"][-1], d["validation_loss"][-1],
d["validation_DICE_coefficient"][-1], d["validation_IoU_coefficient"]
[-1]
    summaries.append({"LOSS" : LOSS, "DICE" : DICE, "VAL LOSS" :
V LOSS, "VAL DICE" : V DICE, "VAL IOU" : V IOU})
predictions.append(torch.argmax(model mixed(eval batch data[0].to(devi
ce)), dim=1).permute(1, 2, 0))
    for Beta in np.arange(0, 1. + 1e-9, delta Beta):
        start = time()
        model = model ref(**model params)
        model = model.to(device)
        summary = train model CE DICE(model, device, train dataloader,
val dataloader, Beta=Beta,
                                                lr=lr, epochs=epochs,
update pb every batch=10)
        d = defaultdict(list)
        for x in summary:
            for k,v in x.items():
                d[k].append(v)
        CE, DICE, V CE, V DICE, V IOU = d["training CE"][-1],
d["training DICE coefficient"][-1], d["validation CE"][-1],
d["validation DICE coefficient"][-1], d["validation IoU coefficient"]
[-1]
        summaries.append({"BETA" : Beta, "CE" : CE, "DICE" : DICE,
"VAL CE" : V CE, "VAL DICE" : V DICE, "VAL IOU" : V IOU})
predictions.append(torch.argmax(model(eval batch data[0].to(device)),
dim=1).permute(1, 2, 0))
        print(f"[{int(time() - start)}s] Model with Beta={Beta}
finished with training CE : {CE}, training DICE : {DICE}, validation
CE : {V CE}, Validation DICE : {V DICE}, Validation IOU : {V IOU}")
    return summaries, predictions
```

## Unet

## Description

Unet is one of the most popular architectures for semantic segmentation, which won multiple semantic segmentation competitions for medical images [1]. This architecture was introduced in the paper "U-Net: Convolutional Networks for Biomedical Image Segmentation". The most likely reason why Unet is popular is that it works well and the architecture itself is easy to code and intuitive. It is a fully convolutional neural network (the main computational operation is 2D convolution, with no Dense layers) and the original architecture is represented in figure a). The architecture is nicely symmetric and intuitive. We start with an input image and apply convolutions twice. we then save this output, and downscale (using max-pooling layer), which

results in an image of size  $\left(\frac{h}{2}, \frac{w}{2}\right)$ . We continue to follow this pattern until we reach a bottleneck

layer (bottom-middle layer). After applying 2 convolutions, we apply deconvolution (which will upscale the image, from  $(h,w) \rightarrow (2\,h,2\,w)$ ). There a crucial step is done: the outputs from applying the convolutions (left-side) are concatenated with the deconvolutions (the upsampling). This is done in order to localize high resolution, and convolutions can learn to assemble a more precise output based on this information [1]. An important property of this architecture is that upsampling layers has also many channels, which should allow propagating information context properly.

#### Some other notes:

- Unet paper also introduces a data augmentation for semantic segmentation. Data augmentation is a very popular technique to make the network generalize better, by perturbing or modifying the dataset. It also allows training networks with smaller datasets (as we constantly perturb the data). They achieve that by introducing elastic deformations.
- An important observation in the architecture that there is that the output image is a different size than the input image. Depending on the application, it might or might not matter. [TODO] Making the output the same as the input has some fairly hidden drawbacks, find a paper. However in order to simplify the implementation, and because at the end we are most likely interested in the pixel-wise classification (output size = input size). This will be achieved by adding paddings to the convolutions and making sure that max pool layers will not be applied to odd sizes.

[1] https://arxiv.org/abs/1505.04597

### Architecture

```
class ConvBlock(nn.Module):
    """apply twice convolution followed by batch normalization and
relu. Preserves the width and height of input"""

def __init__(self, in_channels, out_channels, mid_channels=None):
    super().__init__()
    if not mid_channels:
```

```
mid channels = out channels
        self.cn1 = nn.Conv2d(in channels, mid channels, kernel size=3,
padding=1, bias=False)
        self.bn1 = nn.BatchNorm2d(mid channels)
        self.activ1 = nn.ReLU(inplace=True)
        self.cn2 = nn.Conv2d(mid channels, out channels,
kernel size=3, padding=1, bias=False)
        self.bn2 = nn.BatchNorm2d(out channels)
        self.activ2 = nn.ReLU(inplace=True)
    def forward(self, x):
        x = self.cn1(x)
        x = self.bn1(x)
        x = self.activ1(x)
        x = self.cn2(x)
        x = self.bn2(x)
        return self.activ2(x)
class DownScale(nn.Module):
    """Downscaling with maxpool then ConvBlock, transforming an input
with (h, w, in_channels) to (h/2, w/2, out channels)"""
    def __init__(self, in_channels, out_channels):
        super().__init__()
        self.pool = nn.MaxPool2d(2)
        self.block = ConvBlock(in channels, out channels)
    def forward(self, x):
        x = self.pool(x)
        x = self.block(x)
        return x
class UpScale(nn.Module):
    """apply upscaling and then convolution block transforming an
input with (h,w,in channels) to (2h, 2w, out channels).
       Forward function also simplifies Unet propagation by taking two
inputs : first one from constantly propagating (from upscaling)
       and the second one, which is the output from applying Downscale
(first input is upscaled, then concatenated with second)"""
    def init (self, in channels, out channels, bilinear=True):
        super().__init ()
        # if bilinear, use the normal convolutions to reduce the
number of channels
        if bilinear:
            self.up = nn.Upsample(scale factor=2, mode="bilinear",
```

```
align corners=True)
            self.conv = ConvBlock(in channels, out channels,
in channels // 2)
        else:
            self.up = nn.ConvTranspose2d(in channels, in channels //
2, kernel size=2, stride=2)
            self.conv = ConvBlock(in channels, out channels)
    def forward(self, x1, x2):
        x1 = self.up(x1)
        # input is (batch, channel, height, width)
        diffY = x2.size()[2] - x1.size()[2]
        diffX = x2.size()[3] - x1.size()[3]
        x1 = F.pad(x1, [diffX // 2, diffX - diffX // 2,
                        diffY // 2, diffY - diffY // 2])
        x = torch.cat([x2, x1], dim=1)
        return self.conv(x)
class Unet(nn.Module):
    def init (self, n channels, n classes, start=32,
bilinear=False):
        super(Unet, self). init ()
        self.n_channels = n_channels
        self.n classes = n classes
        self.bilinear = bilinear
        self.inc = ConvBlock(n channels, start)
        self.down1 = DownScale(start, 2*start)
        self.down2 = DownScale(2*start, 4*start)
        self.down3 = DownScale(4*start, 8*start)
        factor = 2 if bilinear else 1
        self.down4 = DownScale(8*start, 16*start // factor)
        self.up1 = UpScale(16*start, 8*start // factor, bilinear)
        self.up2 = UpScale(8*start, 4*start // factor, bilinear)
        self.up3 = UpScale(4*start, 2*start // factor, bilinear)
        self.up4 = UpScale(2*start, start, bilinear)
        self.outc = nn.Conv2d(start, n classes, kernel size=1)
    def forward(self, x):
        x1 = self.inc(x)
        x2 = self.down1(x1)
        x3 = self.down2(x2)
        x4 = self.down3(x3)
```

```
x5 = self.down4(x4)
        x = self.up1(x5, x4)
        x = self.up2(x, x3)
        x = self.up3(x, x2)
        x = self.up4(x, x1)
        logits = self.outc(x)
        return logits
number of classes = len(set(name to category.values()))
summary(Unet(3, number of classes), input data=eval batch data[0])
Layer (type:depth-idx)
                                           Output Shape
Param #
                                           [8, 8, 128, 128]
Unet
                                           [8, 32, 128, 128]
—ConvBlock: 1-1
                                                                       - -
                                           [8, 32, 128, 128]
     └─Conv2d: 2-1
                                                                      864
                                           [8, 32, 128, 128]
     └─BatchNorm2d: 2-2
                                                                      64
     └ReLU: 2-3
                                           [8, 32, 128, 128]
     └─Conv2d: 2-4
                                           [8, 32, 128, 128]
9,216
     └─BatchNorm2d: 2-5
                                           [8, 32, 128, 128]
                                                                      64
     └ReLU: 2-6
                                           [8, 32, 128, 128]
                                           [8, 64, 64, 64]
 -DownScale: 1-2
     └─MaxPool2d: 2-7
                                           [8, 32, 64, 64]
                                                                       - -
     └ConvBlock: 2-8
                                           [8, 64, 64, 64]
           └─Conv2d: 3-1
                                           [8, 64, 64, 64]
18,432
                                           [8, 64, 64, 64]
           └─BatchNorm2d: 3-2
                                                                      128
           └ReLU: 3-3
                                           [8, 64, 64, 64]
                                                                       - -
                                           [8, 64, 64, 64]
           └─Conv2d: 3-4
36,864
           └─BatchNorm2d: 3-5
                                           [8, 64, 64, 64]
                                                                      128
           └ReLU: 3-6
                                           [8, 64, 64, 64]
 -DownScale: 1-3
                                           [8, 128, 32, 32]
     └─MaxPool2d: 2-9
                                           [8, 64, 32, 32]
     └─ConvBlock: 2-10
                                           [8, 128, 32, 32]
                                           [8, 128, 32, 32]
           └─Conv2d: 3-7
73,728
           └─BatchNorm2d: 3-8
                                           [8, 128, 32, 32]
                                                                      256
           └ReLU: 3-9
                                           [8, 128, 32, 32]
           └─Conv2d: 3-10
                                           [8, 128, 32, 32]
147,456
           └─BatchNorm2d: 3-11
                                           [8, 128, 32, 32]
                                                                      256
           └─ReLU: 3-12
                                           [8, 128, 32, 32]
```

-DownScale: 1-4	[8, 256, 16, 16]	-
☐ MaxPool2d: 2-11	[8, 128, 16, 16]	-
└─ConvBlock: 2-12	[8, 256, 16, 16]	-
294,912	[8, 250, 10, 10]	
│ │ │ └─BatchNorm2d: 3-14	[8, 256, 16, 16] 53	12
☐ReLU: 3-15	[8, 256, 16, 16]	-
	[8, 256, 16, 16]	
	[8, 256, 16, 16] 53	12
□ ReLU: 3-18	[8, 256, 16, 16]	-
-DownScale: 1-5	[8, 512, 8, 8]	-
└─MaxPool2d: 2-13 └─ConvBlock: 2-14	[8, 256, 8, 8]	-
Conv2d: 3-19	[8, 512, 8, 8]	-
1,179,648	[0, 312, 0, 0]	
	[8, 512, 8, 8]	
1,024 	[0 [12 0 0]	
—ReLU: 3-21 —Conv2d: 3-22	[8, 512, 8, 8]	-
2,359,296	[0, 312, 0, 0]	
│ │ │ │ □BatchNorm2d: 3-23	[8, 512, 8, 8]	
1,024 	[0 [12 0 0]	
UpScale: 1-6	[8, 512, 8, 8] [8, 256, 16, 16]	_
ConvTranspose2d: 2-15	[8, 256, 16, 16]	
524,544		
└─ConvBlock: 2-16	[8, 256, 16, 16]	-
	[8, 256, 16, 16]	
	[8, 256, 16, 16] 53	12
☐ReLU: 3-27	[8, 256, 16, 16]	-
Conv2d: 3-28	[8, 256, 16, 16]	
589,824	[8, 256, 16, 16] 53	12
□ ReLU: 3-30	[8, 256, 16, 16]	-
-UpScale: 1-7	[8, 128, 32, 32]	-
ConvTranspose2d: 2-17	[8, 128, 32, 32]	
131,200	[8, 128, 32, 32]	
Conv2d: 3-31	[8, 128, 32, 32]	
294,912		
☐BatchNorm2d: 3-32	- · · · · · · -	56
└─ReLU: 3-33 └─Conv2d: 3-34	[8, 128, 32, 32]	-
147,456	[0, 120, 32, 32]	
│ │ │ │ │ BatchNorm2d: 3-35	- · · · · · · -	56
	[8, 128, 32, 32]	-
├─UpScale: 1-8	[8, 64, 64, 64]	-

```
└ConvTranspose2d: 2-19
                                           [8, 64, 64, 64]
32,832
     └─ConvBlock: 2-20
                                           [8, 64, 64, 64]
          └Conv2d: 3-37
                                           [8, 64, 64, 64]
73,728
           └─BatchNorm2d: 3-38
                                           [8, 64, 64, 64]
                                                                      128
          └ReLU: 3-39
                                           [8, 64, 64, 64]
           └─Conv2d: 3-40
                                           [8, 64, 64, 64]
36,864
           └─BatchNorm2d: 3-41
                                           [8, 64, 64, 64]
                                                                      128
                                           [8, 64, 64, 64]
          └─ReLU: 3-42
 -UpScale: 1-9
                                           [8, 32, 128, 128]
     ConvTranspose2d: 2-21
                                           [8, 32, 128, 128]
8,224
       ConvBlock: 2-22
                                           [8, 32, 128, 128]
           └─Conv2d: 3-43
                                           [8, 32, 128, 128]
18,432
          └─BatchNorm2d: 3-44
                                           [8, 32, 128, 128]
                                                                      64
          └─ReLU: 3-45
                                           [8, 32, 128, 128]
                                                                      - -
          └Conv2d: 3-46
                                           [8, 32, 128, 128]
9,216
           └─BatchNorm2d: 3-47
                                           [8, 32, 128, 128]
                                                                      64
          └─ReLU: 3-48
                                           [8, 32, 128, 128]
                                                                      - -
 -Conv2d: 1-10
                                           [8, 8, 128, 128]
                                                                      264
Total params: 7,763,272
Trainable params: 7,763,272
Non-trainable params: 0
Total mult-adds (G): 27.40
Input size (MB): 1.57
Forward/backward pass size (MB): 583.01
Params size (MB): 31.05
Estimated Total Size (MB): 615.63
```

## Training

```
clear_output(True)
plot_losses_coeffs(Unet_training_val_summary)
```







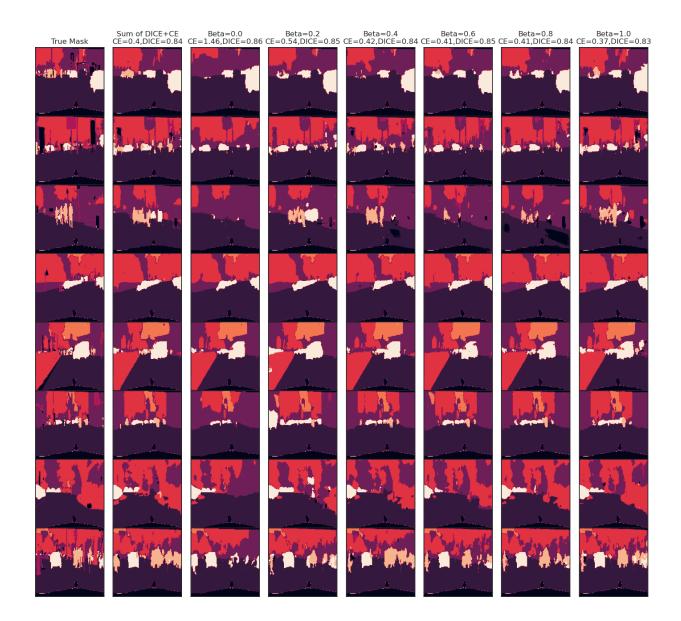
## Inference (Show results)

```
batch = next(iter(val_dataloader))
predictions = model(batch[0].to(device))
show_inference(batch, predictions)
```



# Comparison of how loss affects the prediction (CE and DICE)

```
# LONG COMPUTATION !
summaries, preds = get summaries preds for comparison(cfg.DELTA BETA,
Unet, {"n channels" : 3, "n classes" : number of classes},
model mixed=model, summary mixed=Unet training val summary,
epochs=cfg.CE VS DICE EVAL EPOCHS)
plot summaries preds(summaries, preds)
[341s] Model with Beta=0.0 finished with training CE:
1.0574553329804364, training DICE: 0.8912241382158103, validation
CE: 1.4579989490509033, Validation DICE: 0.8617529125213623,
Validation IOU: 0.758564356803894
[340s] Model with Beta=0.2 finished with training CE:
0.3518959712781826, training DICE: 0.8852573117488572, validation
CE: 0.5413730051517487, Validation DICE: 0.8460474448204041,
Validation IOU: 0.7354206352233886
[341s] Model with Beta=0.4 finished with training CE:
0.31280772093965226, training DICE: 0.8759067748574649, validation CE
: 0.41505336356163025, Validation DICE : 0.8400001773834228,
Validation IOU: 0.7250734853744507
[341s] Model with Beta=0.600000000000001 finished with training CE :
0.2993443368062252, training DICE: 0.869216700321486, validation CE:
0.40533775854110715, Validation DICE: 0.8483039331436157, Validation
IOU: 0.7375851068496704
[339s] Model with Beta=0.8 finished with training CE:
0.2940808230538328, training DICE: 0.8627328762286851, validation
CE: 0.4104516186714172, Validation DICE: 0.8416477770805358,
Validation IOU: 0.7276208910942078
[340s] Model with Beta=1.0 finished with training CE:
0.29596909069213545, training DICE: 0.8563255496586071, validation CE
: 0.3713782172203064, Validation DICE : 0.8297091226577759, Validation
IOU: 0.7098365216255188
```



## **ENet**

## Description

UNet is great and very commonly used. In typical usages of this network however, it has a lot (millons) parameters. For autonomous cars, we care not only about the accuracy of precision (which is of course extremely important and there is a lot of research done in validation and verfication of neural networks) but also how fast we can came up with this result. Camera systems are not perfect, and regular cameras can create a blurrly, non ideal pictures especially when moving (fast). A very famous quote comes to mind, saying "Garbage In, Garbage Out". Despite having an extremely good classifier, perhaps a particular input image will be quite distorted and classifier won't be able to handle that. If we could make inference fast however,

the probability that at least some of the pictures will be good quality (came from appropriate distribution) and contain good information greatly increases.

Researchers created many architectures for fast and accurate semantic segmenation. A good, comprehensive list can be found here [2]. The first architecture we explore will be ENet. This architecture was introduced in 2016 in paper "ENet: A Deep Neural Network Architecture for Real-Time Semantic Segmentation" and achieved real-time (15-20 fps) on embedded device (NVIDIA TX1, an under 10W module with CUDA capability). The network itself uses many "tricks" and is very lightweight: model itself weights only 0.7MB for 640 x 480 image input. Some of the tricks to make it work, are described below as well as in the paper [1]:

- The network itself has an **encoder-decoder architecture**. This is different to UNET, where it follows kind of encoder-decoder architecture, but outputs from encoder are combined in decoder layers with upsampled values. Here the network is simpler, and it is constantly applying forward blocks (no taking outputs from decoder as in UNET, however indices from maxPool operations are saved and used in Upsampling): (3, 512, 512) -> (16, 256, 256) -> (64, 128, 128) -> (128, 64, 64) -> (64, 128, 128) -> (16, 256, 256) -> (C, 512, 512). Another good thing, is that input size is equal to output size.
- it downsamples early: as authors point out, processing large frames is expensive, therefore downscaling is applied as soon as possible (first two blocks reduce the input size). An interesting insight from the paper: "The idea behind it, is that visual information is highly spatially redundant, and thus can be compressed into a more efficient representation. Also, our intuition is that the initial network layers should not directly contribute to classification. Instead, they should rather act as good feature extractors and only preprocess the input for later portions of the network"
- An important improvement (which makes the networks much scalable) is **factoring Filters (asymmetric convolutions)**. They decompose applying a convolution of N x N to applying two subsequent convolution with kernel sizes 1xN and Nx1 respectively.
- large encoder and small decoder (encoder should work simillarly to image classification networks, decoder should only focus on upsampling the output of encoder and only finetune the details)
- Authors observed that performing pooling followed by convolution (which increases the dimensionality by making the output channel number greater than input channels) introduces a representation bottleneck. It is however cheaper to do (Pooling so reducing the number of pixel to 1/4 of original input, and convolutions) in comparison to other way around (convolution on original number of pixels, then downscaling). The authors decided to do pooling operation in parallel with convolution with stride of 2, and concatenate feature maps.
- different nonlinearity (using PREeLU instead of ReLU) [1]
   https://arxiv.org/abs/1606.02147 [2] https://paperswithcode.com/task/real-time-semantic-segmentation [3]
   https://github.com/davidtvs/PyTorch-ENet/blob/master/models/enet.py (reference implementation)

#### Architecture

```
# Original implementaton can be found here
https://github.com/davidtvs/PyTorch-ENet/blob/master/models/enet.py
class InitialBlock(nn.Module):
    """The initial block is composed of two branches:
    1. a main branch which performs a regular convolution with stride
2;
    2. an extension branch which performs max-pooling.
    Doing both operations in parallel and concatenating their results
    allows for efficient downsampling and expansion. The main branch
    outputs 13 feature maps while the extension branch outputs 3, for
a
    total of 16 feature maps after concatenation.
    Keyword arguments:
    - in channels (int): the number of input channels.
    - out channels (int): the number output channels.
    - kernel size (int, optional): the kernel size of the filters used
in
    the convolution layer. Default: 3.
    - padding (int, optional): zero-padding added to both sides of the
    input. Default: 0.
    - bias (bool, optional): Adds a learnable bias to the output if
    ``True``. Default: False.
    - relu (bool, optional): When ``True`` ReLU is used as the
activation
    function; otherwise, PReLU is used. Default: True.
    0.00
    def init (self,
                 in channels,
                 out channels,
                 bias=False.
                 relu=True):
        super(). init ()
        if relu:
            activation = nn.ReLU
        else:
            activation = nn.PReLU
        # Main branch - As stated above the number of output channels
for this
        # branch is the total minus 3, since the remaining channels
come from
        # the extension branch
```

```
self.main branch = nn.Conv2d(
            in channels,
            out channels - 3,
            kernel size=3,
            stride=2.
            padding=1,
            bias=bias)
        # Extension branch
        self.ext branch = nn.MaxPool2d(3, stride=2, padding=1)
        # Initialize batch normalization to be used after
concatenation
        self.batch norm = nn.BatchNorm2d(out channels)
        # PReLU layer to apply after concatenating the branches
        self.out activation = activation()
    def forward(self, x):
        main = self.main branch(x)
        ext = self.ext branch(x)
        # Concatenate branches
        out = torch.cat((main, ext), 1)
        # Apply batch normalization
        out = self.batch norm(out)
        return self.out activation(out)
class RegularBottleneck(nn.Module):
    """Regular bottlenecks are the main building block of ENet.
    Main branch:
    1. Shortcut connection.
    Extension branch:
    1. 1x1 convolution which decreases the number of channels by
    ``internal ratio``, also called a projection;
    2. regular, dilated or asymmetric convolution;
    3. 1x1 convolution which increases the number of channels back to
    ``channels``, also called an expansion;
    4. dropout as a regularizer.
    Keyword arguments:
    - channels (int): the number of input and output channels.
    - internal ratio (int, optional): a scale factor applied to
    ``channels`` used to compute the number of
    channels after the projection. eg. given ``channels`` equal to 128
    internal ratio equal to 2 the number of channels after the
projection
```

```
is 64. Default: 4.
    - kernel size (int, optional): the kernel size of the filters used
in
    the convolution laver described above in item 2 of the extension
    branch. Default: 3.
    - padding (int, optional): zero-padding added to both sides of the
    input. Default: 0.
    - dilation (int, optional): spacing between kernel elements for
the
    convolution described in item 2 of the extension branch. Default:
1.
    asymmetric (bool, optional): flags if the convolution described in
    item 2 of the extension branch is asymmetric or not. Default:
False.
    dropout_prob (float, optional): probability of an element to be
    zeroed. Default: 0 (no dropout).
    - bias (bool, optional): Adds a learnable bias to the output if
    ``True``. Default: False.
    - relu (bool, optional): When ``True`` ReLU is used as the
activation
    function; otherwise, PReLU is used. Default: True.
    0.00
    def init (self,
                 channels,
                 internal ratio=4,
                 kernel size=3,
                 padding=0,
                 dilation=1,
                 asymmetric=False,
                 dropout_prob=0,
                 bias=False,
                 relu=True):
        super(). init ()
        # Check in the internal scale parameter is within the expected
range
        # [1, channels]
        if internal ratio <= 1 or internal ratio > channels:
            raise RuntimeError("Value out of range. Expected value in
the "
                               "interval [1, {0}], got
internal scale={1}."
                               .format(channels, internal ratio))
        internal channels = channels // internal ratio
        if relu:
            activation = nn.ReLU
```

```
else:
            activation = nn.PReLU
        # Main branch - shortcut connection
        # Extension branch - 1x1 convolution, followed by a regular,
dilated or
        # asymmetric convolution, followed by another 1x1 convolution,
and.
        # finally, a regularizer (spatial dropout). Number of channels
is constant.
        # 1x1 projection convolution
        self.ext conv1 = nn.Sequential(
            nn.Conv2d(
                channels,
                internal_channels,
                kernel size=1,
                stride=1,
                bias=bias), nn.BatchNorm2d(internal channels),
activation())
        # If the convolution is asymmetric we split the main
convolution in
        # two. Eq. for a 5x5 asymmetric convolution we have two
convolution:
        # the first is 5x1 and the second is 1x5.
        if asymmetric:
            self.ext_conv2 = nn.Sequential(
                nn.Conv2d(
                    internal channels,
                    internal channels,
                    kernel size=(kernel size, 1),
                    stride=1,
                    padding=(padding, 0),
                    dilation=dilation,
                    bias=bias), nn.BatchNorm2d(internal channels),
activation(),
                nn.Conv2d(
                    internal channels,
                    internal channels,
                    kernel size=(1, kernel size),
                    stride=1,
                    padding=(0, padding),
                    dilation=dilation,
                    bias=bias), nn.BatchNorm2d(internal channels),
activation())
        else:
            self.ext_conv2 = nn.Sequential(
                nn.Conv2d(
```

```
internal channels,
                    internal channels,
                    kernel size=kernel size,
                    stride=1.
                    padding=padding,
                    dilation=dilation,
                    bias=bias), nn.BatchNorm2d(internal channels),
activation())
        # 1x1 expansion convolution
        self.ext conv3 = nn.Sequential(
            nn.Conv2d(
                internal channels,
                channels,
                kernel size=1,
                stride=1,
                bias=bias), nn.BatchNorm2d(channels), activation())
        self.ext regul = nn.Dropout2d(p=dropout prob)
        # PReLU layer to apply after adding the branches
        self.out activation = activation()
    def forward(self, x):
        # Main branch shortcut
        main = x
        # Extension branch
        ext = self.ext conv1(x)
        ext = self.ext conv2(ext)
        ext = self.ext conv3(ext)
        ext = self.ext regul(ext)
        # Add main and extension branches
        out = main + ext
        return self.out activation(out)
class DownsamplingBottleneck(nn.Module):
    """Downsampling bottlenecks further downsample the feature map
size.
    Main branch:
    1. max pooling with stride 2; indices are saved to be used for
    unpooling later.
    Extension branch:
    1. 2x2 convolution with stride 2 that decreases the number of
channels
   by ``internal_ratio``, also called a projection;
    2. regular convolution (by default, 3x3);
```

```
3. 1x1 convolution which increases the number of channels to
    ``out channels``, also called an expansion;
    4. dropout as a regularizer.
    Keyword arguments:
    - in channels (int): the number of input channels.
    - out channels (int): the number of output channels.
    - internal ratio (int, optional): a scale factor applied to
``channels``
    used to compute the number of channels after the projection. eg.
    ``channels`` equal to 128 and internal ratio equal to 2 the number
of
    channels after the projection is 64. Default: 4.
    - return_indices (bool, optional): if ``True``, will return the
max
    indices along with the outputs. Useful when unpooling later.
    - dropout prob (float, optional): probability of an element to be
    zeroed. Default: 0 (no dropout).
    - bias (bool, optional): Adds a learnable bias to the output if
    ``True``. Default: False.
    - relu (bool, optional): When ``True`` ReLU is used as the
activation
    function; otherwise, PReLU is used. Default: True.
    def init (self,
                 in channels,
                 out channels,
                 internal ratio=4,
                 return indices=False,
                 dropout prob=0,
                 bias=False.
                 relu=True):
        super(). init ()
        # Store parameters that are needed later
        self.return indices = return indices
        # Check in the internal scale parameter is within the expected
range
        # [1, channels]
        if internal ratio <= 1 or internal ratio > in channels:
            raise RuntimeError("Value out of range. Expected value in
the "
                               "interval [1, {0}], got
internal scale={1}. "
                               .format(in channels, internal ratio))
```

```
internal channels = in channels // internal ratio
        if relu:
            activation = nn.ReLU
        else:
            activation = nn.PReLU
        # Main branch - max pooling followed by feature map (channels)
padding
        self.main max1 = nn.MaxPool2d(
            2,
            stride=2,
            return indices=return indices)
        # Extension branch - 2x2 convolution, followed by a regular,
dilated or
        # asymmetric convolution, followed by another 1x1 convolution.
Number
        # of channels is doubled.
        # 2x2 projection convolution with stride 2
        self.ext conv1 = nn.Sequential(
            nn.Conv2d(
                in channels,
                internal channels,
                kernel size=2,
                stride=2,
                bias=bias), nn.BatchNorm2d(internal channels),
activation())
        # Convolution
        self.ext conv2 = nn.Sequential(
            nn.Conv2d(
                internal channels,
                internal channels,
                kernel size=3,
                stride=1,
                padding=1,
                bias=bias), nn.BatchNorm2d(internal channels),
activation())
        # 1x1 expansion convolution
        self.ext conv3 = nn.Sequential(
            nn.Conv2d(
                internal channels,
                out channels,
                kernel size=1,
                stride=1,
                bias=bias), nn.BatchNorm2d(out channels),
activation())
```

```
self.ext regul = nn.Dropout2d(p=dropout prob)
        # PReLU layer to apply after concatenating the branches
        self.out activation = activation()
    def forward(self, x):
        # Main branch shortcut
        if self.return indices:
            main, max indices = self.main max1(x)
            main = self.main max1(x)
        # Extension branch
        ext = self.ext conv1(x)
        ext = self.ext conv2(ext)
        ext = self.ext conv3(ext)
        ext = self.ext_regul(ext)
        # Main branch channel padding
        n, ch ext, h, w = ext.size()
        ch main = main.size()[1]
        padding = torch.zeros(n, ch ext - ch main, h, w)
        # Before concatenating, check if main is on the CPU or GPU and
        # convert padding accordingly
        if main.is cuda:
            padding = padding.cuda()
        # Concatenate
        main = torch.cat((main, padding), 1)
        # Add main and extension branches
        out = main + ext
        return self.out activation(out), max indices
class UpsamplingBottleneck(nn.Module):
    """The upsampling bottlenecks upsample the feature map resolution
using max
    pooling indices stored from the corresponding downsampling
bottleneck.
    Main branch:
    1. 1x1 convolution with stride 1 that decreases the number of
    ``internal ratio``, also called a projection;
    2. max unpool layer using the max pool indices from the
corresponding
    downsampling max pool layer.
```

```
Extension branch:
    1. 1x1 convolution with stride 1 that decreases the number of
channels by
    ``internal_ratio``, also called a projection;
    2. transposed convolution (by default, 3x3);
    3. 1x1 convolution which increases the number of channels to
    ``out_channels``, also called an expansion;
    4. dropout as a regularizer.
    Keyword arguments:
    - in channels (int): the number of input channels.
    - out channels (int): the number of output channels.
    internal_ratio (int, optional): a scale factor applied to
``in channels``
     used to compute the number of channels after the projection. eg.
     ``in channels`` equal to 128 and ``internal_ratio`` equal to 2
the number
     of channels after the projection is 64. Default: 4.
    - dropout prob (float, optional): probability of an element to be
zeroed.
    Default: 0 (no dropout).
    - bias (bool, optional): Adds a learnable bias to the output if
``True``.
    Default: False.
    - relu (bool, optional): When ``True`` ReLU is used as the
activation
    function; otherwise, PReLU is used. Default: True.
    0.00
    def __init__(self,
                 in channels,
                 out channels,
                 internal_ratio=4,
                 dropout prob=0,
                 bias=False,
                 relu=True):
        super(). init ()
        # Check in the internal scale parameter is within the expected
range
        # [1, channels]
        if internal ratio <= 1 or internal ratio > in channels:
            raise RuntimeError("Value out of range. Expected value in
the "
                               "interval [1, {0}], got
internal scale={1}. "
                               .format(in channels, internal ratio))
```

```
internal channels = in channels // internal ratio
        if relu:
            activation = nn.ReLU
        else:
            activation = nn.PReLU
        # Main branch - max pooling followed by feature map (channels)
padding
        self.main conv1 = nn.Sequential(
            nn.Conv2d(in channels, out channels, kernel size=1,
bias=bias).
            nn.BatchNorm2d(out channels))
        # Remember that the stride is the same as the kernel size,
just like
        # the max pooling layers
        self.main unpool1 = nn.MaxUnpool2d(kernel size=2)
        # Extension branch - 1x1 convolution, followed by a regular,
dilated or
        # asymmetric convolution, followed by another 1x1 convolution.
Number
        # of channels is doubled.
        # 1x1 projection convolution with stride 1
        self.ext_conv1 = nn.Sequential(
            nn.Conv2d(
                in_channels, internal channels, kernel size=1,
bias=bias),
            nn.BatchNorm2d(internal channels), activation())
        # Transposed convolution
        self.ext_tconv1 = nn.ConvTranspose2d(
            internal channels,
            internal channels,
            kernel size=2,
            stride=2,
            bias=bias)
        self.ext tconv1 bnorm = nn.BatchNorm2d(internal channels)
        self.ext tconv1 activation = activation()
        # 1x1 expansion convolution
        self.ext conv2 = nn.Sequential(
            nn.Conv2d(
                internal channels, out channels, kernel size=1,
bias=bias),
            nn.BatchNorm2d(out channels))
```

```
self.ext regul = nn.Dropout2d(p=dropout prob)
        # PReLU layer to apply after concatenating the branches
        self.out activation = activation()
   def forward(self, x, max indices, output size):
        # Main branch shortcut
        main = self.main conv1(x)
        main = self.main unpool1(
            main, max indices, output size=output size)
        # Extension branch
        ext = self.ext conv1(x)
        ext = self.ext tconv1(ext, output size=output size)
        ext = self.ext tconv1 bnorm(ext)
        ext = self.ext tconv1 activation(ext)
        ext = self.ext conv2(ext)
        ext = self.ext regul(ext)
        # Add main and extension branches
        out = main + ext
        return self.out activation(out)
class ENet(nn.Module):
    """Generate the ENet model.
   Keyword arguments:
    - num classes (int): the number of classes to segment.
    - encoder relu (bool, optional): When ``True`` ReLU is used as the
   activation function in the encoder blocks/layers; otherwise, PReLU
   is used. Default: False.
   - decoder relu (bool, optional): When ``True`` ReLU is used as the
   activation function in the decoder blocks/layers; otherwise, PReLU
   is used. Default: True.
    0.00
   def __init__(self, num_classes, encoder relu=False,
decoder relu=True):
        super().__init__()
        self.initial_block = InitialBlock(3, 16, relu=encoder relu)
        # Stage 1 - Encoder
        self.downsample1 0 = DownsamplingBottleneck(
            16.
            64.
            return indices=True,
            dropout prob=0.01,
            relu=encoder relu)
```

```
self.regular1 1 = RegularBottleneck(
            64, padding=1, dropout prob=0.01, relu=encoder relu)
        self.regular1 2 = RegularBottleneck(
            64, padding=1, dropout prob=0.01, relu=encoder relu)
        self.regular1 3 = RegularBottleneck(
            64, padding=1, dropout prob=0.01, relu=encoder relu)
        self.regular1 4 = RegularBottleneck(
            64, padding=1, dropout prob=0.01, relu=encoder relu)
        # Stage 2 - Encoder
        self.downsample2 0 = DownsamplingBottleneck(
            64.
            128,
            return indices=True,
            dropout prob=0.1,
            relu=encoder relu)
        self.regular2 1 = RegularBottleneck(
            128, padding=1, dropout prob=0.1, relu=encoder relu)
        self.dilated2 2 = RegularBottleneck(
            128, dilation=2, padding=2, dropout prob=0.1,
relu=encoder relu)
        self.asymmetric2 3 = RegularBottleneck(
            128,
            kernel size=5,
            padding=2,
            asymmetric=True,
            dropout prob=0.1,
            relu=encoder relu)
        self.dilated2 4 = RegularBottleneck(
            128, dilation=4, padding=4, dropout prob=0.1,
relu=encoder relu)
        self.regular2 5 = RegularBottleneck(
            128, padding=1, dropout prob=0.1, relu=encoder relu)
        self.dilated2 6 = RegularBottleneck(
            128, dilation=8, padding=8, dropout prob=0.1,
relu=encoder relu)
        self.asymmetric2 7 = RegularBottleneck(
            128,
            kernel size=5,
            asymmetric=True,
            padding=2,
            dropout prob=0.1,
            relu=encoder relu)
        self.dilated2 8 = RegularBottleneck(
            128, dilation=16, padding=16, dropout prob=0.1,
relu=encoder relu)
        # Stage 3 - Encoder
        self.regular3 0 = RegularBottleneck(
```

```
128, padding=1, dropout prob=0.1, relu=encoder relu)
        self.dilated3 1 = RegularBottleneck(
            128, dilation=2, padding=2, dropout prob=0.1,
relu=encoder relu)
        self.asymmetric3 2 = RegularBottleneck(
            128.
            kernel size=5,
            padding=2,
            asymmetric=True,
            dropout prob=0.1,
            relu=encoder relu)
        self.dilated3 3 = RegularBottleneck(
            128, dilation=4, padding=4, dropout prob=0.1,
relu=encoder relu)
        self.regular3 4 = RegularBottleneck(
            128, padding=1, dropout prob=0.1, relu=encoder relu)
        self.dilated3 5 = RegularBottleneck(
            128, dilation=8, padding=8, dropout prob=0.1,
relu=encoder relu)
        self.asymmetric3 6 = RegularBottleneck(
            128,
            kernel size=5,
            asymmetric=True,
            padding=2,
            dropout prob=0.1,
            relu=encoder relu)
        self.dilated3 7 = RegularBottleneck(
            128, dilation=16, padding=16, dropout prob=0.1,
relu=encoder relu)
        # Stage 4 - Decoder
        self.upsample4 0 = UpsamplingBottleneck(
            128, 64, dropout prob=0.1, relu=decoder relu)
        self.regular4 1 = RegularBottleneck(
            64, padding=1, dropout_prob=0.1, relu=decoder_relu)
        self.regular4 2 = RegularBottleneck(
            64, padding=1, dropout prob=0.1, relu=decoder relu)
        # Stage 5 - Decoder
        self.upsample5 0 = UpsamplingBottleneck(
            64, 16, dropout prob=0.1, relu=decoder relu)
        self.regular5 1 = RegularBottleneck(
            16, padding=1, dropout prob=0.1, relu=decoder relu)
        self.transposed conv = nn.ConvTranspose2d(
            16.
            num classes,
            kernel size=3,
            stride=2,
            padding=1,
```

```
bias=False)
    def forward(self, x):
        # Initial block
        input size = x.size()
        x = self.initial block(x)
        # Stage 1 - Encoder
        stage1 input size = x.size()
        x, max indices 0 = self.downsample 0(x)
        x = self.regular1_1(x)
        x = self.regular1 2(x)
        x = self.regular1 3(x)
        x = self.regular1 4(x)
        # Stage 2 - Encoder
        stage2 input size = x.size()
        x, max indices 0 = self.downsample 20(x)
        x = self.regular2 1(x)
        x = self.dilated2 2(x)
        x = self.asymmetric2 3(x)
        x = self.dilated2 4(x)
        x = self.regular2 5(x)
        x = self.dilated2_6(x)
        x = self.asymmetric2 7(x)
        x = self.dilated2 8(x)
        # Stage 3 - Encoder
        x = self.regular3 0(x)
        x = self.dilated3 1(x)
        x = self.asymmetric3 2(x)
        x = self.dilated3 3(x)
        x = self.regular3 4(x)
        x = self.dilated3_5(x)
        x = self.asymmetric3 6(x)
        x = self.dilated3 7(x)
        # Stage 4 - Decoder
        x = self.upsample4_0(x, max_indices2_0,
output size=stage2 input size)
        x = self.regular4 1(x)
        x = self.regular4 2(x)
        # Stage 5 - Decoder
        x = self.upsample5 0(x, max indices1 0,
output_size=stage1_input size)
        x = self.regular5_1(x)
        x = self.transposed conv(x, output size=input size)
        return x
```

```
number of classes = len(set(name to category.values()))
summary(ENet(number_of_classes), input_data=batch[0])
Layer (type:depth-idx)
                                            Output Shape
Param #
ENet
                                            [8, 8, 128, 128]
 -InitialBlock: 1-1
                                            [8, 16, 64, 64]
                                                                        _ _
      └─Conv2d: 2-1
                                            [8, 13, 64, 64]
                                                                        351
     └─MaxPool2d: 2-2
                                            [8, 3, 64, 64]
                                                                        _ _
     └─BatchNorm2d: 2-3
                                            [8, 16, 64, 64]
                                                                        32
     └─PReLU: 2-4
                                            [8, 16, 64, 64]
                                                                        1
                                            [8, 64, 32, 32]
 -DownsamplingBottleneck: 1-2
                                                                        - -
      └─MaxPool2d: 2-5
                                            [8, 16, 32, 32]
                                                                        - -
     └─Sequential: 2-6
                                            [8, 4, 32, 32]
                                                                        - -
           └─Conv2d: 3-1
                                            [8, 4, 32, 32]
                                                                        256
           └─BatchNorm2d: 3-2
                                            [8, 4, 32, 32]
                                                                        8
           └─PReLU: 3-3
                                            [8, 4, 32, 32]
                                                                        1
                                            [8, 4, 32, 32]
       -Sequential: 2-7
                                                                        - -
                                            [8, 4, 32, 32]
           └─Conv2d: 3-4
                                                                        144
           └─BatchNorm2d: 3-5
                                            [8, 4, 32, 32]
                                                                        8
           └─PReLU: 3-6
                                            [8, 4, 32, 32]
                                                                        1
       -Sequential: 2-8
                                            [8, 64, 32, 32]
                                                                        - -
           └─Conv2d: 3-7
                                            [8, 64, 32, 32]
                                                                        256
                                            [8, 64, 32, 32]
           └─BatchNorm2d: 3-8
                                                                        128
           └─PReLU: 3-9
                                            [8, 64, 32, 32]
                                                                        1
                                            [8, 64, 32, 32]
       -Dropout2d: 2-9
                                                                        - -
     └─PReLU: 2-10
                                            [8, 64, 32, 32]
                                                                        1
                                            [8, 64, 32, 32]
 -RegularBottleneck: 1-3
                                                                        - -
                                            [8, 16, 32, 32]
     └─Sequential: 2-11
           └─Conv2d: 3-10
                                            [8, 16, 32, 32]
1,024
           └─BatchNorm2d: 3-11
                                            [8, 16, 32, 32]
                                                                        32
           └─PReLU: 3-12
                                            [8, 16, 32, 32]
                                                                        1
       -Sequential: 2-12
                                            [8, 16, 32, 32]
           └─Conv2d: 3-13
                                            [8, 16, 32, 32]
2,304
           └─BatchNorm2d: 3-14
                                            [8, 16, 32, 32]
                                                                        32
                                            [8, 16, 32, 32]
           └─PReLU: 3-15
                                                                        1
       -Sequential: 2-13
                                            [8, 64, 32, 32]
           └─Conv2d: 3-16
                                            [8, 64, 32, 32]
1,024
           └─BatchNorm2d: 3-17
                                            [8, 64, 32, 32]
                                                                        128
                                            [8, 64, 32, 32]
           └─PReLU: 3-18
                                                                        1
       -Dropout2d: 2-14
                                            [8, 64, 32, 32]
                                                                        - -
                                            [8, 64, 32, 32]
     └─PReLU: 2-15
                                                                        1
```

```
-RegularBottleneck: 1-4
                                            [8, 64, 32, 32]
       -Sequential: 2-16
                                            [8, 16, 32, 32]
           └─Conv2d: 3-19
                                            [8, 16, 32, 32]
1,024
           └─BatchNorm2d: 3-20
                                            [8, 16, 32, 32]
                                                                        32
           └─PReLU: 3-21
                                            [8, 16, 32, 32]
                                                                         1
       Sequential: 2-17
                                            [8, 16, 32, 32]
                                                                         - -
                                            [8, 16, 32, 32]
           └─Conv2d: 3-22
2,304
           └─BatchNorm2d: 3-23
                                            [8, 16, 32, 32]
                                                                        32
           └─PReLU: 3-24
                                            [8, 16, 32, 32]
                                                                         1
       Sequential: 2-18
                                            [8, 64, 32, 32]
                                                                         - -
                                            [8, 64, 32, 32]
           └Conv2d: 3-25
1,024
           └─BatchNorm2d: 3-26
                                            [8, 64, 32, 32]
                                                                         128
           └─PReLU: 3-27
                                            [8, 64, 32, 32]
                                                                         1
       -Dropout2d: 2-19
                                            [8, 64, 32, 32]
                                                                         - -
     └─PReLU: 2-20
                                            [8, 64, 32, 32]
                                                                         1
 -RegularBottleneck: 1-5
                                            [8, 64, 32, 32]
                                                                         - -
                                            [8, 16, 32, 32]
      -Sequential: 2-21
           └Conv2d: 3-28
                                            [8, 16, 32, 32]
1,024
           └BatchNorm2d: 3-29
                                            [8, 16, 32, 32]
                                                                        32
           └─PReLU: 3-30
                                            [8, 16, 32, 32]
                                                                        1
                                            [8, 16, 32, 32]
       Sequential: 2-22
           └Conv2d: 3-31
                                            [8, 16, 32, 32]
2,304
           └─BatchNorm2d: 3-32
                                            [8, 16, 32, 32]
                                                                        32
           └─PReLU: 3-33
                                            [8, 16, 32, 32]
                                                                        1
                                            [8, 64, 32, 32]
       Sequential: 2-23
           └─Conv2d: 3-34
                                            [8, 64, 32, 32]
1,024
           └─BatchNorm2d: 3-35
                                            [8, 64, 32, 32]
                                                                        128
           └-PReLU: 3-36
                                            [8, 64, 32, 32]
                                                                         1
       -Dropout2d: 2-24
                                            [8, 64, 32, 32]
                                                                         - -
     └─PReLU: 2-25
                                            [8, 64, 32, 32]
                                                                         1
                                            [8, 64, 32, 32]
 -RegularBottleneck: 1-6
     └─Sequential: 2-26
                                            [8, 16, 32, 32]
                                            [8, 16, 32, 32]
           └Conv2d: 3-37
1,024
           └─BatchNorm2d: 3-38
                                            [8, 16, 32, 32]
                                                                         32
           └─PReLU: 3-39
                                            [8, 16, 32, 32]
                                                                         1
       Sequential: 2-27
                                            [8, 16, 32, 32]
                                                                         - -
                                            [8, 16, 32, 32]
           └Conv2d: 3-40
2,304
                                            [8, 16, 32, 32]
           └─BatchNorm2d: 3-41
                                                                        32
           └─PReLU: 3-42
                                            [8, 16, 32, 32]
                                                                         1
                                            [8, 64, 32, 32]
       Sequential: 2-28
                                                                         - -
           └─Conv2d: 3-43
                                            [8, 64, 32, 32]
```

```
1,024
           └─BatchNorm2d: 3-44
                                             [8, 64, 32, 32]
                                                                         128
           └─PReLU: 3-45
                                             [8, 64, 32, 32]
                                                                         1
       -Dropout2d: 2-29
                                             [8, 64, 32, 32]
                                                                         - -
     └─PReLU: 2-30
                                             [8, 64, 32, 32]
                                                                         1
                                             [8, 128, 16, 16]
 -DownsamplingBottleneck: 1-7
      └─MaxPool2d: 2-31
                                             [8, 64, 16, 16]
      └─Sequential: 2-32
                                             [8, 16, 16, 16]
           └─Conv2d: 3-46
                                             [8, 16, 16, 16]
4,096
           └─BatchNorm2d: 3-47
                                             [8, 16, 16, 16]
                                                                         32
           └─PReLU: 3-48
                                             [8, 16, 16, 16]
                                                                         1
                                             [8, 16, 16, 16]
       Sequential: 2-33
           └Conv2d: 3-49
                                             [8, 16, 16, 16]
2,304
           └─BatchNorm2d: 3-50
                                             [8, 16, 16, 16]
                                                                         32
           └-PReLU: 3-51
                                             [8, 16, 16, 16]
                                                                         1
       Sequential: 2-34
                                             [8, 128, 16, 16]
                                                                         - -
           └Conv2d: 3-52
                                             [8, 128, 16, 16]
2,048
           └─BatchNorm2d: 3-53
                                             [8, 128, 16, 16]
                                                                         256
           └─PReLU: 3-54
                                             [8, 128, 16, 16]
                                                                         1
       -Dropout2d: 2-35
                                             [8, 128, 16, 16]
                                                                         - -
     └─PReLU: 2-36
                                             [8, 128, 16, 16]
                                                                         1
                                             [8, 128, 16, 16]
 -RegularBottleneck: 1-8
                                                                         - -
      └─Sequential: 2-37
                                             [8, 32, 16, 16]
                                             [8, 32, 16, 16]
           └─Conv2d: 3-55
4,096
           └─BatchNorm2d: 3-56
                                             [8, 32, 16, 16]
                                                                         64
           └─PReLU: 3-57
                                             [8, 32, 16, 16]
                                                                         1
       Sequential: 2-38
                                             [8, 32, 16, 16]
           └Conv2d: 3-58
                                             [8, 32, 16, 16]
9,216
                                             [8, 32, 16, 16]
           └─BatchNorm2d: 3-59
                                                                         64
           └─PReLU: 3-60
                                             [8, 32, 16, 16]
                                                                         1
       Sequential: 2-39
                                             [8, 128, 16, 16]
           └─Conv2d: 3-61
                                             [8, 128, 16, 16]
4,096
           └─BatchNorm2d: 3-62
                                             [8, 128, 16, 16]
                                                                         256
           └-PReLU: 3-63
                                                                         1
                                             [8, 128, 16, 16]
                                             [8, 128, 16, 16]
       -Dropout2d: 2-40
                                                                         - -
     └─PReLU: 2-41
                                             [8, 128, 16, 16]
                                                                         1
 -RegularBottleneck: 1-9
                                             [8, 128, 16, 16]
                                                                         - -
                                             [8, 32, 16, 16]
      └─Sequential: 2-42
                                                                         - -
           └Conv2d: 3-64
                                             [8, 32, 16, 16]
4,096
           └BatchNorm2d: 3-65
                                             [8, 32, 16, 16]
                                                                         64
           └─PReLU: 3-66
                                             [8, 32, 16, 16]
                                                                         1
       Sequential: 2-43
                                             [8, 32, 16, 16]
```

 9,216	└─Conv2d: 3-67		[8, 32, 16, 16]	
Ls	└─BatchNorm2d: └─PReLU: 3-69 equential: 2-44 └─Conv2d: 3-70		[8, 32, 16, 16] [8, 32, 16, 16] [8, 128, 16, 16] [8, 128, 16, 16]	64 1 
	└─BatchNorm2d: └─PReLU: 3-72 ropout2d: 2-45 ReLU: 2-46		[8, 128, 16, 16] [8, 128, 16, 16] [8, 128, 16, 16] [8, 128, 16, 16]	256 1  1
	rBottleneck: 1-10 equential: 2-47 └Conv2d: 3-73		[8, 128, 16, 16] [8, 32, 16, 16] [8, 32, 16, 16]	
4,096				
Ls	☐BatchNorm2d:☐PReLU: 3-75 equential: 2-48		[8, 32, 16, 16] [8, 32, 16, 16] [8, 32, 16, 16]	64 1 
 5,120	└─Conv2d: 3-76		[8, 32, 16, 16]	
3,120	└─BatchNorm2d: └─PReLU: 3-78 └─Conv2d: 3-79		[8, 32, 16, 16] [8, 32, 16, 16] [8, 32, 16, 16]	64 1
5,120	00117241 0 75		[0, 02, 10, 10]	
Ls	└─BatchNorm2d: └─PReLU: 3-81 equential: 2-49 └─Conv2d: 3-82		[8, 32, 16, 16] [8, 32, 16, 16] [8, 128, 16, 16] [8, 128, 16, 16]	64 1 
4,096				
⊢Regula	─BatchNorm2d: ─PReLU: 3-84 Propout2d: 2-50 PReLU: 2-51 PROTTLENECK: 1-13 Prequential: 2-52		[8, 128, 16, 16] [8, 32, 16, 16]	256 1  1 
	└─Conv2d: 3-85		[8, 32, 16, 16]	
	└─BatchNorm2d: └─PReLU: 3-87 equential: 2-53 └─Conv2d: 3-88	3-86	[8, 32, 16, 16] [8, 32, 16, 16] [8, 32, 16, 16] [8, 32, 16, 16]	64 1 
9,216	DatchNormad.	2 00	[0 22 16 16]	6.1
	└─BatchNorm2d: └─PReLU: 3-90 equential: 2-54 └─Conv2d: 3-91	3-89	[8, 32, 16, 16] [8, 32, 16, 16] [8, 128, 16, 16] [8, 128, 16, 16]	64 1 
4,096	Dot obloam2d	2 02	[0 120 16 16]	256
	└─BatchNorm2d: └─PReLU: 3-93	3-92	[8, 128, 16, 16] [8, 128, 16, 16]	256 1

```
└─Dropout2d: 2-55
                                            [8, 128, 16, 16]
     └─PReLU: 2-56
                                            [8, 128, 16, 16]
                                                                         1
 -RegularBottleneck: 1-12
                                            [8, 128, 16, 16]
                                                                         - -
      └─Sequential: 2-57
                                            [8, 32, 16, 16]
           └─Conv2d: 3-94
                                            [8, 32, 16, 16]
4,096
                                            [8, 32, 16, 16]
           └─BatchNorm2d: 3-95
                                                                        64
           └─PReLU: 3-96
                                            [8, 32, 16, 16]
                                                                         1
       -Sequential: 2-58
                                            [8, 32, 16, 16]
                                                                         - -
           └─Conv2d: 3-97
                                            [8, 32, 16, 16]
9,216
           └BatchNorm2d: 3-98
                                            [8, 32, 16, 16]
                                                                        64
           └─PReLU: 3-99
                                            [8, 32, 16, 16]
                                                                         1
       -Sequential: 2-59
                                            [8, 128, 16, 16]
                                                                         - -
           └─Conv2d: 3-100
                                            [8, 128, 16, 16]
4,096
           └─BatchNorm2d: 3-101
                                            [8, 128, 16, 16]
                                                                        256
                                            [8, 128, 16, 16]
           └─PReLU: 3-102
                                                                         1
       -Dropout2d: 2-60
                                            [8, 128, 16, 16]
                                                                         - -
                                            [8, 128, 16, 16]
     └─PReLU: 2-61
                                                                         1
                                            [8, 128, 16, 16]
 -RegularBottleneck: 1-13
                                                                         - -
      └─Sequential: 2-62
                                            [8, 32, 16, 16]
           └─Conv2d: 3-103
                                            [8, 32, 16, 16]
4,096
                                            [8, 32, 16, 16]
           └─BatchNorm2d: 3-104
                                                                        64
           └─PReLU: 3-105
                                            [8, 32, 16, 16]
                                                                         1
                                            [8, 32, 16, 16]
       Sequential: 2-63
           └Conv2d: 3-106
                                            [8, 32, 16, 16]
9,216
           └─BatchNorm2d: 3-107
                                            [8, 32, 16, 16]
                                                                        64
           └-PReLU: 3-108
                                            [8, 32, 16, 16]
                                                                         1
                                            [8, 128, 16, 16]
       Sequential: 2-64
           └─Conv2d: 3-109
                                            [8, 128, 16, 16]
4,096
                                            [8, 128, 16, 16]
                                                                        256
           └─BatchNorm2d: 3-110
           └─PReLU: 3-111
                                            [8, 128, 16, 16]
                                                                         1
       -Dropout2d: 2-65
                                            [8, 128, 16, 16]
                                                                         - -
     └─PReLU: 2-66
                                            [8, 128, 16, 16]
                                                                         1
                                            [8, 128, 16, 16]
 -RegularBottleneck: 1-14
      └─Sequential: 2-67
                                            [8, 32, 16, 16]
                                            [8, 32, 16, 16]
           └Conv2d: 3-112
4,096
           └─BatchNorm2d: 3-113
                                            [8, 32, 16, 16]
                                                                        64
                                            [8, 32, 16, 16]
           └─PReLU: 3-114
                                                                         1
                                            [8, 32, 16, 16]
       Sequential: 2-68
           └─Conv2d: 3-115
                                            [8, 32, 16, 16]
5,120
                                            [8, 32, 16, 16]
           └─BatchNorm2d: 3-116
                                                                        64
           └─PReLU: 3-117
                                            [8, 32, 16, 16]
                                                                        1
```

 5,120	└─Conv2d: 3-118	[8, 32, 16, 16]	
	└─BatchNorm2d: 3-119	[8, 32, 16, 16]	64
	└─PReLU: 3-120	[8, 32, 16, 16]	1
	quential: 2-69 └─Conv2d: 3-121	[8, 128, 16, 16] [8, 128, 16, 16]	
4,096	CONV24. 3 121	[0, 120, 10, 10]	
	└─BatchNorm2d: 3-122	[8, 128, 16, 16]	256
	└─PReLU: 3-123 opout2d: 2-70	[8, 128, 16, 16] [8, 128, 16, 16]	1
	eLU: 2-71	[8, 128, 16, 16]	1
	Bottleneck: 1-15	[8, 128, 16, 16]	
_Se	quential: 2-72 └─Conv2d: 3-124	[8, 32, 16, 16] [8, 32, 16, 16]	
4,096			
	└─BatchNorm2d: 3-125	[8, 32, 16, 16]	64
L <sub>Se</sub>	└─PReLU: 3-126 quential: 2-73	[8, 32, 16, 16] [8, 32, 16, 16]	1
	└─Conv2d: 3-127	[8, 32, 16, 16]	
9,216	Do+ chNo cm2d . 2 120	10 22 16 161	6.4
	└─BatchNorm2d: 3-128 └─PReLU: 3-129	[8, 32, 16, 16] [8, 32, 16, 16]	64 1
L <sub>Se</sub>	quential: 2-74	[8, 128, 16, 16]	
 4,096	└─Conv2d: 3-130	[8, 128, 16, 16]	
	└BatchNorm2d: 3-131	[8, 128, 16, 16]	256
	└─PReLU: 3-132	[8, 128, 16, 16]	1
	opout2d: 2-75 eLU: 2-76	[8, 128, 16, 16] [8, 128, 16, 16]	 1
	Bottleneck: 1-16	[8, 128, 16, 16]	
	quential: 2-77	[8, 32, 16, 16]	
1 4,096	└─Conv2d: 3-133	[8, 32, 16, 16]	
	└─BatchNorm2d: 3-134	[8, 32, 16, 16]	64
	└─PReLU: 3-135	[8, 32, 16, 16]	1
se	quential: 2-78 └─Conv2d: 3-136	[8, 32, 16, 16] [8, 32, 16, 16]	
9,216	_		
	└─BatchNorm2d: 3-137 └─PReLU: 3-138	[8, 32, 16, 16] [8, 32, 16, 16]	64 1
L <sub>Se</sub>	quential: 2-79	[8, 128, 16, 16]	
	└─Conv2d: 3-139	[8, 128, 16, 16]	
4,096 	└─BatchNorm2d: 3-140	[8, 128, 16, 16]	256
	—BatchNorm2d. 3-140 —PReLU: 3-141	[8, 128, 16, 16]	1
	opout2d: 2-80	[8, 128, 16, 16]	
	eLU: 2-81 Bottleneck: 1-17	[8, 128, 16, 16] [8, 128, 16, 16]	1
	quential: 2-82	[8, 32, 16, 16]	

 4,096	└─Conv2d: 3-142	[8, 32, 16, 16]	
_Se	└─BatchNorm2d: 3-143 └─PReLU: 3-144 quential: 2-83 └─Conv2d: 3-145	[8, 32, 16, 16] [8, 32, 16, 16] [8, 32, 16, 16] [8, 32, 16, 16]	64 1 
9,216 	└─BatchNorm2d: 3-146 └─PReLU: 3-147 quential: 2-84 └─Conv2d: 3-148	[8, 32, 16, 16] [8, 32, 16, 16] [8, 128, 16, 16] [8, 128, 16, 16]	64 1 
4,096			
└─PR ──Regular	└─BatchNorm2d: 3-149 └─PReLU: 3-150 opout2d: 2-85 eLU: 2-86 Bottleneck: 1-18 quential: 2-87 └─Conv2d: 3-151	[8, 128, 16, 16] [8, 32, 16, 16] [8, 32, 16, 16]	256 1  1 
4,096			
	└─BatchNorm2d: 3-152 └─PReLU: 3-153 quential: 2-88 └─Conv2d: 3-154	[8, 32, 16, 16] [8, 32, 16, 16] [8, 32, 16, 16] [8, 32, 16, 16]	64 1 
5,120	D-1-1-N2-1 2 155	10 22 16 161	C 4
	└─BatchNorm2d: 3-155 └─PReLU: 3-156 └─Conv2d: 3-157	[8, 32, 16, 16] [8, 32, 16, 16] [8, 32, 16, 16]	64 1
5,120			
	└─BatchNorm2d: 3-158 └─PReLU: 3-159 quential: 2-89 └─Conv2d: 3-160	[8, 32, 16, 16] [8, 32, 16, 16] [8, 128, 16, 16] [8, 128, 16, 16]	64 1 
4,096	DatahNamad. 2 161	[0 120 16 16]	256
⊢PR	└─BatchNorm2d: 3-161 └─PReLU: 3-162 opout2d: 2-90 eLU: 2-91	[8, 128, 16, 16] [8, 128, 16, 16] [8, 128, 16, 16] [8, 128, 16, 16]	256 1  1
	Bottleneck: 1-19	[8, 128, 16, 16]	
	quential: 2-92 └─Conv2d: 3-163	[8, 32, 16, 16] [8, 32, 16, 16]	
4,096	DatchNorm2d, 2 164	[0 22 16 16]	6.4
Se	└─BatchNorm2d: 3-164 └─PReLU: 3-165 quential: 2-93 └─Conv2d: 3-166	[8, 32, 16, 16] [8, 32, 16, 16] [8, 32, 16, 16] [8, 32, 16, 16]	64 1 
9,216			
	└─BatchNorm2d: 3-167 └─PReLU: 3-168	[8, 32, 16, 16] [8, 32, 16, 16]	64 1

```
[8, 128, 16, 16]
      -Seguential: 2-94
           └─Conv2d: 3-169
                                             [8, 128, 16, 16]
4,096
           └─BatchNorm2d: 3-170
                                             [8, 128, 16, 16]
                                                                         256
           └-PReLU: 3-171
                                            [8, 128, 16, 16]
                                                                         1
                                            [8, 128, 16, 16]
       Dropout2d: 2-95
     └─PReLU: 2-96
                                            [8, 128, 16, 16]
                                                                         1
 -RegularBottleneck: 1-20
                                             [8, 128, 16, 16]
                                                                         - -
     └─Sequential: 2-97
                                             [8, 32, 16, 16]
           └─Conv2d: 3-172
                                             [8, 32, 16, 16]
4,096
           └─BatchNorm2d: 3-173
                                             [8, 32, 16, 16]
                                                                         64
           └─PReLU: 3-174
                                            [8, 32, 16, 16]
                                                                         1
       -Sequential: 2-98
                                             [8, 32, 16, 16]
                                                                         - -
           └─Conv2d: 3-175
                                            [8, 32, 16, 16]
9,216
           └─BatchNorm2d: 3-176
                                            [8, 32, 16, 16]
                                                                         64
           └─PReLU: 3-177
                                             [8, 32, 16, 16]
                                                                         1
       Sequential: 2-99
                                            [8, 128, 16, 16]
                                                                         - -
                                             [8, 128, 16, 16]
           └Conv2d: 3-178
4,096
           └─BatchNorm2d: 3-179
                                             [8, 128, 16, 16]
                                                                         256
           └-PReLU: 3-180
                                                                         1
                                            [8, 128, 16, 16]
                                            [8, 128, 16, 16]
       -Dropout2d: 2-100
                                                                         - -
                                            [8, 128, 16, 16]
     └─PReLU: 2-101
                                                                         1
                                            [8, 128, 16, 16]
 -RegularBottleneck: 1-21
                                                                         - -
                                            [8, 32, 16, 16]
     └─Sequential: 2-102
           └─Conv2d: 3-181
                                             [8, 32, 16, 16]
4,096
                                             [8, 32, 16, 16]
                                                                         64
           └─BatchNorm2d: 3-182
           └-PReLU: 3-183
                                             [8, 32, 16, 16]
                                                                         1
       Sequential: 2-103
                                             [8, 32, 16, 16]
           └─Conv2d: 3-184
                                             [8, 32, 16, 16]
9,216
                                             [8, 32, 16, 16]
                                                                         64
           └─BatchNorm2d: 3-185
           └─PReLU: 3-186
                                            [8, 32, 16, 16]
                                                                         1
       -Sequential: 2-104
                                             [8, 128, 16, 16]
           └─Conv2d: 3-187
                                             [8, 128, 16, 16]
4,096
                                                                         256
           └─BatchNorm2d: 3-188
                                             [8, 128, 16, 16]
           └─PReLU: 3-189
                                             [8, 128, 16, 16]
                                                                         1
       -Dropout2d: 2-105
                                             [8, 128, 16, 16]
                                                                         - -
     └─PReLU: 2-106
                                             [8, 128, 16, 16]
                                                                         1
                                            [8, 128, 16, 16]
 -RegularBottleneck: 1-22
                                                                         - -
     └─Sequential: 2-107
                                             [8, 32, 16, 16]
                                                                         - -
                                             [8, 32, 16, 16]
           └─Conv2d: 3-190
4,096
           └─BatchNorm2d: 3-191
                                            [8, 32, 16, 16]
                                                                         64
           └-PReLU: 3-192
                                             [8, 32, 16, 16]
                                                                         1
```

│ └─Sequential: 2-108	[8, 32, 16, 16]	
	[8, 32, 16, 16]	
5,120		
□ □ BatchNorm2d: 3-194	[8, 32, 16, 16]	64
☐—PReLU: 3-195	[8, 32, 16, 16]	1
	[8, 32, 16, 16]	
	[8, 32, 16, 16]	64
⊢PReLU: 3-198	[8, 32, 16, 16]	1
Sequential: 2-109	[8, 128, 16, 16]	
│	[8, 128, 16, 16]	
4,096		
□ □ BatchNorm2d: 3-200	[8, 128, 16, 16]	256
☐ ☐ PReLU: 3-201	[8, 128, 16, 16]	1
└─Dropout2d: 2-110 └─PReLU: 2-111	[8, 128, 16, 16] [8, 128, 16, 16]	1
RegularBottleneck: 1-23	[8, 128, 16, 16]	
Sequential: 2-112	[8, 32, 16, 16]	
Conv2d: 3-202	[8, 32, 16, 16]	
4,096	- , , , -	
│ │ │ │ │ BatchNorm2d: 3-203	[8, 32, 16, 16]	64
PReLU: 3-204	[8, 32, 16, 16]	1
└─Sequential: 2-113	[8, 32, 16, 16]	
	[8, 32, 16, 16]	
9,216	[8, 32, 16, 16]	64
□ PReLU: 3-207	[8, 32, 16, 16]	1
Sequential: 2-114	[8, 128, 16, 16]	
│	[8, 128, 16, 16]	
4,096		
□ □ BatchNorm2d: 3-209	[8, 128, 16, 16]	256
☐ PReLU: 3-210	[8, 128, 16, 16]	1
└─Dropout2d: 2-115 └─PReLU: 2-116	[8, 128, 16, 16]	1
—UpsamplingBottleneck: 1-24	[8, 128, 16, 16] [8, 64, 32, 32]	
Sequential: 2-117	[8, 64, 16, 16]	
	[8, 64, 16, 16]	
8,192		
│ │ │ │ │ BatchNorm2d: 3-212	[8, 64, 16, 16]	128
☐ MaxUnpool2d: 2-118	[8, 64, 32, 32]	
└─Sequential: 2-119	[8, 32, 16, 16]	
	[8, 32, 16, 16]	
	[8, 32, 16, 16]	64
ReLU: 3-215	[8, 32, 16, 16]	
ConvTranspose2d: 2-120	[8, 32, 32, 32]	
4,096		
☐BatchNorm2d: 2-121	[8, 32, 32, 32]	64
	[8, 32, 32, 32]	

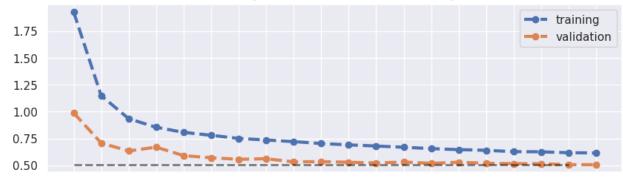
```
[8, 64, 32, 32]
       -Sequential: 2-123
           └Conv2d: 3-216
                                            [8, 64, 32, 32]
2,048
           └─BatchNorm2d: 3-217
                                            [8, 64, 32, 32]
                                                                        128
       Dropout2d: 2-124
                                            [8, 64, 32, 32]
     └─ReLU: 2-125
                                            [8, 64, 32, 32]
 -RegularBottleneck: 1-25
                                            [8, 64, 32, 32]
                                            [8, 16, 32, 32]
      └─Sequential: 2-126
           └─Conv2d: 3-218
                                            [8, 16, 32, 32]
1,024
           └─BatchNorm2d: 3-219
                                            [8, 16, 32, 32]
                                                                        32
           └─ReLU: 3-220
                                            [8, 16, 32, 32]
                                                                         - -
                                            [8, 16, 32, 32]
       Sequential: 2-127
           └─Conv2d: 3-221
                                            [8, 16, 32, 32]
2,304
           └─BatchNorm2d: 3-222
                                            [8, 16, 32, 32]
                                                                        32
           └ReLU: 3-223
                                            [8, 16, 32, 32]
                                                                         - -
                                            [8, 64, 32, 32]
       Sequential: 2-128
           └Conv2d: 3-224
                                            [8, 64, 32, 32]
1,024
           └─BatchNorm2d: 3-225
                                            [8, 64, 32, 32]
                                                                        128
           └ReLU: 3-226
                                            [8, 64, 32, 32]
       -Dropout2d: 2-129
                                            [8, 64, 32, 32]
     └─ReLU: 2-130
                                            [8, 64, 32, 32]
                                            [8, 64, 32, 32]
 -RegularBottleneck: 1-26
      └─Sequential: 2-131
                                            [8, 16, 32, 32]
           └─Conv2d: 3-227
                                            [8, 16, 32, 32]
1,024
           └─BatchNorm2d: 3-228
                                            [8, 16, 32, 32]
                                                                        32
           └ReLU: 3-229
                                            [8, 16, 32, 32]
       Sequential: 2-132
                                            [8, 16, 32, 32]
                                            [8, 16, 32, 32]
           └─Conv2d: 3-230
2,304
           └─BatchNorm2d: 3-231
                                            [8, 16, 32, 32]
                                                                        32
           └ReLU: 3-232
                                            [8, 16, 32, 32]
                                                                         - -
       Sequential: 2-133
                                            [8, 64, 32, 32]
           └─Conv2d: 3-233
                                            [8, 64, 32, 32]
1,024
           └─BatchNorm2d: 3-234
                                            [8, 64, 32, 32]
                                                                        128
           └ReLU: 3-235
                                            [8, 64, 32, 32]
                                            [8, 64, 32, 32]
       -Dropout2d: 2-134
     └─ReLU: 2-135
                                            [8, 64, 32, 32]
 -UpsamplingBottleneck: 1-27
                                            [8, 16, 64, 64]
                                                                         - -
                                            [8, 16, 32, 32]
      └─Sequential: 2-136
           └─Conv2d: 3-236
                                            [8, 16, 32, 32]
1,024
           └─BatchNorm2d: 3-237
                                            [8, 16, 32, 32]
                                                                        32
                                            [8, 16, 64, 64]
       -MaxUnpool2d: 2-137
                                                                         - -
     └─Sequential: 2-138
                                            [8, 16, 32, 32]
                                                                         - -
```

```
└Conv2d: 3-238
                                           [8, 16, 32, 32]
1,024
           └─BatchNorm2d: 3-239
                                           [8, 16, 32, 32]
                                                                       32
           └─ReLU: 3-240
                                           [8, 16, 32, 32]
       -ConvTranspose2d: 2-139
                                           [8, 16, 64, 64]
1,024
      └─BatchNorm2d: 2-140
                                           [8, 16, 64, 64]
                                                                       32
     └─ReLU: 2-141
                                           [8, 16, 64, 64]
                                                                       - -
     └─Sequential: 2-142
                                           [8, 16, 64, 64]
                                                                       - -
           └─Conv2d: 3-241
                                           [8, 16, 64, 64]
                                                                       256
                                           [8, 16, 64, 64]
           └─BatchNorm2d: 3-242
                                                                       32
       -Dropout2d: 2-143
                                           [8, 16, 64, 64]
                                                                       - -
     └─ReLU: 2-144
                                           [8, 16, 64, 64]
                                           [8, 16, 64, 64]
 -RegularBottleneck: 1-28
                                                                       - -
      └─Sequential: 2-145
                                           [8, 4, 64, 64]
                                                                       - -
           └─Conv2d: 3-243
                                           [8, 4, 64, 64]
                                                                       64
           └─BatchNorm2d: 3-244
                                           [8, 4, 64, 64]
                                                                       8
           └─ReLU: 3-245
                                           [8, 4, 64, 64]
      -Sequential: 2-146
                                           [8, 4, 64, 64]
           └─Conv2d: 3-246
                                           [8, 4, 64, 64]
                                                                       144
           └─BatchNorm2d: 3-247
                                           [8, 4, 64, 64]
                                                                       8
           └─ReLU: 3-248
                                           [8, 4, 64, 64]
                                                                       - -
      -Sequential: 2-147
                                           [8, 16, 64, 64]
                                                                       - -
           └─Conv2d: 3-249
                                           [8, 16, 64, 64]
                                                                       64
                                           [8, 16, 64, 64]
           └─BatchNorm2d: 3-250
                                                                       32
          □ReLU: 3-251
                                           [8, 16, 64, 64]
       -Dropout2d: 2-148
                                           [8, 16, 64, 64]
     └─ReLU: 2-149
                                           [8, 16, 64, 64]
                                           [8, 8, 128, 128]
├ConvTranspose2d: 1-29
1,152
Total params: 350,076
Trainable params: 350,076
Non-trainable params: 0
Total mult-adds (G): 1.12
Input size (MB): 1.57
Forward/backward pass size (MB): 406.06
Params size (MB): 1.40
Estimated Total Size (MB): 409.03
```

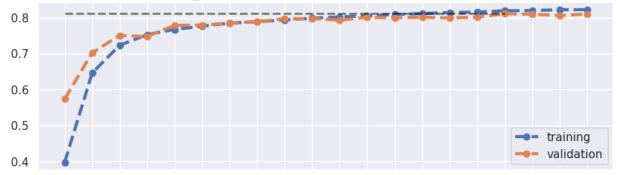
#### **Training**

```
device = "cuda"
model = ENet(number_of_classes)
```

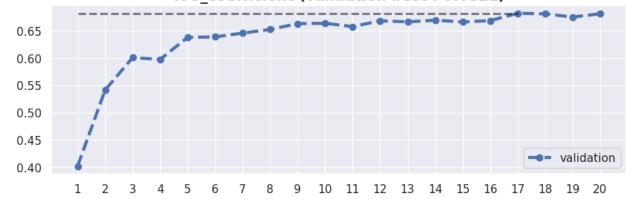




#### DICE\_coefficient (validation best : 0.8105)

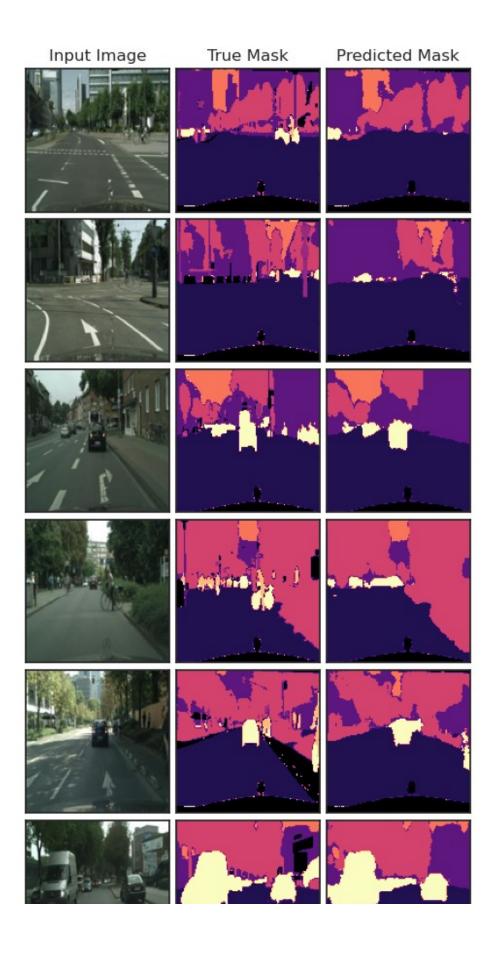


IoU coefficient (validation best: 0.6821)



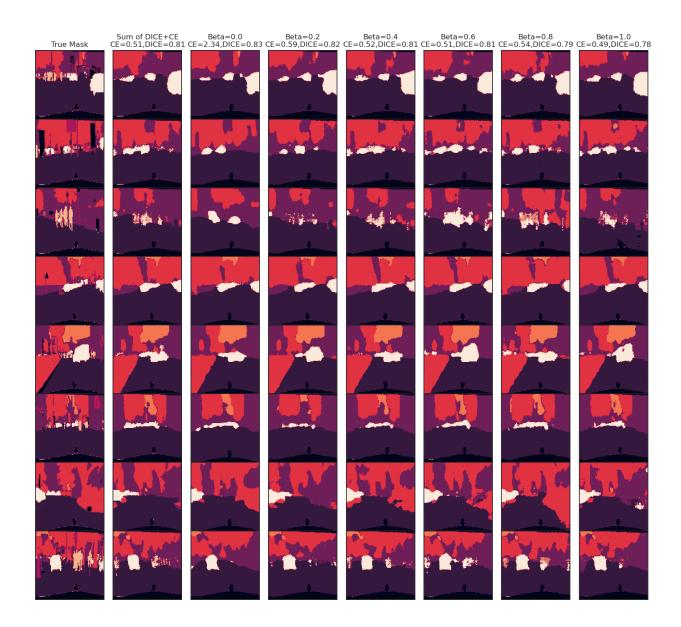
## Inference (Show results)

```
batch = next(iter(val_dataloader))
predictions = model(batch[0].to(device))
show_inference(batch, predictions)
```



# Comparison of how loss affects the prediction (CE and DICE)

```
# LONG COMPUTATION !
summaries, preds = get summaries preds for comparison(cfg.DELTA BETA,
ENet, {"num classes" : number of classes},
model mixed=model, summary mixed=ENet training val summary,
epochs=cfg.CE VS DICE EVAL EPOCHS)
plot summaries preds(summaries, preds)
[605s] Model with Beta=0.0 finished with training CE :
1.979523107704996, training DICE: 0.8488202039333953, validation CE:
2.3374594383239744, Validation DICE: 0.8338890442848206, Validation
IOU: 0.7165566878318786
[606s] Model with Beta=0.2 finished with training CE:
0.522042145508678, training DICE: 0.8365040243172845, validation CE:
0.5926298832893372, Validation DICE: 0.8225647625923157, Validation
IOU: 0.6992501564025879
[608s] Model with Beta=0.4 finished with training CE:
0.46669727273348, training DICE: 0.8211252089708793, validation CE:
0.5223665194511413, Validation DICE: 0.8136104822158814, Validation
IOU: 0.6866537160873413
[619s] Model with Beta=0.6000000000000001 finished with training CE:
0.4562842844313934, training DICE: 0.8075732401639474, validation
CE: 0.5111929860115051, Validation DICE: 0.8069924721717835,
Validation IOU : 0.6776913461685181
[619s] Model with Beta=0.8 finished with training CE:
0.444800979289688, training DICE: 0.7995015824141622, validation CE:
0.5431346793174744, Validation DICE: 0.7852451000213623, Validation
IOU: 0.6472662196159363
[613s] Model with Beta=1.0 finished with training CE:
0.4449858499575062, training DICE: 0.790660586617574, validation CE:
0.4923299667835236, Validation DICE: 0.7807266063690186, Validation
IOU: 0.6409798121452331
```



#### **ESNET**

## https://arxiv.org/pdf/1906.09826.pdf

## Description

The research on finding fast and well performing architectures for semantic segmentation is very active and there a lot of different architectures. A more recent than ENet and UNet, from 2019 is ESNet from paper "ESNet: An Efficient Symmetric Network for Real-time Semantic Segmentation" which is an extension of previously discussed ENET. The architecture is quite simillar to ENet: simillar encoder-decoder architecture, encoder-decoder architecture, presence of techniques such as factorized convolutions.

• The core is 4 blocks: **Downscaling block** (very simillar to ENet), **Upsampling block** (very simillar to ENet) and 2 new blocks: **Factorized convolution unit** (FCU) and **parallel factorized convolution unit** (PFCU), which is a new strategy in designs of residual layers, but with lower computational complexity [1] (by doing 1x1 convolutions and factorized convolutions with 1D kernels).

[1] https://arxiv.org/pdf/1906.09826.pdf

#### Architecture

```
class Downscale (nn.Module):
    def __init__(self, in_channel, out_channel):
        super(Downscale, self). init ()
        self.conv = nn.Conv2d(in channel, out channel-in channel, (3,
3), stride=2, padding=1, bias=True)
        self.pool = nn.MaxPool2d(2, stride=2)
        self.bn = nn.BatchNorm2d(out channel, eps=1e-3)
        self.relu = nn.ReLU(inplace=True)
    def forward(self, input):
        x1 = self.pool(input)
        x2 = self.conv(input)
        diffY = x2.size()[2] - x1.size()[2]
        diffX = x2.size()[3] - x1.size()[3]
        x1 = F.pad(x1, [diffX // 2, diffX - diffX // 2,
                        diffY // 2, diffY - diffY // 2])
        output = torch.cat([x2, x1], 1)
        output = self.bn(output)
        output = self.relu(output)
        return output
class FCU(nn.Module):
    def init (self, chann, kernel size, dropprob, dilated,
bias=True):
        super(FCU, self).__init__()
        padding = int((kernel size-1)//2) * dilated
        self.conv3x1_1 = nn.Conv2d(chann, chann, (kernel_size,1),
stride=1, padding=(int((kernel_size-1)//2)*1,0), bias=bias)
        self.conv1x3 1 = nn.Conv2d(chann, chann, (1,kernel_size),
stride=1, padding=(0,int((kernel size-1)//2)*1), bias=bias)
        self.bn1 = nn.BatchNorm2\overline{d}(chann, eps=1e-03)
        self.conv3x1 2 = nn.Conv2d(chann, chann, (kernel size, 1),
stride=1, padding=(padding,0), bias=bias, dilation = (dilated,1))
        self.conv1x3 2 = nn.Conv2d(chann, chann, (1,kernel_size),
stride=1, padding=(0, padding), bias=bias, dilation = (1, dilated))
        self.bn2 = nn.BatchNorm2d(chann, eps=1e-03)
```

```
self.relu = nn.ReLU(inplace = True)
        self.dropout = nn.Dropout2d(dropprob)
    def forward(self, input):
        residual = input
        output = self.conv3x1 1(input)
        output = self.relu(output)
        output = self.conv1x3 1(output)
        output = self.bn1(output)
        output = self.relu(output)
        output = self.conv3x1 2(output)
        output = self.relu(output)
        output = self.conv1x3 2(output)
        output = self.bn2(output)
        if (self.dropout.p != 0):
            output = self.dropout(output)
        output = self.relu(residual+output)
        return output
class PFCU(nn.Module):
    def init (self, chann, bias=True):
        super(PFCU, self). init ()
        self.conv3x1_1 = nn.Conv2d(chann, chann, (3,1), stride=1,
padding=(1,0), bias=bias)
        self.conv1x3 1 = nn.Conv2d(chann, chann, (1,3), stride=1,
padding=(0,1), bias=bias)
        self.bn1 = nn.BatchNorm2d(chann, eps=1e-03)
        self.conv3x1 22 = nn.Conv2d(chann, chann, (3,1), stride=1,
padding=(2,0), bias=bias, dilation = (2,1))
        self.conv1x3_22 = nn.Conv2d(chann, chann, (1,3), stride=1,
padding=(0,2), bias=bias, dilation = (1,2))
        self.conv3x1 25 = nn.Conv2d(chann, chann, (3,1), stride=1,
padding=(5,0), bias=bias, dilation = (5,1))
        self.conv1x3_25 = nn.Conv2d(chann, chann, (1,3), stride=1,
padding=(0,5), bias=bias, dilation = (1,5))
        self.conv3x1 29 = nn.Conv2d(chann, chann, (3,1), stride=1,
padding=(9,0), bias=bias, dilation = (9,1))
        self.conv1x3 29 = nn.Conv2d(chann, chann, (1,3), stride=1,
padding=(0,9), bias=bias, dilation = (1,9))
        self.bn2 = nn.BatchNorm2d(chann, eps=1e-03)
        self.relu = nn.ReLU(inplace=True)
        self.dropout = nn.Dropout2d(0.3)
```

```
def forward(self, input):
        residual = input
        output = self.conv3x1 1(input)
        output = F.relu(output)
        output = self.conv1x3 1(output)
        output = self.bn1(output)
        output = F.relu(output)
        output2 = self.conv3x1 22(output)
        output2 = F.relu(output2)
        output2 = self.conv1x3 22(output2)
        output2 = self.bn2(output2)
        if (self.dropout.p != 0):
            output2 = self.dropout(output2)
        output5 = self.conv3x1 25(output)
        output5 = F.relu(output5)
        output5 = self.conv1x3 25(output5)
        output5 = self.bn2(output5)
        if (self.dropout.p != 0):
            output5 = self.dropout(output5)
        output9 = self.conv3x1 29(output)
        output9 = F.relu(output9)
        output9 = self.conv1x3 29(output9)
        output9 = self.bn2(output9)
        if (self.dropout.p != 0):
            output9 = self.dropout(output9)
        output = self.relu(residual + output2 + output5 + output9)
        return output
class Encoder(nn.Module):
    def init (self):
        super(). init ()
        self.downscale1 = Downscale(3,16)
        self.FCU1 1 = FCU(16, 3, 0.03, 1)
        self.FCU1 2 = FCU(16, 3, 0.03, 1)
        self.FCU1 3 = FCU(16, 3, 0.03, 1)
        self.downscale2 = Downscale(16, 64)
        self.FCU2 1 = FCU(64, 5, 0.03, 1)
        self.FCU2 2 = FCU(64, 5, 0.03, 1)
        self.downscale3 = Downscale(64,128)
        self.PFCU1 1 = PFCU(chann=128)
```

```
self.PFCU1 2 = PFCU(chann=128)
        self.PFCU1 3 = PFCU(chann=128)
    def forward(self, x):
        x = self.downscale1(x)
        x = self.FCU1 1(x)
        x = self.FCU1 2(x)
        x = self.FCU1 3(x)
        x = self.downscale2(x)
        x = self.FCU2 1(x)
        x = self.FCU2 2(x)
        x = self.downscale3(x)
        x = self.PFCU1 1(x)
        x = self.PFCU1 2(x)
        x = self.PFCU1 3(x)
        return x
class UpsamplerBlock(nn.Module):
    def __init__(self, ninput, noutput):
        super().__init__()
        self.conv = nn.ConvTranspose2d(ninput, noutput, 3, stride=2,
padding=1, output padding=1, bias=True)
        self.bn = nn.BatchNorm2d(noutput, eps=1e-3)
        self.relu = nn.ReLU(inplace=True)
    def forward(self, input):
        output = self.conv(input)
        output = self.bn(output)
        output = self.relu(output)
        return output
class Decoder(nn.Module):
    def init (self, num classes):
        super(). init ()
        self.layers = nn.ModuleList()
        self.upsample1 = UpsamplerBlock(128,64)
        self.FCU1 1 = FCU(64, 5, 0, 1)
        self.FCU1_2 = FCU(64, 5, 0, 1)
        self.upsample2 = UpsamplerBlock(64,16)
        self.FCU2 1 = FCU(16, 3, 0, 1)
        self.FCU2^{-}2 = FCU(16, 3, 0, 1)
```

```
self.pred = nn.ConvTranspose2d( 16, num_classes, 2, stride=2,
padding=0, output padding=0, bias=True)
    def forward(self, x):
        x = self.upsample1(x)
        x = self.FCU1 1(x)
        x = self.FCU1 2(x)
        x = self.upsample2(x)
        x = self.FCU2_1(x)
        x = self.FCU2 2(x)
        x = self.pred(x)
        return x
# ESNet
class ESNet(nn.Module):
    def init (self, num classes, giveEncoder=None):
        # use encoder to pass pretrained encoder
        super(). init ()
        self.encoder = Encoder()
        self.decoder = Decoder(num classes)
    def forward(self, rgb input):
        output = self.encoder(rgb input)
        return self.decoder(output)
number of classes = len(set(name to category.values()))
print(ESNet(number of classes)(eval batch data[0]).shape)
summary(ESNet(number of classes), input data=eval batch data[0])
torch.Size([8, 8, 128, 128])
Layer (type:depth-idx)
                                          Output Shape
Param #
                                          [8, 8, 128, 128]
ESNet
                                          [8, 128, 16, 16]
 -Encoder: 1-1
                                          [8, 16, 64, 64]
     └─Downscale: 2-1
          └─MaxPool2d: 3-1
                                          [8, 3, 64, 64]
                                                                     - -
          └─Conv2d: 3-2
                                          [8, 13, 64, 64]
                                                                     364
          └─BatchNorm2d: 3-3
                                          [8, 16, 64, 64]
                                                                     32
          └─ReLU: 3-4
                                          [8, 16, 64, 64]
                                                                     - -
                                          [8, 16, 64, 64]
      -FCU: 2-2
                                                                     - -
          └─Conv2d: 3-5
                                          [8, 16, 64, 64]
                                                                     784
          └─ReLU: 3-6
                                          [8, 16, 64, 64]
                                                                     - -
```

```
└─Conv2d: 3-7
                                             [8, 16, 64, 64]
                                                                         784
           └─BatchNorm2d: 3-8
                                             [8, 16, 64, 64]
                                                                         32
           └ReLU: 3-9
                                             [8, 16, 64, 64]
           └─Conv2d: 3-10
                                            [8, 16, 64, 64]
                                                                         784
           └─ReLU: 3-11
                                            [8, 16, 64, 64]
                                                                         - -
                                            [8, 16, 64, 64]
                                                                         784
           └─Conv2d: 3-12
           └─BatchNorm2d: 3-13
                                            [8, 16, 64, 64]
                                                                         32
           └─Dropout2d: 3-14
                                            [8, 16, 64, 64]
           └ReLU: 3-15
                                            [8, 16, 64, 64]
                                                                         - -
       -FCU: 2-3
                                            [8, 16, 64, 64]
                                                                         - -
           └─Conv2d: 3-16
                                             [8, 16, 64, 64]
                                                                         784
           └─ReLU: 3-17
                                             [8, 16, 64, 64]
                                            [8, 16, 64, 64]
           └─Conv2d: 3-18
                                                                         784
           └─BatchNorm2d: 3-19
                                             [8, 16, 64, 64]
                                                                         32
           └ReLU: 3-20
                                             [8, 16, 64, 64]
                                                                         - -
           └─Conv2d: 3-21
                                            [8, 16, 64, 64]
                                                                         784
           └─ReLU: 3-22
                                            [8, 16, 64, 64]
                                                                         - -
                                            [8, 16, 64, 64]
           └─Conv2d: 3-23
                                                                         784
           └─BatchNorm2d: 3-24
                                            [8, 16, 64, 64]
                                                                         32
                                            [8, 16, 64, 64]
           └─Dropout2d: 3-25
           └ReLU: 3-26
                                            [8, 16, 64, 64]
                                                                         - -
       -FCU: 2-4
                                            [8, 16, 64, 64]
                                                                         - -
           └─Conv2d: 3-27
                                            [8, 16, 64, 64]
                                                                         784
           └ReLU: 3-28
                                            [8, 16, 64, 64]
                                                                         - -
                                             [8, 16, 64, 64]
           └─Conv2d: 3-29
                                                                         784
           └BatchNorm2d: 3-30
                                             [8, 16, 64, 64]
                                                                         32
                                            [8, 16, 64, 64]
           └─ReLU: 3-31
           └─Conv2d: 3-32
                                             [8, 16, 64, 64]
                                                                         784
           └─ReLU: 3-33
                                             [8, 16, 64, 64]
                                                                         - -
                                             [8, 16, 64, 64]
           └─Conv2d: 3-34
                                                                         784
           └─BatchNorm2d: 3-35
                                             [8, 16, 64, 64]
                                                                         32
                                            [8, 16, 64, 64]
           └─Dropout2d: 3-36
           └ReLU: 3-37
                                            [8, 16, 64, 64]
                                            [8, 64, 32, 32]
       -Downscale: 2-5
                                            [8, 16, 32, 32]
           └─MaxPool2d: 3-38
                                            [8, 48, 32, 32]
           └─Conv2d: 3-39
6,960
           └─BatchNorm2d: 3-40
                                             [8, 64, 32, 32]
                                                                         128
           └─ReLU: 3-41
                                             [8, 64, 32, 32]
                                             [8, 64, 32, 32]
       FCU: 2-6
                                            [8, 64, 32, 32]
           └─Conv2d: 3-42
20,544
                                            [8, 64, 32, 32]
           └─ReLU: 3-43
           └─Conv2d: 3-44
                                             [8, 64, 32, 32]
20,544
                                             [8, 64, 32, 32]
           └─BatchNorm2d: 3-45
                                                                         128
           └─ReLU: 3-46
                                            [8, 64, 32, 32]
                                             [8, 64, 32, 32]
           └─Conv2d: 3-47
20,544
```

	└─ReLU: 3-48 └─Conv2d: 3-49	[8, 64, 32, 32] [8, 64, 32, 32]	
FCU	└─BatchNorm2d: 3-50 └─Dropout2d: 3-51 └─ReLU: 3-52 : 2-7	[8, 64, 32, 32] [8, 64, 32, 32] [8, 64, 32, 32] [8, 64, 32, 32]	128  
	└─Conv2d: 3-53	[8, 64, 32, 32]	
	└─ReLU: 3-54 └─Conv2d: 3-55	[8, 64, 32, 32] [8, 64, 32, 32]	
	└─BatchNorm2d: 3-56 └─ReLU: 3-57 └─Conv2d: 3-58	[8, 64, 32, 32] [8, 64, 32, 32] [8, 64, 32, 32]	128
	└─ReLU: 3-59 └─Conv2d: 3-60	[8, 64, 32, 32] [8, 64, 32, 32]	
20,544			
	└─BatchNorm2d: 3-61 └─Dropout2d: 3-62 └─ReLU: 3-63	[8, 64, 32, 32] [8, 64, 32, 32] [8, 64, 32, 32]	128 
	nscale: 2-8 └MaxPool2d: 3-64 └Conv2d: 3-65	[8, 128, 16, 16] [8, 64, 16, 16] [8, 64, 16, 16]	
L-PFCI	└─BatchNorm2d: 3-66 └─ReLU: 3-67 U: 2-9 └─Conv2d: 3-68	[8, 128, 16, 16] [8, 128, 16, 16] [8, 128, 16, 16] [8, 128, 16, 16]	256  
	└─Conv2d: 3-69	[8, 128, 16, 16]	
	└─BatchNorm2d: 3-70 └─Conv2d: 3-71	[8, 128, 16, 16] [8, 128, 16, 16]	256
49,280	└─Conv2d: 3-72	[8, 128, 16, 16]	
49,280	─BatchNorm2d: 3-73 └─Dropout2d: 3-74	[8, 128, 16, 16] [8, 128, 16, 16]	256
	└─Conv2d: 3-75	[8, 128, 16, 16]	
	└─Conv2d: 3-76	[8, 128, 16, 16]	
	└─BatchNorm2d: 3-77	[8, 128, 16, 16]	
(recursive	└─Dropout2d: 3-78	[8, 128, 16, 16]	
 49,280	└─Conv2d: 3-79	[8, 128, 16, 16]	

```
└─Conv2d: 3-80
                                            [8, 128, 16, 16]
49,280
           └─BatchNorm2d: 3-81
                                            [8, 128, 16, 16]
(recursive)
           └─Dropout2d: 3-82
                                            [8, 128, 16, 16]
           └─ReLU: 3-83
                                            [8, 128, 16, 16]
       -PFCU: 2-10
                                            [8, 128, 16, 16]
           └─Conv2d: 3-84
                                            [8, 128, 16, 16]
49,280
           └─Conv2d: 3-85
                                            [8, 128, 16, 16]
49,280
           └─BatchNorm2d: 3-86
                                            [8, 128, 16, 16]
                                                                        256
           └─Conv2d: 3-87
                                            [8, 128, 16, 16]
49,280
           └─Conv2d: 3-88
                                            [8, 128, 16, 16]
49,280
           └─BatchNorm2d: 3-89
                                            [8, 128, 16, 16]
                                                                        256
           └─Dropout2d: 3-90
                                            [8, 128, 16, 16]
           └Conv2d: 3-91
                                            [8, 128, 16, 16]
49,280
           └─Conv2d: 3-92
                                            [8, 128, 16, 16]
49,280
           └─BatchNorm2d: 3-93
                                            [8, 128, 16, 16]
(recursive)
            -Dropout2d: 3-94
                                            [8, 128, 16, 16]
           └─Conv2d: 3-95
                                            [8, 128, 16, 16]
49,280
           └─Conv2d: 3-96
                                            [8, 128, 16, 16]
49,280
           └─BatchNorm2d: 3-97
                                            [8, 128, 16, 16]
(recursive)
           └─Dropout2d: 3-98
                                            [8, 128, 16, 16]
           └─ReLU: 3-99
                                            [8, 128, 16, 16]
                                            [8, 128, 16, 16]
       PFCU: 2-11
           └─Conv2d: 3-100
                                            [8, 128, 16, 16]
49,280
           └─Conv2d: 3-101
                                            [8, 128, 16, 16]
49,280
           └─BatchNorm2d: 3-102
                                            [8, 128, 16, 16]
                                                                        256
           └─Conv2d: 3-103
                                            [8, 128, 16, 16]
49,280
                                            [8, 128, 16, 16]
           └─Conv2d: 3-104
49,280
           └─BatchNorm2d: 3-105
                                            [8, 128, 16, 16]
                                                                        256
           └─Dropout2d: 3-106
                                            [8, 128, 16, 16]
           └─Conv2d: 3-107
                                            [8, 128, 16, 16]
49,280
           └-Conv2d: 3-108
                                            [8, 128, 16, 16]
49,280
```

```
└─BatchNorm2d: 3-109
                                            [8, 128, 16, 16]
(recursive)
           └─Dropout2d: 3-110
                                            [8, 128, 16, 16]
           └─Conv2d: 3-111
                                            [8, 128, 16, 16]
49,280
           └─Conv2d: 3-112
                                            [8, 128, 16, 16]
49,280
           └─BatchNorm2d: 3-113
                                            [8, 128, 16, 16]
(recursive)
           └─Dropout2d: 3-114
                                            [8, 128, 16, 16]
           └─ReLU: 3-115
                                            [8, 128, 16, 16]
 -Decoder: 1-2
                                            [8, 8, 128, 128]
                                            [8, 64, 32, 32]
     └─UpsamplerBlock: 2-12
                                            [8, 64, 32, 32]
           └ConvTranspose2d: 3-116
73,792
                                            [8, 64, 32, 32]
           └─BatchNorm2d: 3-117
                                                                        128
           └─ReLU: 3-118
                                            [8, 64, 32, 32]
                                            [8, 64, 32, 32]
       -FCU: 2-13
           └─Conv2d: 3-119
                                            [8, 64, 32, 32]
20,544
           └─ReLU: 3-120
                                            [8, 64, 32, 32]
           └─Conv2d: 3-121
                                            [8, 64, 32, 32]
20,544
           └─BatchNorm2d: 3-122
                                            [8, 64, 32, 32]
                                                                        128
           └─ReLU: 3-123
                                            [8, 64, 32, 32]
           └─Conv2d: 3-124
                                            [8, 64, 32, 32]
20,544
           └ReLU: 3-125
                                            [8, 64, 32, 32]
           └Conv2d: 3-126
                                            [8, 64, 32, 32]
20,544
                                            [8, 64, 32, 32]
           └─BatchNorm2d: 3-127
                                                                        128
           └─ReLU: 3-128
                                            [8, 64, 32, 32]
       -FCU: 2-14
                                            [8, 64, 32, 32]
                                            [8, 64, 32, 32]
           └─Conv2d: 3-129
20,544
           └─ReLU: 3-130
                                            [8, 64, 32, 32]
           └─Conv2d: 3-131
                                            [8, 64, 32, 32]
20,544
           └─BatchNorm2d: 3-132
                                            [8, 64, 32, 32]
                                                                        128
           └─ReLU: 3-133
                                            [8, 64, 32, 32]
                                            [8, 64, 32, 32]
           └Conv2d: 3-134
20,544
           └─ReLU: 3-135
                                            [8, 64, 32, 32]
           └─Conv2d: 3-136
                                            [8, 64, 32, 32]
20,544
           └─BatchNorm2d: 3-137
                                            [8, 64, 32, 32]
                                                                        128
           └─ReLU: 3-138
                                            [8, 64, 32, 32]
                                                                        - -
                                            [8, 16, 64, 64]
       UpsamplerBlock: 2-15
           └─ConvTranspose2d: 3-139
                                            [8, 16, 64, 64]
```

```
9,232
          └─BatchNorm2d: 3-140
                                           [8, 16, 64, 64]
                                                                      32
          └ReLU: 3-141
                                           [8, 16, 64, 64]
                                                                      - -
                                           [8, 16, 64, 64]
       -FCU: 2-16
          └─Conv2d: 3-142
                                           [8, 16, 64, 64]
                                                                      784
                                           [8, 16, 64, 64]
          └─ReLU: 3-143
          └─Conv2d: 3-144
                                           [8, 16, 64, 64]
                                                                      784
          └─BatchNorm2d: 3-145
                                           [8, 16, 64, 64]
                                                                      32
          └─ReLU: 3-146
                                           [8, 16, 64, 64]
                                                                      _ _
          └─Conv2d: 3-147
                                           [8, 16, 64, 64]
                                                                      784
                                           [8, 16, 64, 64]
          └─ReLU: 3-148
          └─Conv2d: 3-149
                                           [8, 16, 64, 64]
                                                                      784
          └─BatchNorm2d: 3-150
                                           [8, 16, 64, 64]
                                                                      32
          └ReLU: 3-151
                                           [8, 16, 64, 64]
                                                                      - -
       -FCU: 2-17
                                           [8, 16, 64, 64]
                                           [8, 16, 64, 64]
          └─Conv2d: 3-152
                                                                      784
          └ReLU: 3-153
                                           [8, 16, 64, 64]
                                                                      _ _
                                           [8, 16, 64, 64]
          └─Conv2d: 3-154
                                                                      784
          └─BatchNorm2d: 3-155
                                           [8, 16, 64, 64]
                                                                      32
          └─ReLU: 3-156
                                           [8, 16, 64, 64]
          └─Conv2d: 3-157
                                           [8, 16, 64, 64]
                                                                      784
          └─ReLU: 3-158
                                           [8, 16, 64, 64]
                                                                      - -
                                           [8, 16, 64, 64]
          └─Conv2d: 3-159
                                                                      784
          └─BatchNorm2d: 3-160
                                           [8, 16, 64, 64]
                                                                      32
                                          [8, 16, 64, 64]
          └─ReLU: 3-161
                                          [8, 8, 128, 128]
       ConvTranspose2d: 2-18
                                                                      520
Total params: 1,658,356
Trainable params: 1,658,356
Non-trainable params: 0
Total mult-adds (G): 6.75
_____
Input size (MB): 1.57
Forward/backward pass size (MB): 345.24
Params size (MB): 6.63
Estimated Total Size (MB): 353.45
```

### **Training**

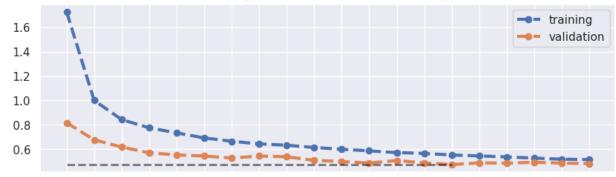
```
device = "cuda"
model = ESNet(number_of_classes)
model = model.to(device)

ESNet_training_val_summary = train_model(model, device, train_dataloader, val_dataloader,
```

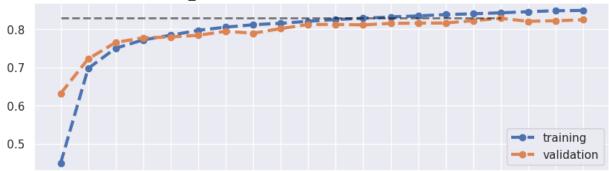
```
lr=3e-4, epochs=20,
update_pb_every_batch=10)

clear_output(wait=True)
plot_losses_coeffs(ESNet_training_val_summary)
```

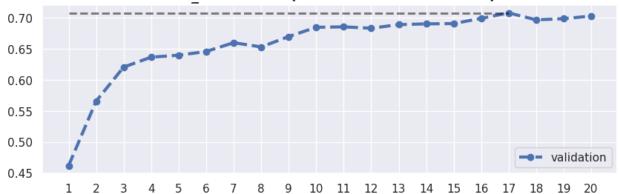
#### loss (validation best: 0.4746)



#### DICE\_coefficient (validation best : 0.8286)



#### IoU coefficient (validation best: 0.7081)



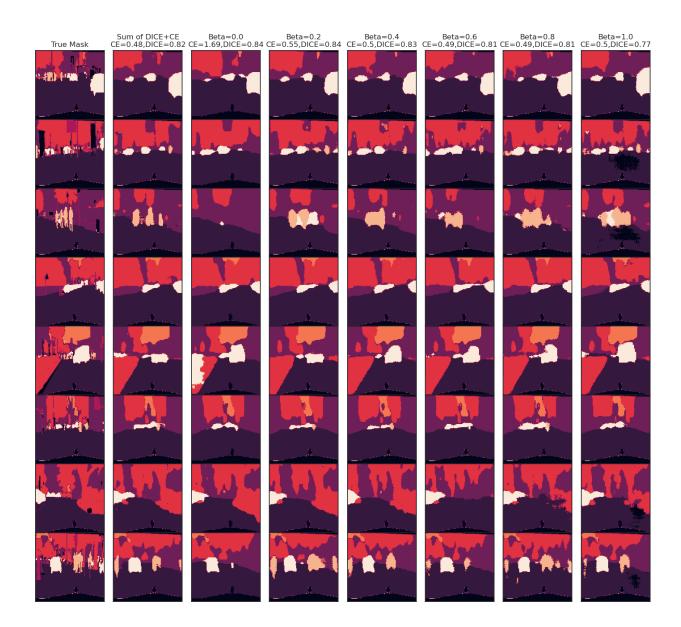
## Inference (Show results)

```
batch = next(iter(val_dataloader))
predictions = model(batch[0].to(device))
show_inference(batch, predictions)
```



# Comparison of how loss affects the prediction (CE and DICE)

```
# LONG COMPUTATION !
summaries, preds = get summaries preds for comparison(0.2, ESNet,
{"num classes" : number of classes},
model mixed=model, summary mixed=ESNet training val summary,
epochs=cfg.CE VS DICE EVAL EPOCHS)
plot summaries preds(summaries, preds)
[425s] Model with Beta=0.0 finished with training CE:
1.4583119362342258, training DICE : 0.8568492243269913, validation
CE: 1.688820016860962, Validation DICE: 0.8398173084259033,
Validation IOU: 0.7250333175659179
[424s] Model with Beta=0.2 finished with training CE:
0.44952147806391995, training DICE: 0.8563047631448056, validation CE
: 0.5510570664405823, Validation DICE : 0.835548825263977, Validation
IOU: 0.7186846170425415
[422s] Model with Beta=0.4 finished with training CE:
0.39764400495200597, training DICE: 0.845405949203908, validation
CE: 0.5035213303565979, Validation DICE: 0.8279288253784179,
Validation IOU: 0.7073951687812805
[425s] Model with Beta=0.6000000000000001 finished with training CE:
0.39587021076378703, training DICE: 0.830356583835698, validation
CE: 0.49121958112716674, Validation DICE: 0.8135785174369812,
Validation IOU : 0.6869305453300476
[426s] Model with Beta=0.8 finished with training CE:
0.37742765956566116, training DICE: 0.8274697994184094, validation CE
: 0.48856550455093384, Validation DICE : 0.8082010660171509,
Validation IOU: 0.6794788584709167
[423s] Model with Beta=1.0 finished with training CE:
0.38733865198968837, training DICE: 0.814956085301247, validation
CE: 0.5006751222610474, Validation DICE: 0.7687566018104554,
Validation IOU: 0.6256794548034668
```



## DeepLabV3

## Description

So far all the networks worked pretty well, despite the fairly small number of parameters (very simillar scores). In order to see how well these networks are performing, I decided to evaluate it against some fairly standard, fairly large architecture, in order to compare the scores. I decided to go with the DeepLabV3, which is quite well performing semantic segmentation network. This network is much larger however because it's pretty standard, saved weights are available from torchvision. Few points about the DeepLabV3:

• the network similarly to ENet and ESNet uses **dilated convolutions** (in paper named atrous convolutions). Dilation enlarges the field-of-view (receptive field) therefore

- enable encoding information at different scales. Dilation here plays I believe the most significant importance: authors argue that it will allow them to work on multi-scale context.
- Second key element is Atrous Spatial Pyramid Pooling (ASPP). Spatial Pyramid Pooling is a technique which allows capturing context at several ranges. Atrous Spatial Pyramid Pooling was introduced in previous version of deeplab (V2). In ASPP we will have in parallel (a bit simillarly to previously explored PFCU) dilated convolutions with different rates applied to the input. After applying these convoultions, the outputs are fused together (by summing them) and returned.
- [1] https://arxiv.org/pdf/1706.05587.pdf [2] https://arxiv.org/abs/1606.00915v2

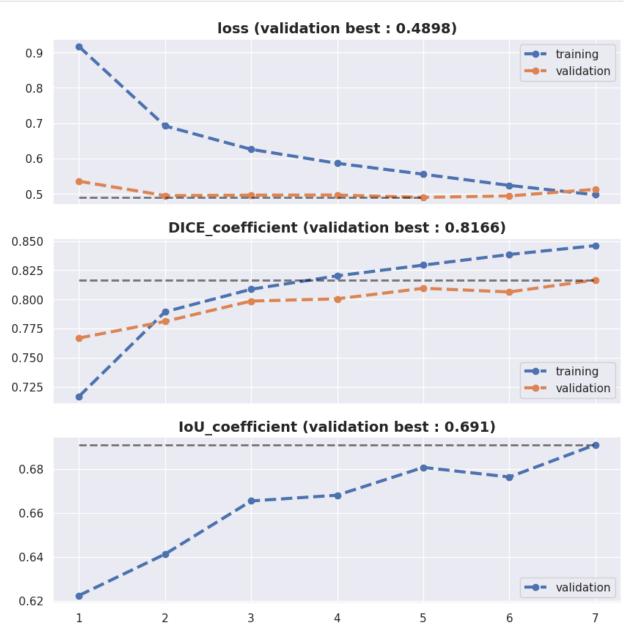
#### Architecture

```
from torchvision.models.segmentation import deeplabv3 resnet50
class DeepLabV3Wrapper(nn.Module):
    def init (self, num classes):
        # use encoder to pass pretrained encoder
        super(). init ()
        self.model = deeplabv3 resnet50(num classes=num classes)
    def forward(self, image):
        return self.model(image)['out']
number of classes = len(set(name to category.values()))
model = DeepLabV3Wrapper(number of classes)
summary(model, input data=batch[0])
Downloading: "https://download.pytorch.org/models/resnet50-
0676ba61.pth" to /root/.cache/torch/hub/checkpoints/resnet50-
0676ba61.pth
{"model id":"949f96f0312249bcb38ed2bb05bd1640","version major":2,"vers
ion minor":0}
Layer (type:depth-idx)
                                                         Output Shape
Param #
DeepLabV3Wrapper
                                                         [8, 8, 128,
1281
⊢DeepLabV3: 1-1
                                                         [8, 8, 128,
128]
     └─IntermediateLayerGetter: 2-1
                                                         [8, 2048, 16,
16]
          └─Conv2d: 3-1
                                                         [8, 64, 64,
64]
              9.408
          └─BatchNorm2d: 3-2
                                                         [8, 64, 64,
```

```
641
              128
           └ReLU: 3-3
                                                           [8, 64, 64,
64]
           └─MaxPool2d: 3-4
                                                           [8, 64, 32,
32]
           └─Sequential: 3-5
                                                           [8, 256, 32,
             215,808
32]
           └─Sequential: 3-6
                                                           [8, 512, 16,
16]
             1,219,584
           └─Sequential: 3-7
                                                           [8, 1024, 16,
16]
            7,098,368
           └─Sequential: 3-8
                                                           [8, 2048, 16,
            14,964,736
16]
     └─DeepLabHead: 2-2
                                                           [8, 8, 16, 16]
           └ASPP: 3-9
                                                           [8, 256, 16,
16]
             15,535,104
           └─Conv2d: 3-10
                                                           [8, 256, 16,
16]
             589,824
           └─BatchNorm2d: 3-11
                                                           [8, 256, 16,
16]
             512
           └─ReLU: 3-12
                                                           [8, 256, 16,
16]
           └─Conv2d: 3-13
                                                           [8, 8, 16, 16]
2,056
Total params: 39,635,528
Trainable params: 39,635,528
Non-trainable params: 0
Total mult-adds (G): 81.79
Input size (MB): 1.57
Forward/backward pass size (MB): 1103.27
Params size (MB): 158.54
Estimated Total Size (MB): 1263.38
```

## **Training**

```
update_pb_every_batch=10)
clear_output(wait=True)
plot_losses_coeffs(deeplabv3_training_val_summary)
```



**Note**: results for deeplabV3 does not look very good (simillarly the training curves point to significant overfitting). It is however due to rescaling the images to (128,128,3) from (256,256,3). Rescaling was done due to GPU hour constraint limit. The networks has been trained for the original input and the notebook containing the results can be found on github repository here https://github.com/mlewandowski0/SemanticSegmentation.

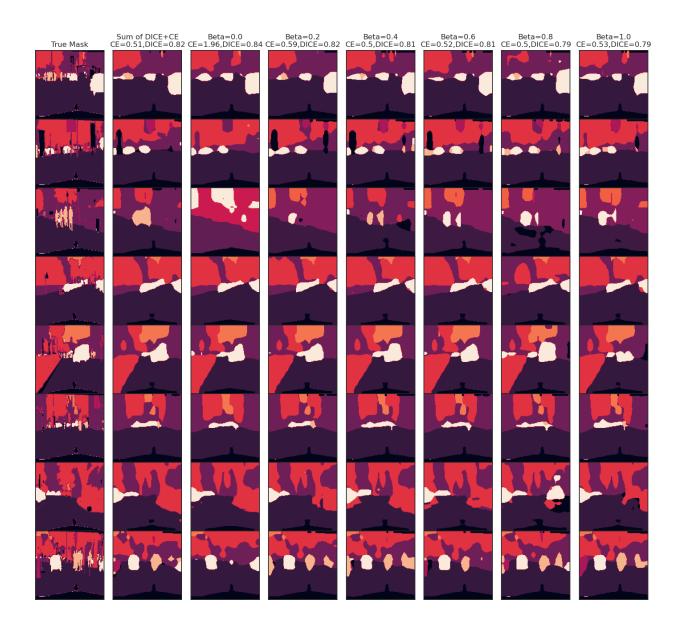
# Inference (Show results)

```
batch = next(iter(val_dataloader))
predictions = model(batch[0].to(device))
show_inference(batch, predictions)
```



# Comparison of how loss affects the prediction (CE and DICE)

```
# VERY LONG COMPUTATION !
summaries, preds = get summaries preds for comparison(0.2,
DeepLabV3Wrapper, {"num classes" : number of classes},
model mixed=model, summary mixed=deeplabv3 training val summary,
epochs=7)
plot summaries preds(summaries, preds)
[675s] Model with Beta=0.0 finished with training CE :
1.622825225581642, training DICE : 0.8634402278691781, validation CE :
1.9594023265838623, Validation DICE: 0.8449090099334717, Validation
IOU : 0.7321907482147216
[690s] Model with Beta=0.2 finished with training CE:
0.43298630991903675, training DICE: 0.8503047303993161, validation CE
: 0.5946194853782654, Validation DICE : 0.8227595825195313, Validation
IOU: 0.6997165150642395
[690s] Model with Beta=0.4 finished with training CE:
0.37473232725087335, training DICE: 0.8428310160877324, validation CE
: 0.49869476771354676, Validation DICE : 0.8130638990402221,
Validation IOU: 0.6858364295959473
[693s] Model with Beta=0.6000000000000001 finished with training CE:
0.3564092984119383, training DICE: 0.8358333107202994, validation
CE: 0.5194212470054627, Validation DICE: 0.8050946068763732,
Validation IOU: 0.6747957072257995
[694s] Model with Beta=0.8 finished with training CE:
0.3593935028244467, training DICE: 0.825804696443702, validation CE:
0.49856691670417785, Validation DICE: 0.7854016389846802, Validation
IOU: 0.6473414568901062
[692s] Model with Beta=1.0 finished with training CE:
0.3484446392239643, training DICE: 0.8240864096569414, validation
CE: 0.5275791184902191, Validation DICE: 0.7931733894348144,
Validation IOU : 0.6581960039138794
```



## Summary

This notebook was my first try on playing with semantic segmenation and documenting it. It contains some basic pytorch ( + HF accelerate) code for training semantic segmentation networks, DICE/IoU losses and few basic architectures for semantic segmentation. In next one, where I explore different dataset I will document more modern semantic segmentation networks (here all networks were from 2014-2019, the field of course is constantly coming up with new architectures, however this notebook was getting long).

Summary of results (input size = (128,128,3))

			number of	
Network Name	mDICE	mloU	parameters	
UNet	0.8519	0.7431	7,763,272	

			number of
Network Name	mDICE	mloU	parameters
ENet	0.8139	0.6871	350,076
ESNet	0.8262	0.7046	1,658,356
DeepLabV3	0.8276	0.7067	39,635,528

**Note**: results for deeplabV3 does not look very good (simillarly the training curves point to significant overfitting). It is however due to rescaling the images to (128,128,3) from (256,256,3). Rescaling was done due to GPU hour constraint limit. The networks has been trained for the original input and the notebook containing the results can be found on github repository here <a href="https://github.com/mlewandowski0/SemanticSegmentation">https://github.com/mlewandowski0/SemanticSegmentation</a>. Here is the Summary of results for original input size:

Summary of results (input size = (256,256,3))

Network Name	mDICE	mloU	number of parameters
			· · · · · · · · · · · · · · · · · · ·
UNet	0.8541	0.7463	7,763,272
ENet	0.835	0.716	350,076
ESNet	0.8515	0.7422	1,658,356
DeepLabV3	0.8617	0.758	39,635,528