REJ AVERION

Test & Verification Engineer

Munich, Germany • linkedin.com/in/rejaverion • github.com/raverion

PROFESSIONAL SUMMARY

Test & Verification Engineer with 7+ years of experience in automotive semiconductors and a fresh perspective in Medical OEM. Expertise in designing hardware, developing test programs, debugging test setups, and leveraging AI in engineering workflows. A hands-on, forward-looking practitioner and versatile technical generalist who bridges the gap between quality and profitability.

TECHNICAL SKILLS

ATE Platforms: SPEA DOT400, ETS88, LTX MX, Advantest T2000, Teradyne FLEX
IC Test Development: Analog, Digital, Mixed-Signal, SerDes, High-Speed Links
Hardware Design: Altium Designer, Cadence Allegro, LTSpice, Signal Integrity, Power Integrity
Quality & Analysis: SPC, MSA, Gage R&R, FMEA, Yield Improvement, OEE Improvement, Test Optimization
Programming: C++, Python, JavaScript, CAPL, VBA, SQL, Linux
Tools & Platforms: Vector CANoe, Visual Studio, Git, SVN, JIRA, Polarion

PROFESSIONAL EXPERIENCE

T&S Engineering GmbH

Verification and Validation Consultant

Jun 2025 – Present | Munich, Germany

- Developing qualification protocols for verification test equipment
- Executing test cases for embedded systems in medical OEM products
- Implementing automation of bench equipment calibration
- Developing measurement system analysis tools and frameworks

Siltest Semiconductors GmbH

Field Test Engineer

Jul 2023 – May 2025 | Dortmund, Germany

- Provided on-site support for automotive semiconductor test development
- Debugged and qualified various test hardware on Advantest T2000 platform
- Trained junior engineers on ATE operation and test program development
- Achieved significant test time reductions and throughput improvement through test optimization techniques

Manila Finest Technical Services

Project Engineer (Multidisciplinary)

Feb 2023 – Jun 2023 | Dubai, UAE

- Managed renovation projects for residential and commercial properties
- Coordinated with suppliers, contractors, and clients to ensure project timelines were met
- Developed internal software tools that accelerate logistical and administrative tasks
- Gained HVAC, mechanical, painting and construction knowledge and experience

Analog Devices Inc.

Senior Test Engineer

Oct 2021 – Feb 2023 | Cavite, Philippines

- Led test development for automotive-grade power management ICs
- Drove high-visibility ATE platform migration projects with aggressive timelines
- Traveled to off-shore sites for engineering and manufacturing support purposes
- Mentored junior engineers and conducted technical reviews

Maxim Integrated Inc.

Member of Technical Staff

Feb 2018 – Oct 2021 | Cavite, Philippines

- Developed test programs for mixed-signal automotive ICs
- Designed loadboards and probecards for high-speed SerDes
- Performed correlation studies between ATE and bench measurements
- Optimized test solutions to improve yield, reduce downtime, and increase gross margins

EDUCATION

B.S. Electronics and Communication Engineering

Emilio Aguinaldo College

Cavite, Philippines