



FIGURE A-4: AD CLK = Fac * SampleRate * Kad F = 50Hz * 64 * 256 = 819.2 KHz F = 50Hz * 128 * 256 = 1638.4 KHz * 3905 clk range 1M to 4M Output Pulse
1: active power 2: active power +
3: Reactive power
4: Reactive power
5: TBD 6: TBD + U301 TLP521-1 **MAIN BOARD SCHEMATIC - PAGE 2** U302 TLP521-1 U305 MCP1701(5V-SOT8 (L301 (INDUCTOR T1IN T2IN 12 9 RIOUT R2OUT Front-end of three Phase Power Meter Design 2-2