

**Title:** Exercise 1

**Course:** Verification

**Students:** Rayhan Shhadeh, Batool kharraz

**Supervisor:** Dr. Suliman Abu Kharmeh

# 1- What are the values of the all input and out signals

- 1<sup>st</sup> falling edge: as It appears if Fig 1 all of req[0-3], gnt[0-30] values in the first falling edge are **zeros**.

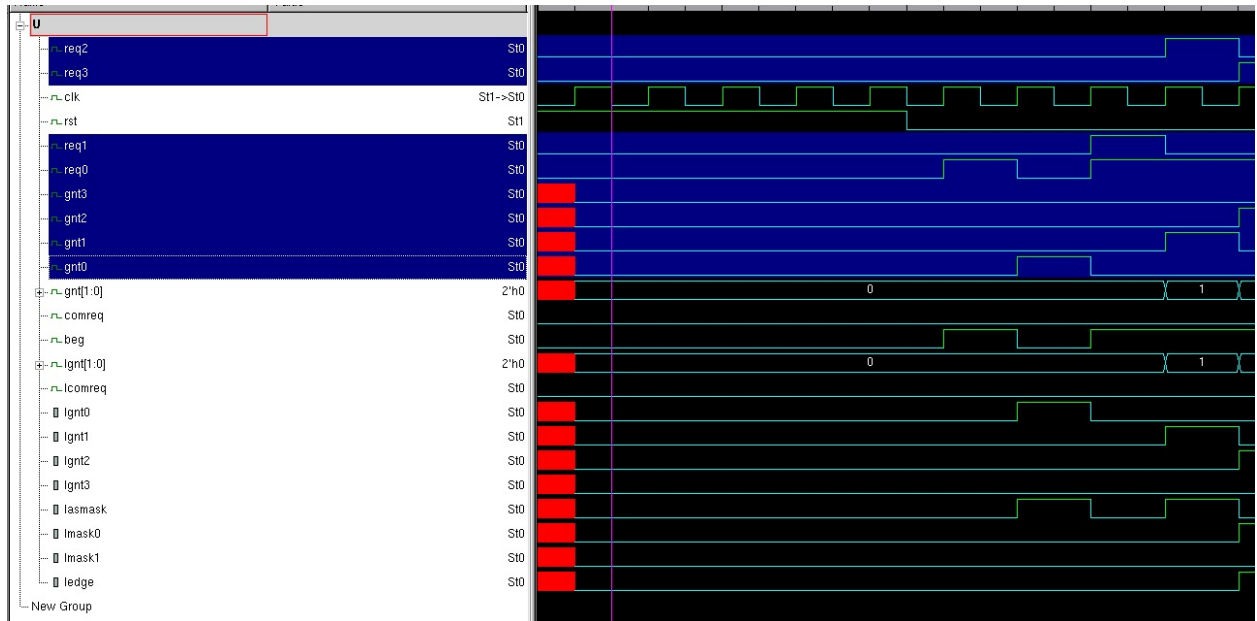


fig 1

- 11<sup>th</sup> falling edge: here we're supposed to take 11<sup>th</sup> falling edge values but we have accidentally took the 10<sup>th</sup> falling edge (look at the courser) but that's fine

	req0	req1	req2	req3	gnt0	gnt1	gnt2	gnt3
10 <sup>th</sup>	1	0	0	1	0	0	1	0
11 <sup>th</sup>	1	0	0	1	0	0	1	1

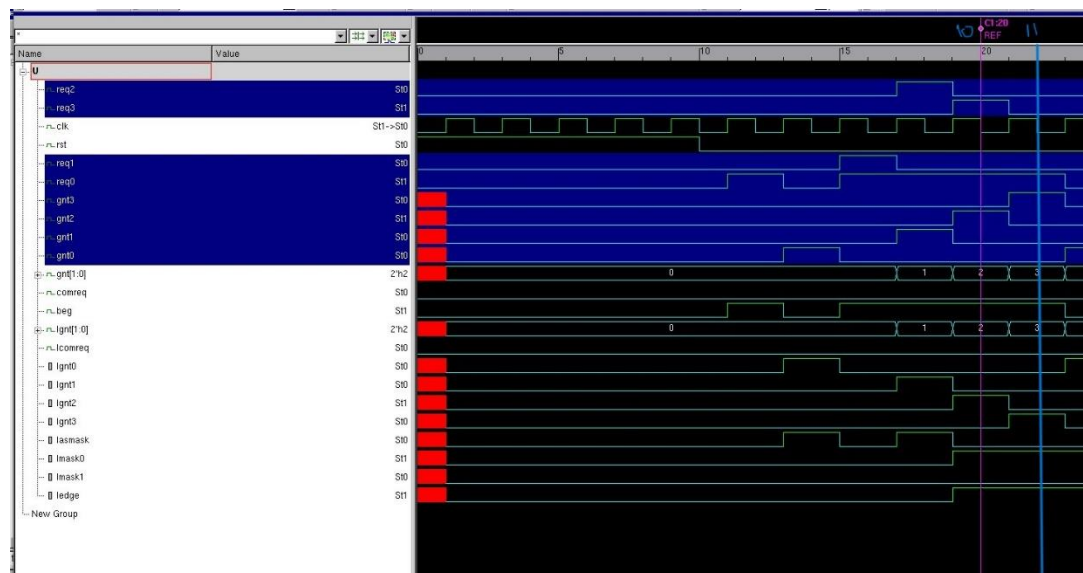
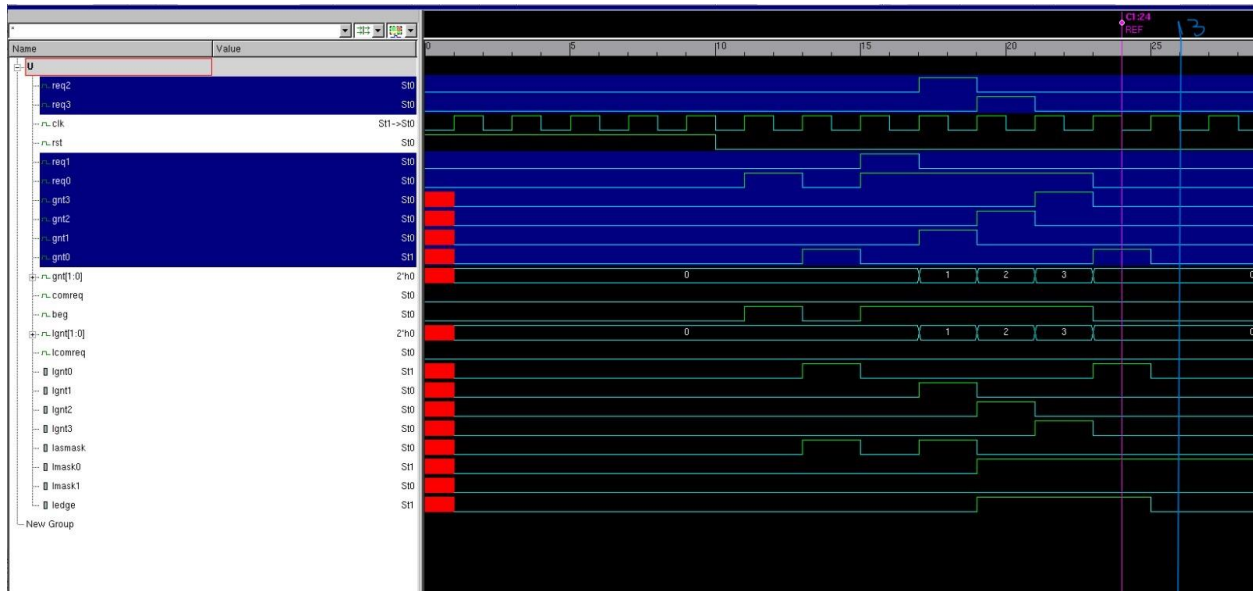


Fig 2

- 13<sup>th</sup> falling edge:

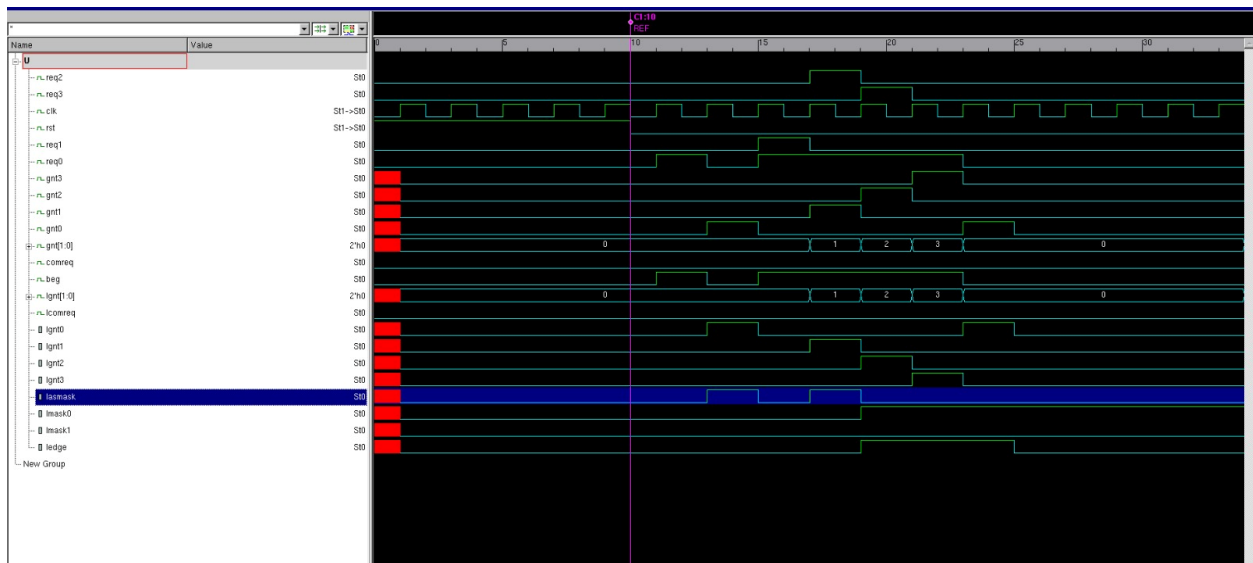
	req0	req1	req2	req3	gnt0	gnt1	gnt2	gnt3
13 <sup>th</sup>	0	0	0	0	0	0	0	0



- 2- When reset signal released?

Answer: when the reset signal transitions from active (resetting) to inactive (released), or 1 → 0 since it active high, it released after 5 clock cycles so 5\* period for one cycle ( suppose it was 10 ns for one cycle) = 50 ns.

- 3- Lasmask variable:



Thank you