HW 5

* Two’s complement
  + Converting numbers into two’s complement
    - 24 = 011000
    - -24 = 100111 + 1 = 101000
      * flip bits of 24 and add 1
  + Adding, subtracting
    - Subtracting: 011001 – 111000
      * 011001 + (-111000)
      * = 011001 + 001000 = 100001
  + Converting two’s complement to decimal
    - For negative numbers, switch to two’s complement and negate value
  + Shifting bits
* ISA problems
* Addressing
  + Immediate = load immediate value
  + Direct = go to address and get value
  + Indirect = go to register, get address, go to register of that address
* Expanding opcodes