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| **Project:** | | | | | Project Name | | | | | | | | | | | |
| **Customer:** | | | | | Customer Name | | | | | **Review**  **date:** | | | | yyyy-mm-dd | | |
|  | | | | | | | | | | | | | | | | |
| **Object to be reviewed:** | | | | | | | | | | | | | | | | |
| Document name: | | | Project\_System\_Architecture\_Specification | | | | | | | | | | | | | |
| Version: | | | 1.0 | | | | | | | | | | | | | |
|  | | | | | | | | | | | | | | | | |
| **Participants:** | | | | | | | **Name** | | | **Org. unit** | | | | | **Review manager**  **(x)** | |
| Person responsible (author): | | | | | | | Gil-Dong, Hong | | | R&D | | | | | x | |
| HW Architect: | | | | | | |  | | |  | | | | |  | |
| SW Architect: | | | | | | |  | | |  | | | | |  | |
| Other reviewer(s): | | | | | | |  | | |  | | | | |  | |
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| **Review type:** | | | | | | | | | | | | | **Project status:** | | | |
|  | | | |  | |  | |  |  | |  | | Phase: | | |  |
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| **Last review of the object to be reviewed:** | | | | | | | | | | | | | | | | |
| Performed on: | |  | | | | | | | | | | | | | | |
| Doc. name: | |  | | | | | | | | | | | | | | |
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| **Follow Up:** | | | | | | | | | | | | | | | | |
|  |  | | | | | | | | | | | Date: | | |  | |
|  |  | | | | | | | | | | | Date: | | |  | |

| **Checklist '** **Review Meeting'** | **OK?** | | **Action/comments** |
| --- | --- | --- | --- |
| **yes** | **no** |
| Are all MHE Procedures, standards, etc. (as specified in the project manual) applied? |  |  | If this checklist is not applicable, you should leave comment and 'NA'.  If you have comment then you should leave comment at this. |
| Does a description exist which measures are to be used to meet reliability requirements? |  |  |  |
| Is the system architecture specification based on the current MHE form 8303? |  |  |  |
| Are all chapters of the MHE form 8303 described? |  |  |  |
| Have no chapters of the MHE form 8303 been deleted, but if not applicable been marked? |  |  |  |
| Does a description exist which measures are to be used to meet reliability requirements? |  |  |  |
| Do special requirements on the safety of certain functions exist? (Check:External watchdog, power supply, reset strategy, safestate, reference voltage as cause for common cause failures for analog inputs.) |  |  |  |
| Does a description exist which measures are to be used to meet safety requirements? |  |  |  |
| Do incomplete sections of the specification exist and are they marked as such? |  |  |  |
| Have cross-references been implemented completely and unambiguously? |  |  |  |
| Is a list of abbreviations available? Does it contain all abbreviations used? |  |  |  |
| Is the change documentation up-to-date ? |  |  |  |
| Were all variants or device varieties taken into account? |  |  |  |
| Have all key elements of the system and their interfaces been illustrated by a block diagram, for example? |  |  |  |
| Does the appendix contain all documents not unmistakably identified? |  |  |  |
| Are those entries automatically produced (e.g. table of contents, fields, ...)correctly set? |  |  |  |
| Were all figures positioned in the right place? |  |  |  |
| Do the denominations in the figures comply with the names in the text? |  |  |  |
| Is there a special identification for safety functions and/or critical functions? |  |  |  |
| Were therefore specific requirements considered? |  |  |  |
| Is the special objective of the functions obvious, completed and clear? Was it described in total? |  |  |  |
| Has a block classification been performed for all blocks? |  |  |  |
| Are the correlating elements of the single functions also taken into account for description? |  |  |  |
| Does the specification contain all information required for successful hardware / software analysis and design? |  |  |  |
| Have limits / value ranges been specified? |  |  |  |
| Have processing speed and timings been specified and documented? |  |  |  |
| Is it defined which interfaces will exist to external systems? |  |  |  |
| Are these interfaces unambiguously defined? |  |  |  |
| Are all interlinks to other variants clearly described? |  |  |  |
| Do requirements remain which are not contained in the specification - neither in total nor in part? |  |  |  |
| Are system requirements allocated to the elements of the architecture? |  |  |  |
| Does the system architecture take the non-functional requirements into account? |  |  |  |
| Have verification criteria been developed? |  |  |  |
| Has a real time analysis been performed and successfully passed? |  |  |  |
| Does a block diagram of the system with the allocation of ASIL exist? |  |  |  |
| If using DOORS: Are all relevant attributes set? |  |  |  |

|  | **Complaint / fault** | | | | | | | |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **No** | **Page/**  **Line** | **Issue** | **Safety**  **relevant** | | **Action** | | | |
| **yes** | **no** | **Solution** | **Responsible person** | **Completion deadline** | **Done**  **(🗸)** |
| 1 | 15/20 | There is no diagram for explian of module. |  | x | Add diagram for module. | Gil-Dong, Hong | yyyy-mm-dd | **🗸** |
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