

# Lab. of Electronics

Lab name : **Subtractor Circuit (Differential Amplifier)** Student ID : B11102112 Name: Chiajui Lee

## I. Purpose

An OPA subtractor circuit is designed to perform subtraction on two or more input signals, producing an output signal equal to the difference between them. This design is useful for various applications, such as measuring signal differences or implementing amplifier adjustments.

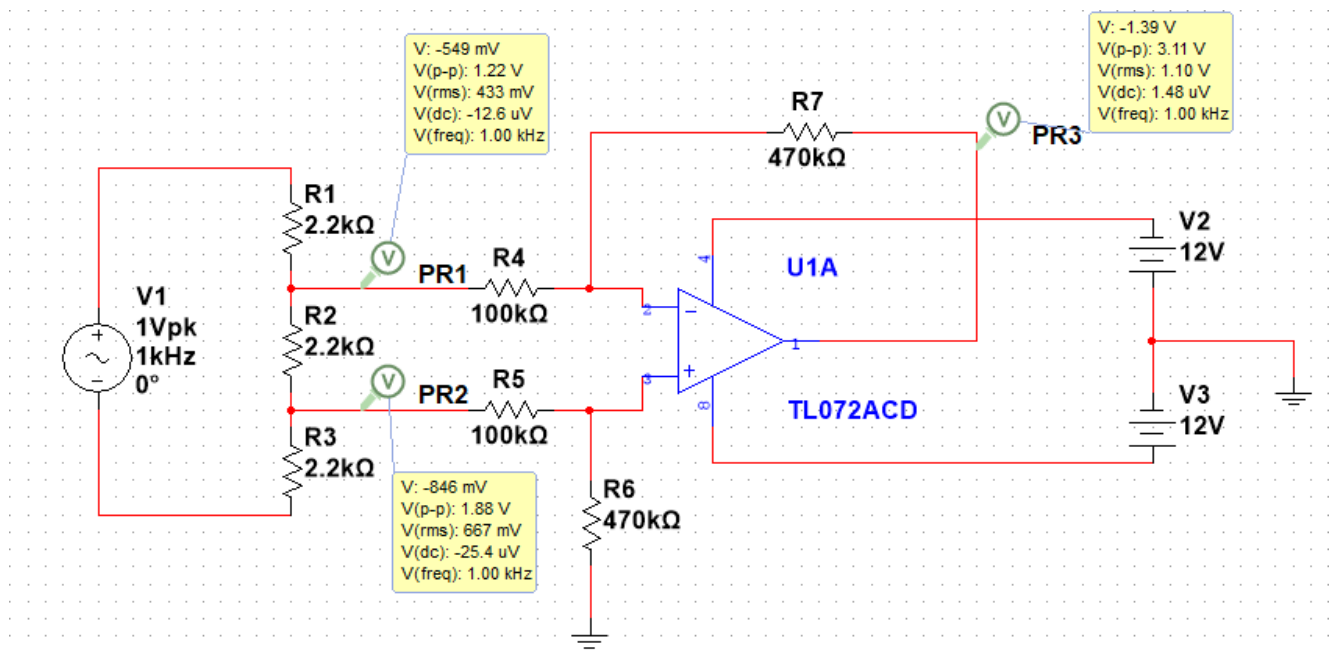
## II. Steps

- Construct the required circuit using operational amplifiers (OPAs) and resistors as specified in the problem.
- Connect the power supply to provide +10V to the  $V_+$  terminal and -10V to the  $V_-$  terminal of the operational amplifier (OPA).
- Measure the output results using an oscilloscope.

## III. Data

### A. Differential Amplifier

#### 1. Circuit Diagram

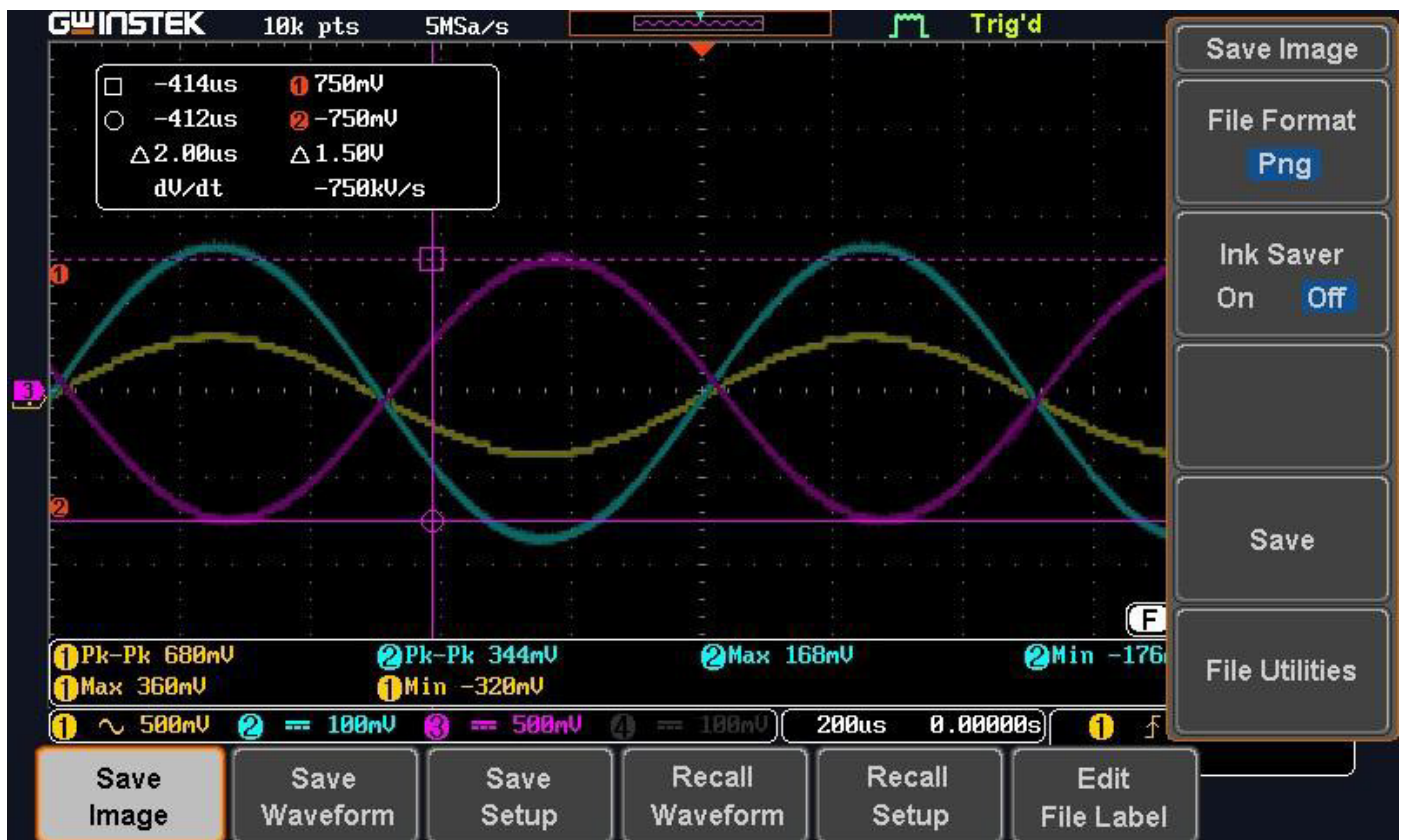


#### 2. Output Value

	Measured Value	Theoretical Value
Output voltage	1.5V	1.5V

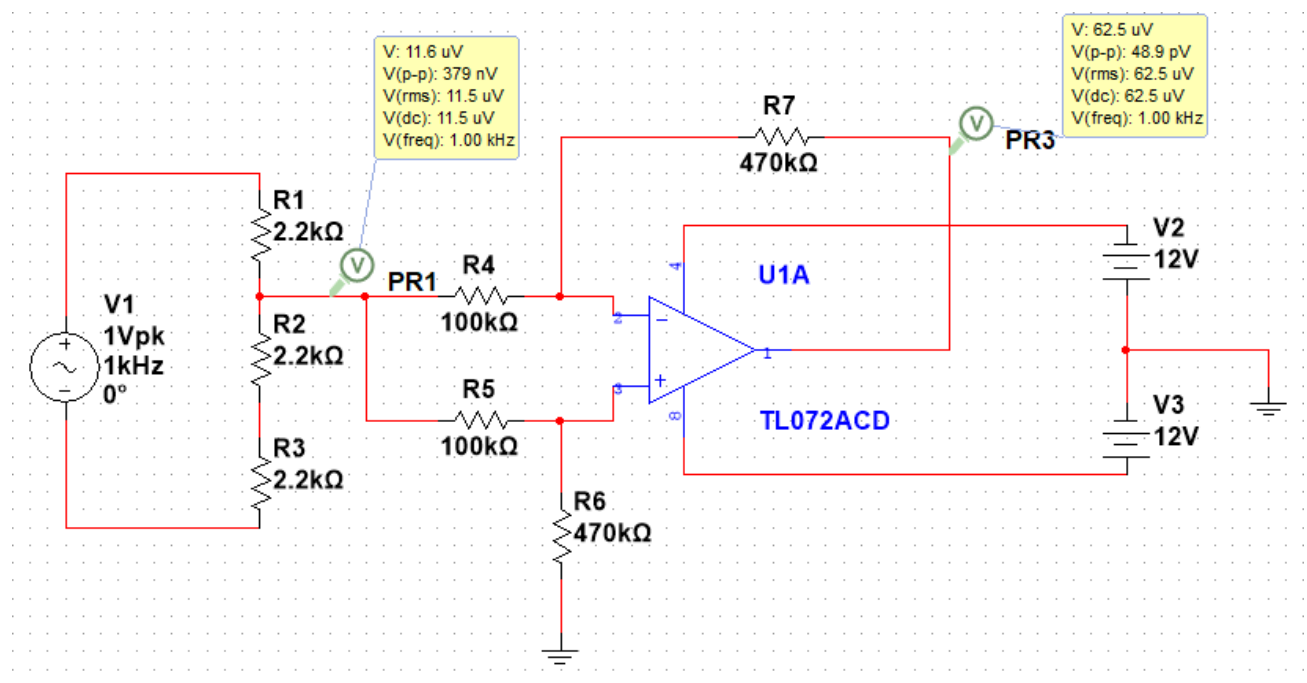
## 3. Output Result

When  $V_{i1} = 680\text{mV}$ ,  $V_{i2} = 344\text{mV}$ ;  $V_o = 1.5\text{V}$



## B. Subtractor Circuit (Part 2)

### 1. Circuit diagram



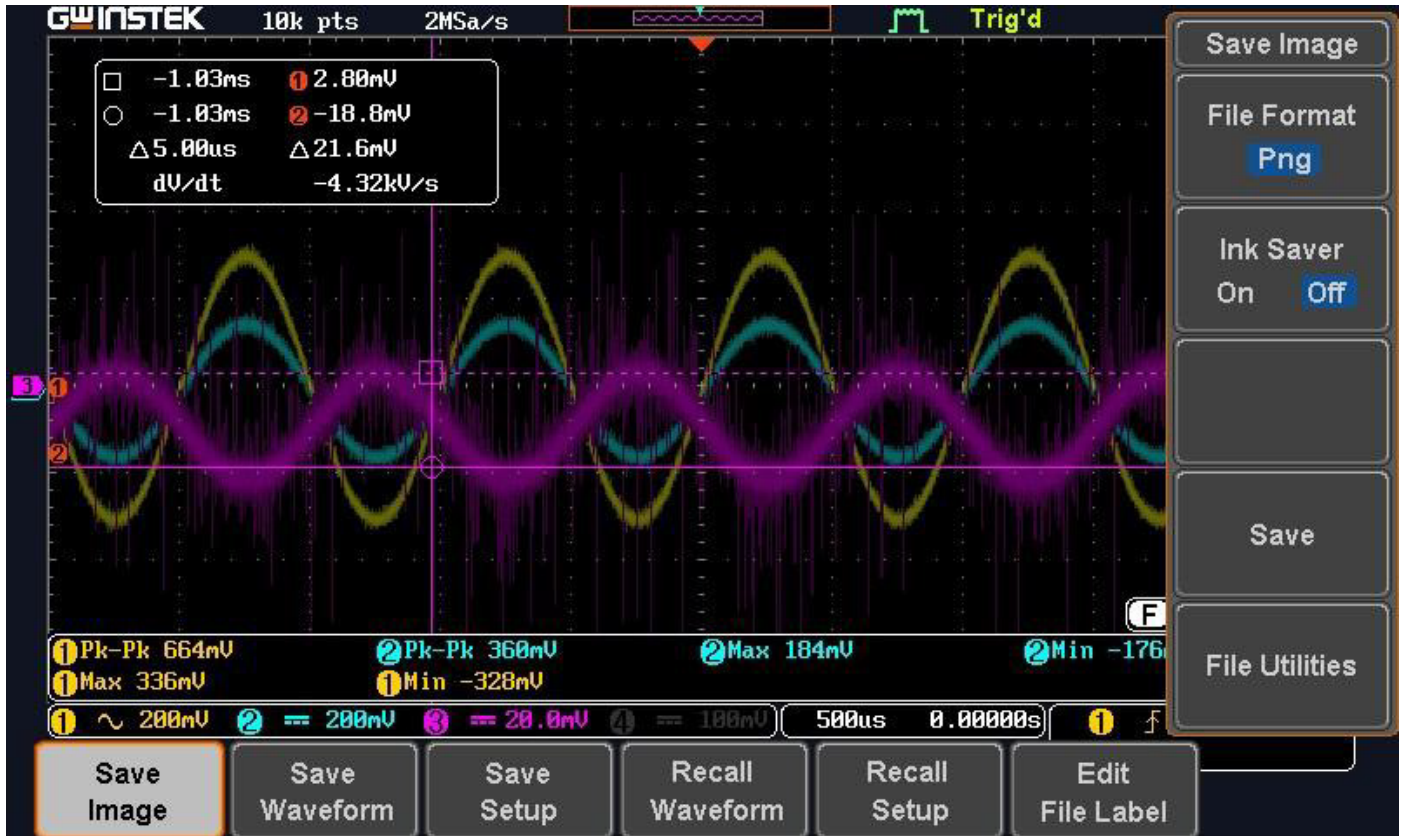
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### 2. Output Value

	Measured Value	Theoretical Value
Output voltage	20mV	12mV

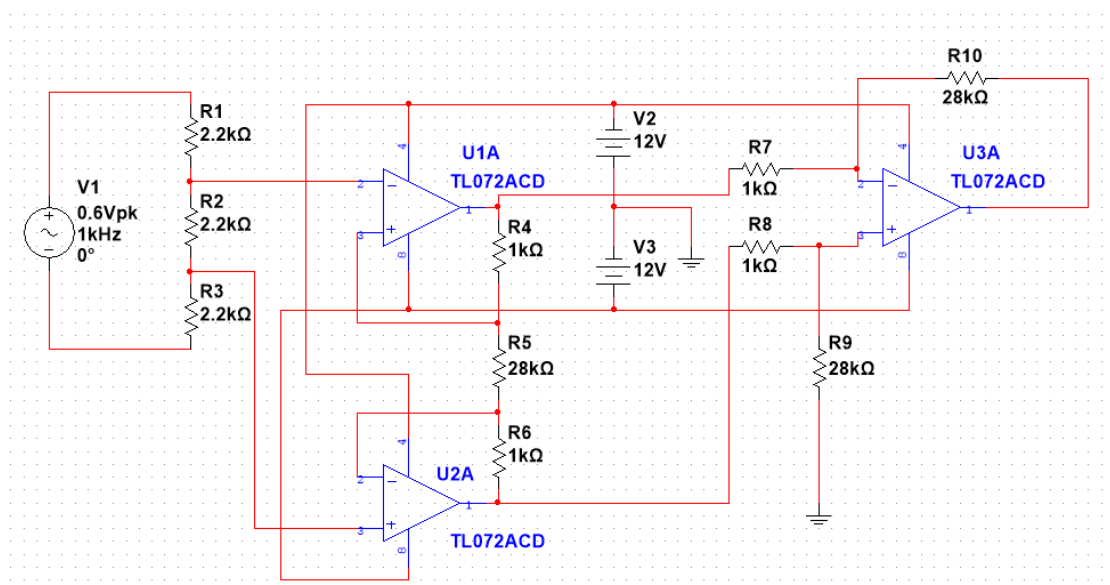
### 3. Output Result

When  $V_{i1} = 680\text{mV}$ ,  $V_{i2} = 344\text{mV}$ ;  $V_o = 20\text{mV}$



## C. Instrumentation Amplifier

### 1. Circuit diagram



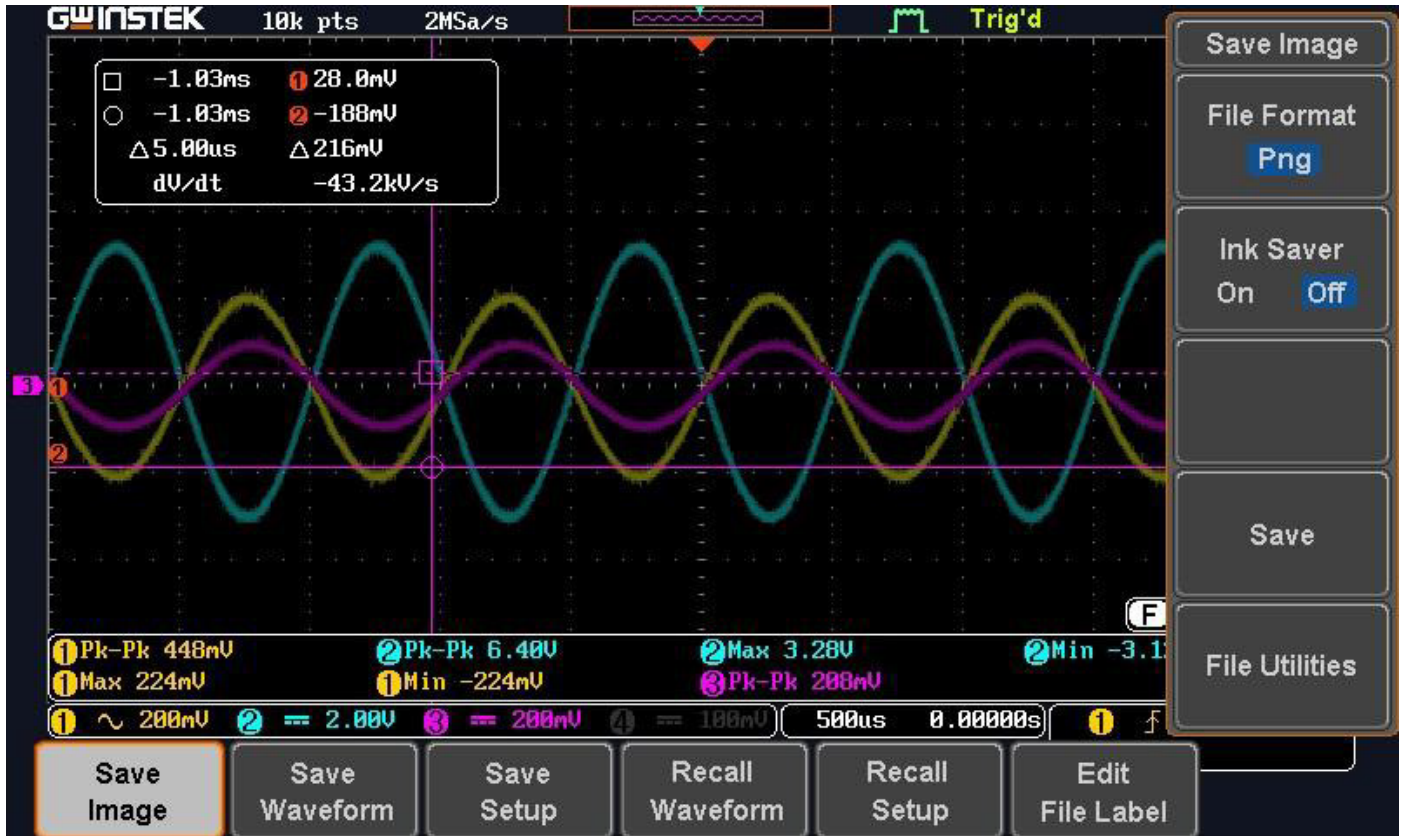
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### 2. Output Value

	Measured Value	Theoretical Value
Output voltage	6.4V	6V

### 3. Output Result

When  $V_{i1} = 448\text{mV}$ ,  $V_{i2} = 208\text{mV}$ ;  $V_o = 6.4\text{V}$  ( $A_v = 30$ )



## IV. Reflections

The focus of this practical session was to investigate the OPA subtractor circuit and its applications. The first circuit's relatively simple structure allowed us to complete the implementation smoothly. However, in the second circuit, the small amplitude of the input signal resulted in significant noise interference in the output signal. To accurately measure the output voltage, we applied the oscilloscope's cursor function learned in the previous lab session.

For the bonus task, we needed to design the resistor ratios independently. Initially, due to insufficient consideration, we selected too many resistors, causing oscillation in the output signal. Through discussions with classmates and subsequent adjustments, we successfully determined appropriate resistor ratios and achieved the expected output results.