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FINAL YEAR BENG ROBOTICS ENGINEERING

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## Portfolio Overview:

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This portfolio showcases hardware engineering projects, highlighting hands-on experience and technical skills in designing, building, and testing systems.

**NB: For discussions, corrections or advice, please reach out to me via email at [rayyanabhram@gmail.com](mailto:rayyanabhram@gmail.com)**

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## Contents

<b>INTRODUCTION</b>	<b>1</b>
<b>PROJECT 1: DESIGN OF A DUAL DC MOTOR DRIVER IN ALTIVUM DESIGNER</b>	<b>1</b>
A Project overview: . . . . .	1
B Component Selection / Schematic Design: . . . . .	1
C PCB layout and routing: . . . . .	2
D Summary: . . . . .	4
<b>PROJECT 2: DESIGN OF A STM32 MCU IN KI-CAD</b>	<b>4</b>
A Project overview: . . . . .	4
B Schematic Design: . . . . .	4
C PCB layout and routing: . . . . .	5
D Summary: . . . . .	6
<b>DESIGN OF A USB LIPO CHARGER MODULE MCU IN ALTIVUM</b>	<b>6</b>
A Project overview: . . . . .	6
B Schematic Design: . . . . .	6
C PCB layout and routing: . . . . .	7
D Summary: . . . . .	7

## INTRODUCTION

This document is intended to present a series of projects produced and designed by myself during my undergraduate studies.

Projects showcased are a mixture of university related coursework and work derived from professionally developed skills. Some projects have been excluded from this document due to NDA restrictions. For each project, all source material can be found on github at: <https://github.com/raytriestodostuff/portfolio-projects.git>

## PROJECT 1: DESIGN OF A DUAL DC MOTOR DRIVER IN ALTIUM DESIGNER

**A. Project overview:** This project set out to design a simple DC motor driver in Altium designer which is capable of running on a 30V input to drive two independent motors. Each motor is expected to maintain an approximate current draw of 1A. This project is based on Altiums educational article, with some key changes made to suit more general applications. The PCB is designed to have a compact footprint, with all components selected as SMDs which should be soldered onto the board using a re-flow oven. All production files produced from this project may be found on github.

The board is powered by the A495 Dual full bridge IC and uses logical inputs to specify rotation of the motor. Inputs can be fed into the board using 5 x 2.54mm pitch header pins and PWM is used to actuate the motor. Figure 1 explains motor control, braking, and how to instruct the IC to enter a low-power mode.

**PWM Control Truth Table**

IN1, IN3	IN2, IN4	$10 \times V_S > V_{REF}$	OUT1, OUT3	OUT2, OUT4	Function
0	1	False	L	H	Reverse
1	0	False	H	L	Forward
0	1	True	H/L	L	Chop (mixed decay), reverse
1	0	True	L	H/L	Chop (mixed decay), forward
1	1	False	L	L	Brake (slow decay)
0	0	False	Z	Z	Coast, enters Low Power Standby mode after 1 ms

**Figure 1: PWM Control**

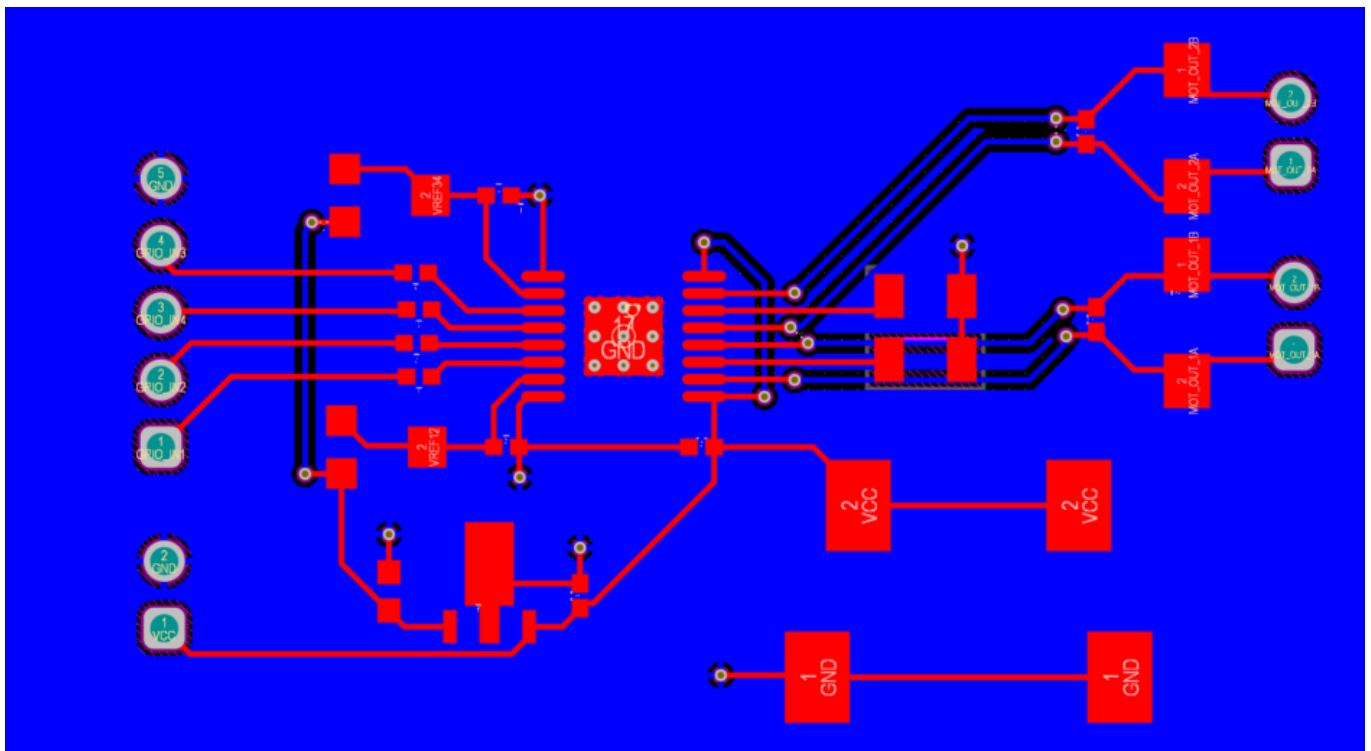
**B. Component Selection / Schematic Design:** The A495 Dual Bridge motor driver was used to control the board due to its ability to handle a peak current of up to 2A. The chip can also handle operating voltages of up to 40V and features in built over-current protection.

The IC also features a few peripheral components to ensure proper functioning. Use of 2 x 100uF electrolytic capacitors and 1 x 0.22uF are strongly recommended by the data sheet. 0.25 Ohm resistors are also recommended, they are placed at the power return pins of the IC.

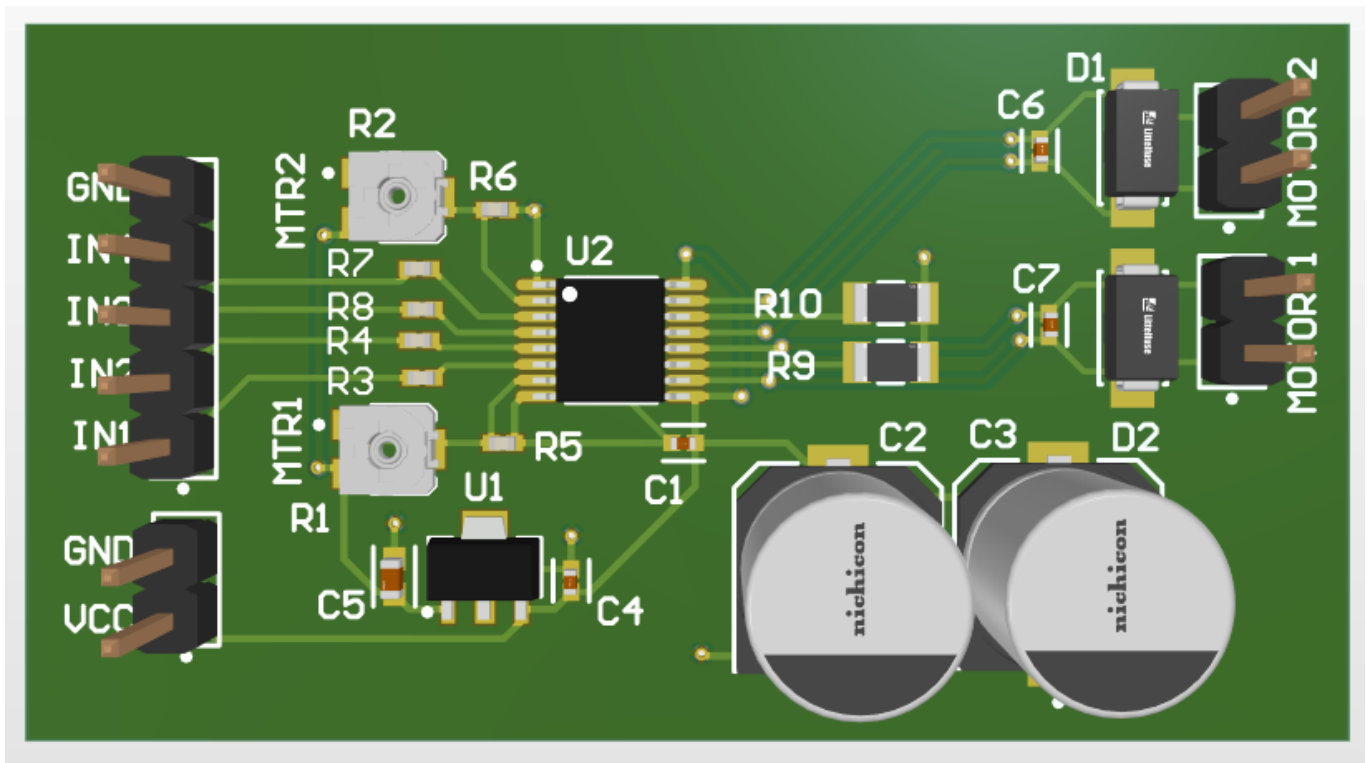
33 Ohm resistors are used at the input pins to protect the controller from large current spikes should the driver IC break or fail. A trimmer potentiometer at the top leg of each voltage divider at the analog input pins (VREF12, VREF34) is placed to set the current for each motor.

A capacitor and TVS diodes are placed at the motor outputs on the PCB to help prevent damage from ESD and transient flyback voltages.





**Figure 4:** Top and bottom layers with completed routing and ground pores



**Figure 5:** 3D Render of PCB

**D.Summary:** The produced output presents a simple solution to control two DC motors using PWM with logic inputs. The footprint of the board is 5cm x 3cm which could be made smaller but is nonetheless reasonable for this application. Where possible, 0402 generics have been used. To further reduce board size, silk screen designators can be removed entirely to free up board real estate in addition to arranging components differently. Optimizing poring would be a good next step to ensure robustness in this design. All fabrication outputs can be found on my github. JLC PCB designs rules were used to pass the DRC check.

## PROJECT 2: DESIGN OF A STM32 MCU IN KI-CAD

**A.Project overview:** The project aims to create a basic micro-controller development board using the STM32F103C8T6 ARM based chip. Generally speaking, it has a range of programmable GPIO pins which can be freely configured using the STM32 IDE and can facilitate communication via UART, SPI, I2C and USB. The device also features in built timers and can generate PWM signals for managing various peripherals. The project intends to outline the basic process that is used to develop a micro-controller and improve understanding around component interactions.

**B.Schematic Design:** The schematic can roughly be broken down into three main parts: Power supply, USB (Micro-USB-B) and the MCU chip. Starting with the Power supply, the AMS1117 3V3 regulator is used to step down an input voltage (assumed to be 5 volts nominally) to 3V3. Two capacitors are used to filter and reduce noise on the power lines. A LED is used as a debugging tool to identify if the regulator is working correctly. The board features 4 VCC pins and one VCCA pin. One decoupling capacitor is used per pin (100n) in addition to one bulk decoupling capacitor of 10U. They are placed close to the power pins in the PCB. Considering that a JTAG interface has not been implemented into this design, a micro-usb-b connector is used to communicate with the device. A differential pair is used to connect the data lines to ensure functional data transfer and the connector is also used to power the board. Various components like the crystal oscillator, ferrite beads and switches are used to ensure proper MCU functioning. The switch for example at the BOOT pin is used to pull the pin high or low depending on the what state of the board is desired. Finally, various connector pins are provided to facilitate external connections. The pins of the MCU can be programmed via STM32 IDE.

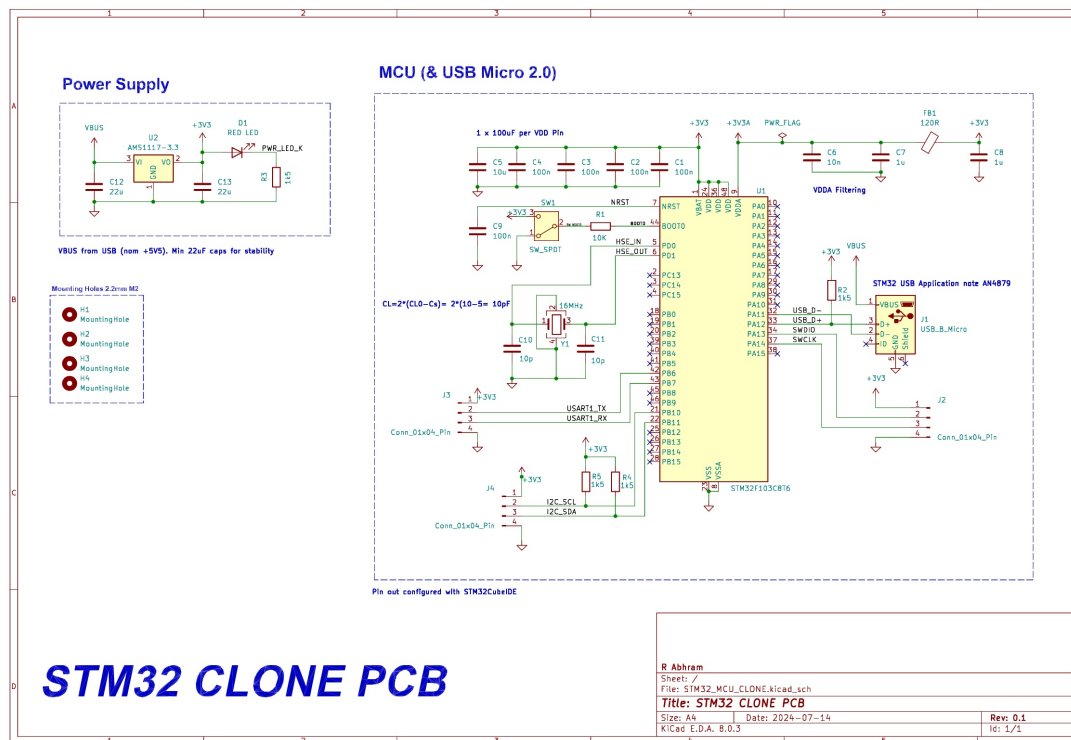
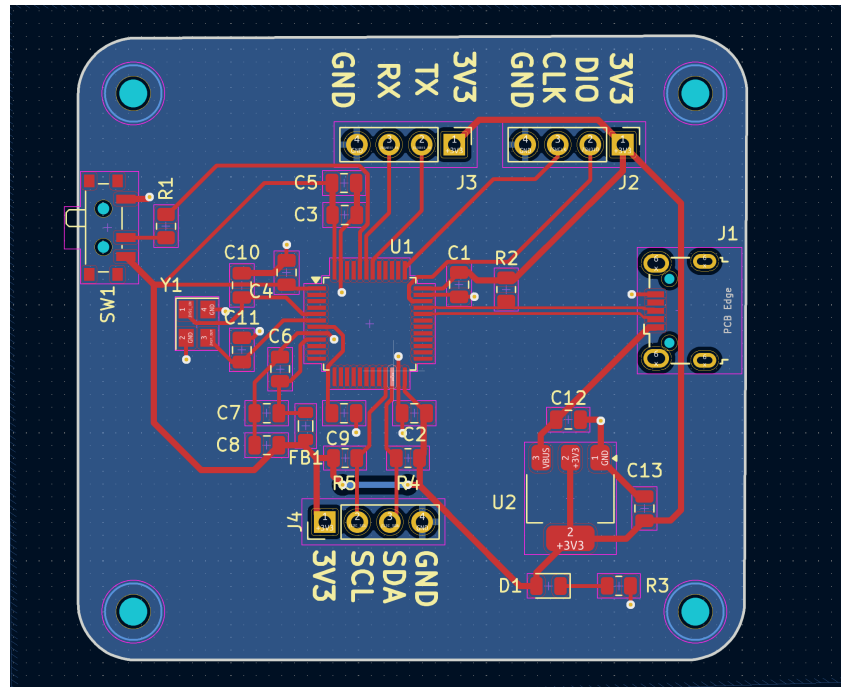
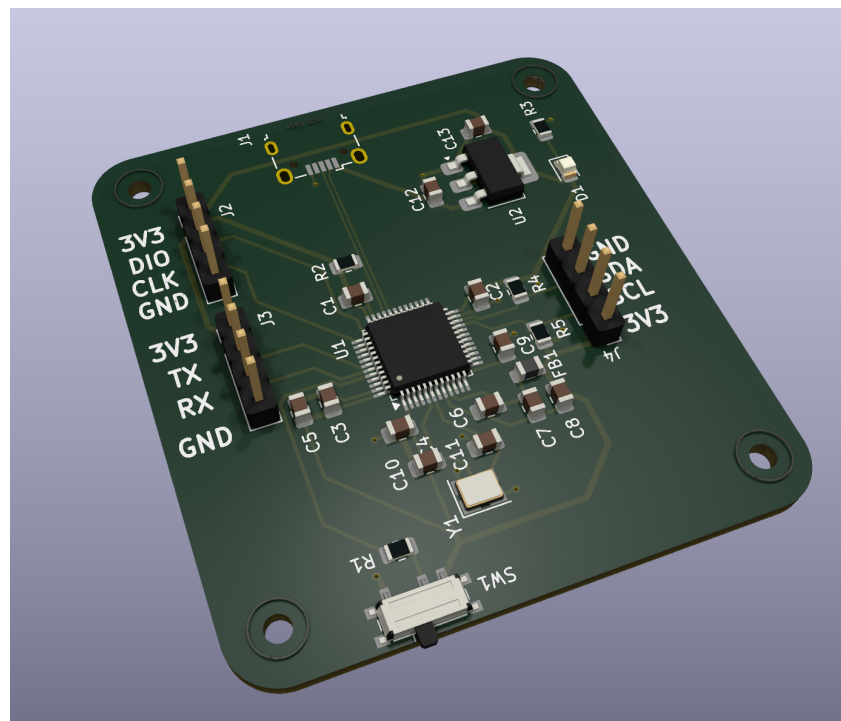


Figure 6: STM32F103C8T6 schematic

**C.PCB layout and routing:** Figure 10 and 11 show component placement, PCB routing and 3D render of the board.



**Figure 7:** Component placement and routing

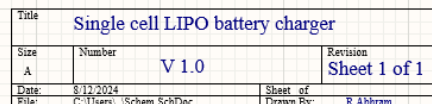


**Figure 8:** PCB 3D Render



## PROJECT 3: DESIGN OF A USB LIPO CHARGER MODULE MCU IN ALTUM

## Single cell LIPO battery charger

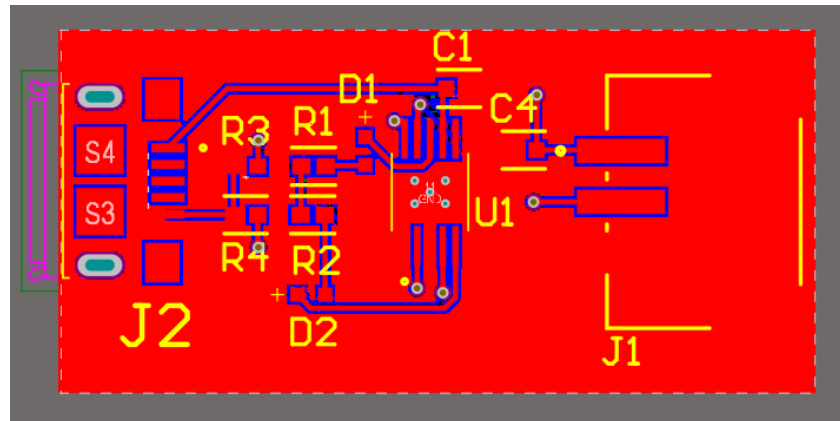


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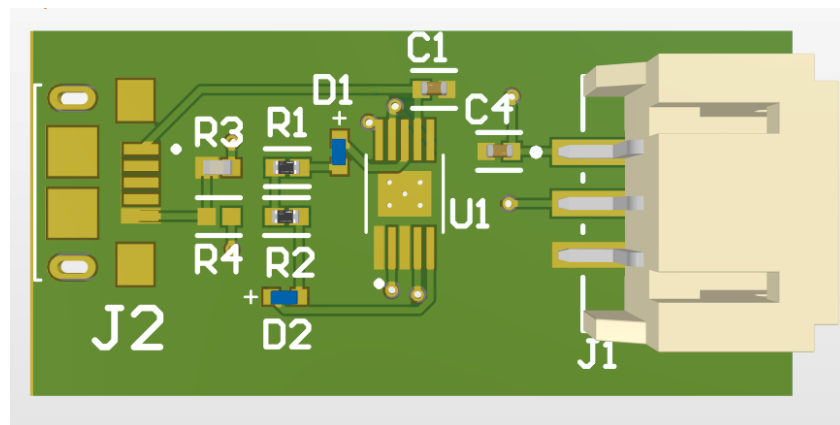


## B. Schematic Design:

**C. PCB layout and routing:** Figure 10 and 11 show component placement, PCB routing and 3D render of the board.



**Figure 10:** Component placement, routing and pores



**Figure 11:** PCB 3D Render

**D. Summary:** To improve the board it would be useful to add ESD protection. Castellated pins would also be useful so that the module may be mounted to additional boards to act as an integrated charger. In order to make the board smaller, the JST connector can be removed and instead replaced by pads to solder jumpers on to. Furthermore, designators may optionally be removed from the boards silkscreen to reduce spacing between components - especially if boards are going to be machine assembled. It may also be worth optimizing component replacement to reduce track length and produce a more 'beautiful' pcb.