

8085-MNEMONICS TABLE

Note : (n - 8 bit Data or Address, nn - 16 bit Data or Address)

	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E
0	NOP	LXI B	STAX B	INX B	INR B	DCR B	MVI B, n	RLC		DAD B	LDAX B	DCX B	INR C	DCR C	MVI C, n
1		LXI D	STAX D	INX D	INR D	DCR D	MVI D, n	RAL		DAD D	LDAX D	DCX D	INR E	DCR E	MVI E, n
2	RIM	LXI H	SHLD nn	INX H	INR H	DCR H	MVI H, n	DAA		DAD H	LHLD nn	DCX H	INR L	DCR L	MVI L, n
3	SIM	LXI SP	STA nn	INX SP	INR M	DCR M	MVI M, n	STC		DAD SP	LDA nn	DCX SP	INR A	DCR A	MVI A, n
4	MOV B, B	MOV B, C	MOV B, D	MOV B, E	MOV B, H	MOV B, L	MOV B, M	MOV B, A	MOV C, B	MOV C, C	MOV C, D	MOV C, E	MOV C, H	MOV C, L	MOV C, M
5	MOV D, B	MOV D, C	MOV D, D	MOV D, E	MOV D, H	MOV D, L	MOV D, M	MOV D, A	MOV E, B	MOV E, C	MOV E, D	MOV E, E	MOV E, H	MOV E, L	MOV E, M
6	MOV H, B	MOV H, C	MOV H, D	MOV H, E	MOV H, H	MOV H, L	MOV H, M	MOV H, A	MOV L, B	MOV L, C	MOV L, D	MOV L, E	MOV L, H	MOV L, L	MOV L, M
7	MOV M, B	MOV M, C	MOV M, D	MOV M, E	MOV M, H	MOV M, L	HLT	MOV M, A	MOV A, B	MOV A, C	MOV A, D	MOV A, E	MOV A, H	MOV A, L	MOV A, M
8	ADD B	ADD C	ADD D	ADD E	ADD H	ADD L	ADD M	ADD A	ADC B	ADC C	ADC D	ADC E	ADC H	ADC L	ADC M
9	SUB B	SUB C	SUB D	SUB E	SUB H	SUB L	SUB M	SUB A	SBB B	SBB C	SBB D	SBB E	SBB H	SBB L	SBB M
A	ANA B	ANA C	ANA D	ANA E	ANA H	ANA L	ANA M	ANA A	XRA B	XRA C	XRA D	XRA E	XRA H	XRA L	XRA M
B	ORA B	ORA C	ORA D	ORA E	ORA H	ORA L	ORA M	ORA A	CMP B	CMP C	CMP D	CMP E	CMP H	CMP L	CMP M
C	RNZ	POP B	JNZ nn	JMP nn	CNZ nn	PUSH B	ADI n	RST 0	RZ	RET	JZ nn		CZ nn	CALL nn	ACI n
D	RNC	POP D	JNC nn	OUT n	CNC nn	PUSH D	SUI n	RST 2	RC		JC nn	IN n	CC nn		SBI n
E	RPO	POP H	JPO nn	XTHL	CPO nn	PUSH H	ANI n	RST 4	RPE	PCHL	JPE nn	XCHG	CPE nn		XRI n
F	RP	POP PSW	JP nn	DI	CP nn	PUSH PSW	ORI n	RST 6	RM	SPHL	JM nn	EI	CM nn		CPI nn

ess)
F
RRC
RAR
CMA
CMC
MOV C, A
MOV E, A
MOV L, A
MOV A, A
ADC A
SBB A
XRA A
CMP A
RST 1
RST 3
RST 5
RST 7