Rachel Beddor

rmbeddor@gmail.com • US CITIZEN

OBJECTIVE

Seeking a full-time embedded design position to engage my technical and creative skills to design innovative hardware-software products.

EDUCATION

Georgia Institute of Technology

May 2018

Bachelor of Science in Computer Engineering

- Certificate in Chinese Language
- Capstone Project: Drone Based Aircraft Inspection, sponsored by Honeywell

PROFESSIONAL EXPERIENCE

Microchip Technology

Chandler, AZ

8-bit PIC & AVR MCU Outbound Mass Market

Product Marketing Engineer

- June 2018 Present
- Redefining user experience for professional embedded design development tools, products and solutions
- Concentrating on key IoT hardware design elements: microcontroller, secure element, and network controller
- Writing outbound marketing material for web, promotions, and social media to improve digital engagement
- Started an influencer marketing program to nurture long-lasting relationships with the mass market
- Published article on IoT connectivity for embedded devices in All About Circuits publication

Keysight Technologies

Colorado Springs, CO

Formerly Agilent Technologies; Oscilloscopes and Protocol Division

Product Marketing Engineer Intern

Summers 2016, 2017

- Launched ARINC 429, MIL-STD-1553, I2S, Manchester and SpaceWire bus protocol decode software for oscilloscopes
- Interfaced with customers and R&D in U.S., Europe, India and Malaysia to develop quality products
- Wrote datasheets, filmed informational videos, published blogs and designed product pages now used on keysight.com
- Directed meetings with key customers in the Aerospace/Defense industry to forge partnerships
- Edited datasheets and consulted on writing style for product engineers

Georgia Tech Office of Information Technology (OIT)

Atlanta, GA

Technology Support Center - 15 hours per week

Assistant Project Manager

Spring 2016 - Fall 2017

- Determined strategies to resolve OIT website inefficiencies
- Applied skills in database systems and programming for web development on drupal platform
- Initiated project to replace Loaner Laptop Program website to increase user-friendliness

RELEVANT COURSEWORK

- Digital Signal Processing: Foundations of discrete-time signal processing and linear systems
- Programming Hardware/Software Systems: Hardware programming through assembly language
- Digital Design Laboratory: FPGA programming using VHDL
- Physical Foundations of CompE: Concepts of semiconductor physics
- Circuit Analysis: Fundamentals of linear circuits
- Computer Microarchitecture: Architecture, concurrency, and energy in computation
- VLSI and Advanced Digital Design: Integrated circuit design with implementation through Cadence software
- Advanced VLSI Systems: Complex VLSI systems with emphasis on how they are incorporated into a silicon environment
- Embedded System Design: hardware/software design for embedded X86/ARM systems
- Spring 2018 Courses: Data Analytics for CompE, IC Fabrication, Intro Materials Science

SKILLS/ACTIVITIES

Software: C, C++, Objective C, C#, Java, JavaScript, Python, MATLAB, HTML, Linux/Unix, SQL, Drupal

Hardware: PIC, AVR, MIPS, VLSI, Embedded Systems, Secure Elements and HSMs, 8-bit MCU, 16-bit MCU

Protocol: SpaceWire, SpaceFibre, I2S, Ethernet, ARINC429, MIL-STD-1553, UART, USB, I2C, SPI, Wi-Fi, BLE, LoRa

Communication: Marketing, Project Management, Intermediate Chinese, UI, Technical Writing, Applications

Extra-Curricular: Alpha Phi Sorority, Engineers without Borders - Navajo Nation, Cristo Rey Volunteer Tutoring, Technique (Newspaper)