

1:1 Bhagath/Menno

Bhagath,
2022

restricted



Week 26/27

Projects	Activities	Challenges	Feedback
TX baseline	Mini-pac design	Model, time needed for design, design flow, design exploration	
	LP measurement coordination	Ambiguity in requirements, changing plans on the fly.	LDMOS validation could be done in first place. When setting up benchmarks: 1 step/goal at a time.
	Mini-pac CV activity coordination		
	Data analysis and read-out	Data format and initial templates	
	Model vs measurement ?		
RF lab	PCB- pac design → Module design → IC design (ex: Integrated matching)		Design path
	Supplier contacts, discussion & follow-up, RFQ, overview, PO's, Reviews.	New site, no process in place.	PO system in place for Nijmegen
Other	Requirement gathering, coordinating with other labs/sites		
	Quick guide for new comers	Putting together the information in a relevant way	



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