CS251, Spring 2022 Assignment 2: Question 7

Q7) Add the following pairs of 6-bit two's complement binary numbers, giving a 6-bit result (i.e., throw away the carry-out). Also give the signed decimal value of the the 6-bit result (i.e., what is the decimal value of the binary result). Note whether or not an overflow occurred for any addition

110001		101 101
+ 101-001		110 101
25 comp; 011010	2s Comp	100 010
decimal: Z6	decount	
Oversion: Yes	Over flow Na	
0-01 111	010 101	
(11) 000	00, 111	
25 compi 000 111	25 Comps 100 100	
Decruir 7	decoman; -28	
Over FLOW : NO	Over flows yes	