The files described here pertain to my Amazon book “Build Your Own BASIC – From Scratch”:

1. ASM-BYOC24.PY – Python assembler for BYOC-24 CPU
    a. To assemble Logisim Source: python ASM-BYOC24.PY TB-L -L
       Produces TB-L-PROM.TXT, TB-L-DROM.TXT, and a list file.
    b. To assemble Cyclone V Source: python ASM-BYOC24.PY TBX-C -C
       Produces BYOC-PROM.MIF, BYOC-DROM.MIF, and a list file.
2. TB-L.ASM – Tiny BASIC Logisim Version Source File
3. TBX-C.ASM – Tiny BASIC Extended Cyclone V Version Source File
4. TB-L-PROM.TXT – Tiny BASIC Logisim PROM Image
   To load Logisim Program ROM:
    a. From Navigation Pane, right-click “BYOC-I” and click “Edit Circuit Layout”
    b. Right-click “Program ROM” and click “Load Image…”
    c. Navigate to TB-L-PROM.TXT file and click OK.
5. TB-L-DROM.TXT – Tiny BASIC Logisim DROM:
   To load Logisim Data ROM
    a. From Navigation Pane, right-click “BYOC-I” and click “Edit Circuit Layout”
    b. Right-click “Data ROM” and click “Load Image…”
    c. Navigate to TB-L-DROM.TXT file and click OK.
6. BYOC_24.SOF – BYOC-24 CPU Quartus Programming File
    a. See Chapter 17 for instructions.
7. Quartus Files – For users have read my book “Build Your Own Computer – From Scratch” or are otherwise knowledgeable of Intel Quartus, the BYOC-24 CPU archive file below permits modifying the Cyclone V version of TBX, assembling it with the Python assembler, updating in Quartus the MIF files, starting the Quartus assembler, and programming the Cyclone V in the GX Starter Kit.
    a. BYOC-24.QAR
8. Logisim circuit files
    a. BYOC-24-HI-LO.circ – Logisim circuit preprogrammed with Hi-LOW Guessing Game. Use with Chapter 3 (page 39) testing.
    b. BYOC-FPGA-24.circ – Logisim Tiny BASIC version (FPGA compatible). Use with Chapter 13 (page 97) testing.

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