

TITLE Block Diagrams

PROJECT

Continued from Page

Start

Main Code

7-Seg

Button-1

Button-1-ISR

Button-2

Button-2-ISR

Interrupt-Driven:

Button 1

Button 2

Main Code

Button 1 ISR

Button 2 ISR

Timer 1 ISR

7-Seg Display

Interrupt/Pooling:

Start

ISR Flag

ISR 1

Main Loop

ISR Flag

ISR B2

ISR Flag

ISR Timer

7-Seg Display

Button 1

Button 2

Simple Counter:

init

Button 1

ISR B1

Main Code

Button 2

ISR B2

7-Seg Display

System Block Diagram:

7 Seg

Driver

Crystal

ISP

MP

User Buttons

User LEDs

COMMS

Buzzer

Driver

USB

(UART) (Rx) (Tx)

ESP8266

5V

Voltage

Regulator

3.3V

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SIGNATURE

DATE

DISCLOSED TO AND UNDERSTOOD BY

DATE

PROPRIETARY INFORMATION