

```

rtx
gen_peephole2_1270 (curr_insn, operands)
    rtx curr_insn ATTRIBUTE_UNUSED;
    rtx *operands;

{
    rtx _val = 0;
    HARD_REG_SET _regs_allocated;
    CLEAR_HARD_REG_SET (_regs_allocated);
    start_sequence ();
    operands[2] = GEN_INT(
        exact_log2(INTVAL(operands[1])));
    emit (gen_rtx_PARALLEL (VOIDmode,
        gen_rtxvec (2,
            gen_rtx_SET (VOIDmode,
                operands[0],
                gen_rtx_ASHIFT (SImode,
                    copy_rtx (operands[0]),
                    operands[2])),
            gen_rtx_CLOBBER (VOIDmode,
                gen_rtx_REG (CCmode,
                    17))))) );
    _val = gen_sequence ();
    end_sequence ();
    return _val;
}

```

```

rtx
gen_peephole2_1271 (curr_insn, operands)
    rtx curr_insn ATTRIBUTE_UNUSED;
    rtx *operands;

{
    rtx _val = 0;
    HARD_REG_SET _regs_allocated;
    CLEAR_HARD_REG_SET (_regs_allocated);
    start_sequence ();
    operands[2] = GEN_INT(
        exact_log2(INTVAL(operands[1])));
    emit (gen_rtx_PARALLEL (VOIDmode,
        gen_rtxvec (2,
            gen_rtx_SET (VOIDmode,
                operands[0],
                gen_rtx_ASHIFT (DImode,
                    copy_rtx (operands[0]),
                    operands[2])),
            gen_rtx_CLOBBER (VOIDmode,
                gen_rtx_REG (CCmode,
                    17))))) );
    _val = gen_sequence ();
    end_sequence ();
    return _val;
}

```