

TITLE: Copernicus-DIP-Breakout-v11

SFE

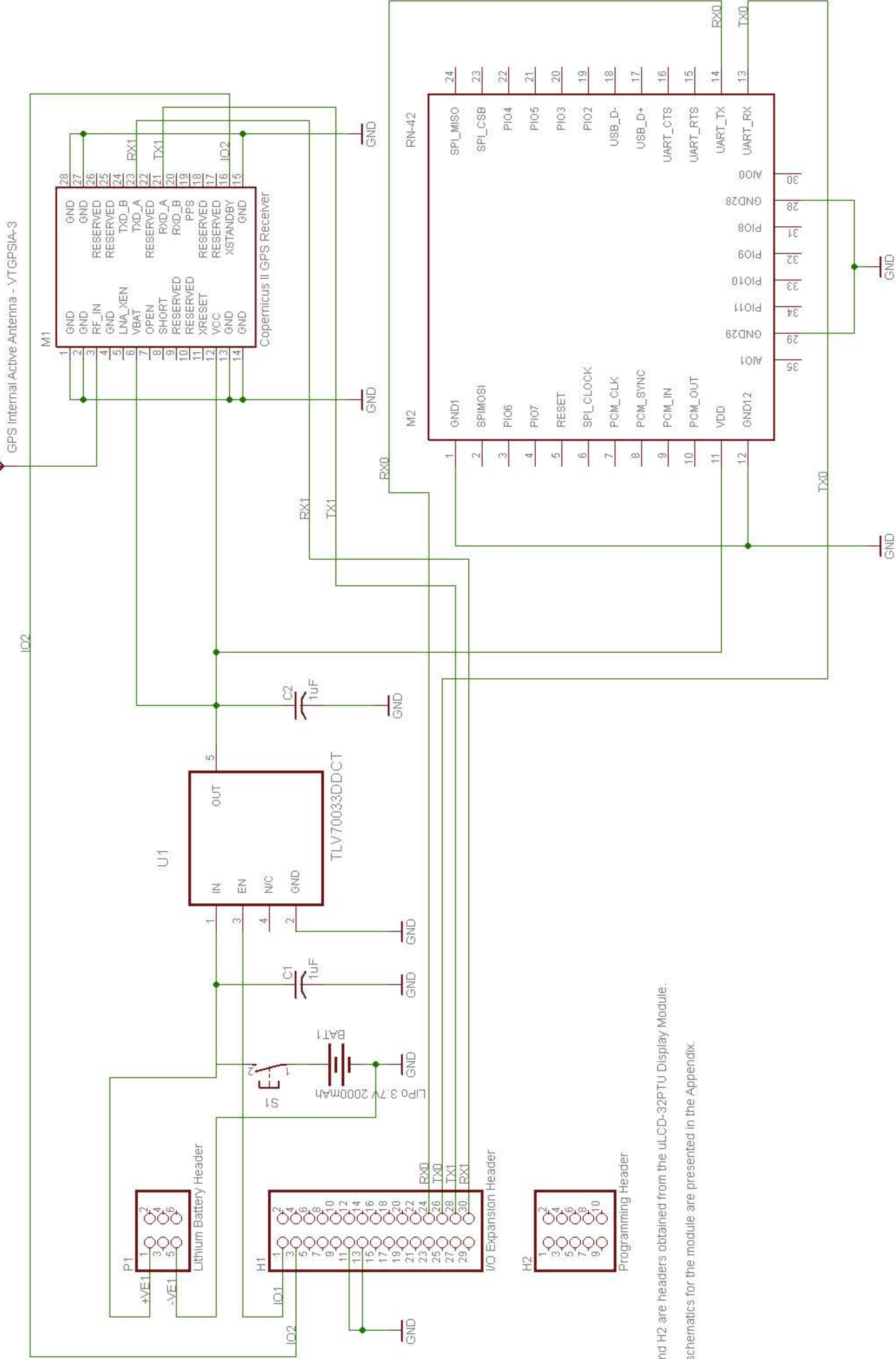
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ANT1 SMA connection and circuitry portrayed presented in more detail in the evaluation board for the GPS.



P1, H1, and H2 are headers obtained from the uLCD-32PTU Display Module.
Detailed schematics for the module are presented in the Appendix.

