

CALL FOR PAPER

2019 Twelfth International Conference on Contemporary Computing (IC3-2019)

August 8-10, 2019 [IEEE Conference Record Number: 46690]

Jaypee Institute of Information Technology, NOIDA, India,

Technically Co-Sponsored by: IEEE Computer Society, USA and IEEE Technical Committee on Parallel Processing (TCPP), USA

<http://www.ic3conf.net/index.html>

The International Conference on Contemporary Computing (IC3) is being jointly organized by **Jaypee Institute of Information Technology, Noida, India** and **University of Florida, Gainesville, USA** annually since 2008. The conference tracks characterize core developments in contemporary areas of computer science. Like past eleven editions, IC3-2019 aims to bring together researchers and practitioners from academia, industry and government to deliberate upon the algorithmic, systemic, applied, and educational aspects of contemporary computing. The conference which is held in NOIDA (outskirts of Indian National Capital, New Delhi), India, typically features multiple eminent keynote speakers from academia and industry as well as presentation of more than 100 peer-reviewed papers and exhibits.

From 2009 onwards, the IC3 proceedings are indexed by DBLP, SCOPUS, and Google Scholar. According to Google Scholar, IC3's h5 index is 14 and h5 median is 21.

The publishers of the previous proceedings have been IEEE Xplore, USA (2013-2016), CCIS-Springer, Germany (2009-2012), and McMillan, India (2008).

Important Dates:

Full Paper Submission: ~~01st May, 2019~~ ~~07th May, 2019~~ **09th May, 2019**
(Hard dead line, no future extension)

Author notification: 15th June, 2019

Final Camera Ready Submission: 01st July, 2019

Conference Tracks:

Contemporary Computing aspects have been organized around four different thematic tracks namely (i) Algorithms, (ii) Systems (Hardware & Software), (iii) Applications, and (iv) Education

Keynote Speakers:

1. Prof. Bharat Bhargva, Purdue university, USA
2. Prof. Inderjit S. Dhillon, University of Texas at Austin, USA
3. Prof. Kavita Bala, Cornell University, New York, USA
4. Christos Douligeris, University of Piraeus, Greece
5. Raghu Kacker, National Institute of Standards and Technology, Gaithersburg, USA

More are yet to confirm.

Paper Submission Guidelines:

Authors are invited to submit manuscripts that demonstrate original unpublished research. Papers are limited to 6 double column single spaced pages (IEEE Format). Please see the conference website for paper submission procedures and detailed guidelines. Authors are advised to ensure that their papers are free of intentional as well as unintentional plagiarism. All submitted papers will be checked for the similarity score with the published literature using *iThenticate* services by EDAS. All papers with similarity score of more than 20 are likely to be rejected without review. Other papers will be peer reviewed on the basis of their clarity, originality, relevance and significance.

Conference Organization:

General Co-Chairs

Prof. Sartaj Sahni, University of Florida, USA

Prof. Vikas Saxena, IIIT Noida, India

Program Chair:

Prof. Sundaraja Sitharama Iyengar, Florida International University, USA

Track Co-Chairs

Track-1: Algorithms

Prof. NingXie, Florida International University, USA

Prof. Debajyoti Bera, Indraprastha Institute of Information Technology, Delhi, India

Track-2: Applications

Prof. Shu-Ching Chen, Florida International University, USA

Prof. Sanjukta Bhowmick, University of Nebraska, Omaha, USA

Track-3: Systems

Prof. Shobha G, R V College of Engineering, Bangalore, India

Prof. Ramesh Agrawal, Jawaharlal Nehru University, New Delhi, India

Track-4: Education

Prof. Sushil Prasad, Georgia State University, Atlanta, GA, USA

Prof. Steven Bogaerts, DePauw University, Greencastle, IN, USA

Publication Co-Chairs

Dr. Sandeep Kumar Singh, IIIT Noida, India

Dr. Manish K Thakur, IIIT, Noida

Publicity Co-Chairs

Prof. Mario Antonio Ribeiro Dantas, Federal University of Juiz de Fora, Brazil

Dr. Sangeeta Mittal, IIIT, Noida, India

Dr. Ankit Vidyarthi, IIIT, Noida, India

Registration Chair

Dr. Kavita Pandey, IIIT, Noida

Web Administration

Dr. Raghu Vamsi P, IIIT Noida, India

Deepti Singh, IIIT Noida, India

Conference location: Jaypee Institute of Information Technology

A-10, Sector-62, NOIDA (Outskirt of New Delhi), India

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Partial list of areas of interest for each of the tracks but not limited to:

Track-1: Applications	Track-2: Algorithms	Track-3: Systems	Track-4: Education
<ul style="list-style-type: none"> • Machine Learning • Big Data processing and applications • Artificial Intelligence • Deep Learning • Natural Language Processing • Cognitive Computing • Artificial General Intelligence • Data mining, Information retrieval • Computer vision, Image processing • Pattern recognition • Audio and speech processing • Computational science applications • Scientific computing applications • E-commerce applications, Web services • Cloud computing applications • Biomedical applications • Emerging applications in Healthcare and Engineering • Smart Cities 	<ul style="list-style-type: none"> • Parallel Algorithms and Distributed Algorithms • Combinatorial Algorithms and Graph Algorithms • Scheduling and Load Balancing Algorithms • Randomized, Approximation, Parameterized Algorithms • On-line and Streaming Algorithms • Locality-Aware, Power/Energy-Aware Algorithms • Optimization Algorithms • Complexity Theory • Cryptography and Theoretical Aspects of Security and Privacy • Fault-tolerant Algorithms • Network and Peer-to-Peer Algorithms • Evolutionary & Nature Inspired Algorithms • Bioinformatics Algorithms and Computational Biology • Quantum Computing • Algorithm Engineering • Algorithmic Game Theory • Computational Finance • Computational Geometry 	<ul style="list-style-type: none"> • Ad hoc, Sensor, Vehicular networks • Cloud, Cluster, Grid, Distributed and P2P Computing, virtualization • Internet of Things • Fog Computing • Blockchain Systems • Cryptography and Applied Mathematics • Software Defined Networks • Embedded Systems and Robotics • Embedded Systems and VLSI • Multi-FPGA reconfigurable systems and architectures • Enterprise, data centre, and storage-area networks • Performance evaluation of networks and distributed systems • High Performance Computing • Heterogeneous Computing Models and Systems • Intelligent Systems, Next generation Internet • Parallel and Multi-core Computing • System Security, Trust and Privacy • Smart phones and Security • Social Network behavior, Modeling and Analysis • 5G Communication 	<ul style="list-style-type: none"> • Computing and Data Science Literacy across all Science, Technology, and Social Science Disciplines • Introductory Computer Science Course Sequence • Parallel, Distributed and High Performance Computing courses, Computational Science courses • Computer Engineering and Computational Engineering courses • Curricular Issues in Computing Programs • Pedagogy for Computing courses • Systems, Networks, and Architecture courses • Programming Language and Tools • Algorithms, Automata and Discrete Math courses • Novel Elective courses, Cyber Security courses • Experience and Case Study reports • Laboratory, Projects, and Internship courses • Collaborative work and Peer learning • Integrated Multi-Disciplinary Curriculum • IT Entrepreneurship Education • Assessment Methodology • Employers' Experiences with and Expectation of Graduating Students.