Featherlight Reuse-distance Measurement

APPENDIX

A. TIME REUSE TO STACK REUSE

RDX allocates several bins to include every sampled reuse pair with time distance falling into different ranges. Common to prior work [1, 2], the histogram uses the *logarithmic* scale for the bin size as the horizontal axis and the percentage of total sampled reuse pairs as the vertical axis.

With the time distance histogram, RDX leverages the existing technique [1] to approximate the reuse distance histogram. This section only shows the intuitive idea of this approximation. Considering a memory access sequence aXXXXa, the time distance of a is 5 while we have no idea how many distinct elements between a. If three time reuses are of distance 1 and one reuses is of distance 5, we definitely know there should be only two distinct elements between a by exhaustive searching.

However, this greedy method usually does not work for calculating the reuse distance of a common application, especially directly calculating the reuse distance of the specific reuse instance of an element. Thus we seek to a statistical model to calculate the probability of a specific reuse distance instead of obtaining the reuse distance of a specific reuse instance. Assuming that the probability of a data element is independent from others, whether a data element is accessed in a given time interval Δ is actually a Bernoulli process. Then the probability of having k distinct data elements in this Δ interval is

$$P(k, \Delta) = \binom{N}{k} P_{interval}(\Delta)^k (1 - P_{interval}(\Delta))^{(N-k)}$$

where $P_{interval}(\Delta)$ is the probability for a data element to appear in the interval Δ and N is total number of distinct data elements. To calculate $P_R(k)$ (the probability of having reuse distance k for the entire program), we need to consider all the possibilities of having reuse distance of k from the interval length ranging between 1 and T, where T is the total number of memory accesses. The probability of having time interval Δ can be obtained from the time reuse histogram, denoted as $P_T(\Delta)$. Thus we have

$$P_R(k) = \sum_{\Delta=1}^{T} P(k, \Delta) P_T(\Delta)$$
 (2)

Since $P_{interval}(\Delta)$ can be derived from $P_T(\Delta)$ and the details can be found in [3], Equation (2) transforms time reuse histogram to stack reuse histogram.

Their later work [1] further developed the algorithm by extending each bar width of both time and stack reuse histograms from 1 to any arbitrary number, which is utilized in our work. Their evaluation shows that this model gives more than 99% accuracy as to cache block granularity.

B. INPUT OF SPEC CPU BENCHMARKS

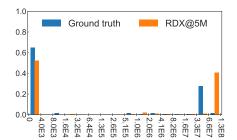
We use ref input to run all SPEC CPU benchmarks but some have multiple inputs, which are distinguished with numerical suffixes such as gcc-1, gcc-2, etc. The actual mapping of these names are shown in Figure 1 and 2.

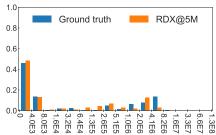
C. STACK REUSE HISTOGRAMS OF SPEC CPU 2006 BENCHMARKS WITH OFF-BY-ONE PROBLEM

In SPEC CPU2006, bwaves (Figure 1), leslie3d (Figure 2), sphinx3 (Figure 3), GemsFDTD (Figure 4), lbm (Figure 5), and cactusADM (Figure 6) have the off-by-one problem, which can be inferred from their stack reuse histograms.

D. STACK REUSE HISTOGRAMS OF SPEC CPU2017

We have plotted the stack reuse histograms of perlbench_r (Figure 7, Figure 8 and Figure 9), gcc_r (Figure 10, Figure 11, Figure 12, Figure 13 and Figure 14), bwaves_r (Figure 15, Figure 16, Figure 17 and Figure 18), mcf_r (Figure 19), cactuBSSN_r (Figure 20), namd_r (Figure 21), povray_r (Figure 22), lbm_r (Figure 23), omnetpp_r (Figure 24), wrf_r (Figure 25), xalancbmk_r (Figure 26), x264_r (Figure 27, Figure 28 and Figure 29), blender_r (Figure 30), cam4_r (Figure 31), deepsjeng_r (Figure 32), imagick_r (Figure 33), leela_r (Figure 34), nab_r (Figure 35), exchange2_r (Figure 36), fotonik3d_r (Figure 37), roms_r (Figure 38), xz_r (Figure 39, Figure 40 and Figure 41), perlbench_s (Figure 42, Figure 43 and Figure 44), gcc_s (Figure 45, Figure 46 and Figure 47), bwaves_s (Figure 48 and Figure 49), mcf_s (Figure 50), cactuB-





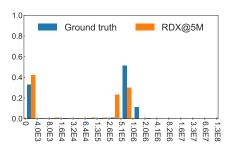


Figure 1: Stack reuse histogram of bwaves.

Ground truth

0.4

0.2

RDX@5M

Figure 2: Stack reuse histogram of leslie3d.

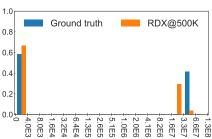


Figure 3: Stack reuse histogram of sphinx3.

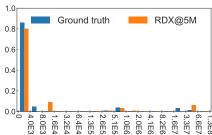


Figure 4: Stack reuse histogram of GemsFDTDs.

.3E5

1.6E7 8.2E6 4.1E6 2.0E6 1.0E6 5.1E5

Figure 5: Stack reuse histogram of 1bm.

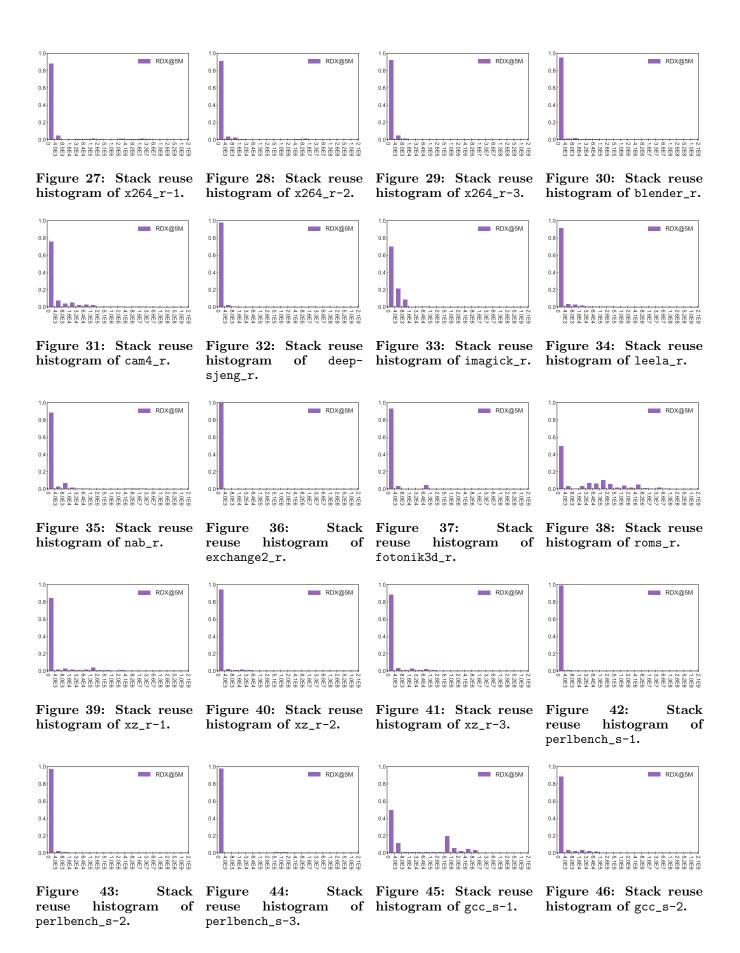
Figure 6: Stack reuse histogram of cactus ADM.

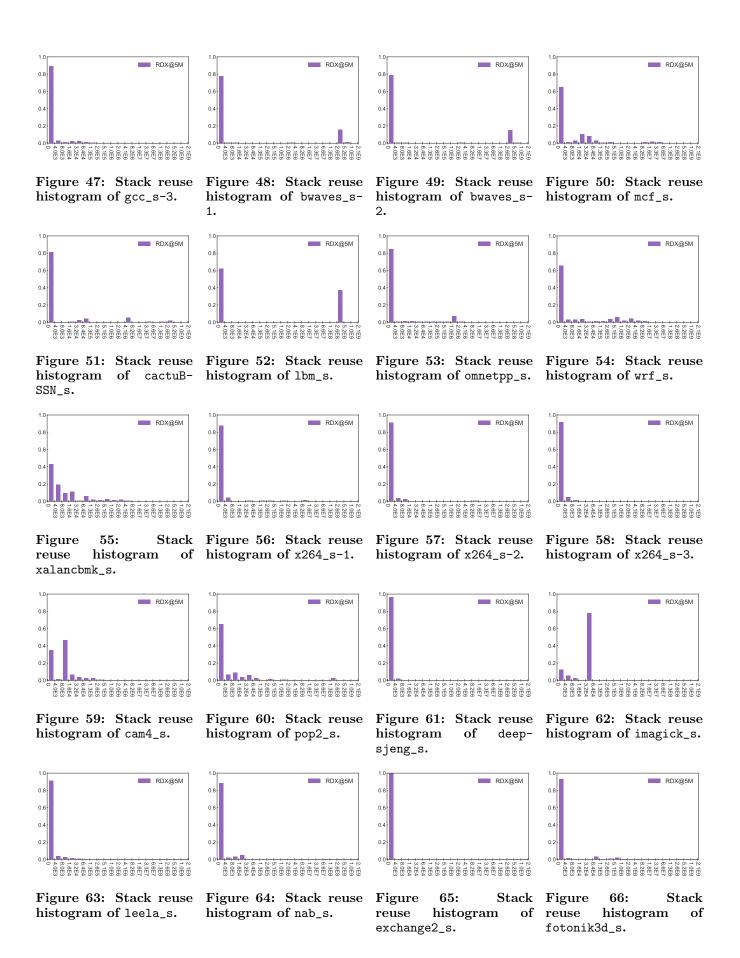
SSN_s (Figure 51), 1bm_s (Figure 52), omnetpp_s (Figure 53), wrf_s (Figure 54), xalancbmk_s (Figure 55), x264_s (Figure 56, Figure 57 and Figure 58), cam4_s (Figure 59), pop2_s (Figure 60), deepsjeng_s (Figure 61), imagick_s (Figure 62), leela_s (Figure 63), nab_s (Figure 64), exchange2_s (Figure 65), fotonik3d_s (Figure 66), roms_s (Figure 67), xz_s (Figure 68 and Figure 69) from SPEC CPU2017.

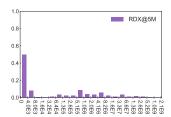
5. REFERENCES

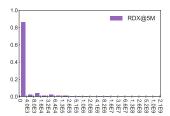
- X. Shen, J. Shaw, and B. Meeker, "Accurate approximation of locality from time distance histograms," tech. rep., Technical Report TR902, Computer Science Department, University of Rochester, 2006.
- [2] E. Berg and E. Hagersten, "Statcache: a probabilistic approach to efficient and accurate data locality analysis," in Performance Analysis of Systems and Software, 2004 IEEE International Symposium on-ISPASS, pp. 20–27, IEEE, 2004
- [3] X. Shen, J. Shaw, B. Meeker, and C. Ding, "Locality approximation using time," in Proc. of the 34th annual ACM SIGPLAN-SIGACT Symposium on Principles Of Programming Languages, POPL '07, (New York, NY, USA), pp. 55-61, ACM, 2007.











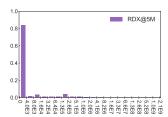


Figure 67: Stack reuse histogram of roms_s.

Figure 68: Stack reuse histogram of xz_s-1.

Figure 69: Stack reuse histogram of xz_s-2.

Benchmark	Input argument
perlbench-1	-I./lib checkspam.pl 2500 5 25 11 150 1 1 1 1
perlbench-2	-I./lib diffmail.pl 4 800 10 17 19 300
perlbench-3	-I./lib splitmail.pl 1600 12 26 16 4500
bzip2-1	input.source 280
bzip2-2	chicken.jpg 30
bzip2-3	liberty.jpg 30
bzip2-4	input.program 280
bzip2-5	text.html 280
bzip2-6	input.combined 200
gcc-1	166.i -o 166.s
gcc-2	200.i -o 200.s
gcc-3	c-typeck.i -o c-typeck.s
gcc-4	cp-decl.i -o cp-decl.s
gcc-5	expr.i -o expr.s
gcc-6	expr2.i -o expr2.s
gcc-7	g23.i -o g23.s
gcc-8	s04.i -o s04.s
gcc-9	scilab.i -o scilab.s
gamess-1	< cytosine.2.config
gamess-2	< h2ocu2+.gradient.config
gamess-3	< triazolium.config
gobmk-1	quietmode gtp" < 13x13.tst
gobmk-2	quietmode gtp" < nngs.tst
gobmk-3	quietmode gtp" < score2.tst
gobmk-4	quietmode gtp" < trevorc.tst
gobmk-5	quietmode gtp" < trevord.tst
soplex-1	-s1 -e -m45000 pds-50.mps
soplex-2	-m3500 ref.mps
hmmer-1	nph3.hmm swiss41
hmmer-2	fixed 0mean 500num 500000sd 350 seed 0 retro.hmm
h264ref-1	-d foreman_ref_encoder_baseline.cfg
h264ref-2	-d foreman_ref_encoder_main.cfg
h264ref-3	-d sss_encoder_main.cfg
astar-1	BigLakes2048.cfg
astar-2	rivers.cfg

Table 1: The input arguments of benchmarks which have several ref inputs for SPEC CPU 2006.

Benchmark	Input argument
11 1 1	
	-I./lib checkspam.pl 2500 5 25 11 150 1 1 1 1
peribench_r-3	-I./lib diffmail.pl 4 800 10 17 19 300 -I./lib splitmail.pl 6400 12 26 16 100 0
peribencii_1-3	qcc-pp.c -03 -finline-limit=0 -fif-conversion -fif-
gcc_r-1	conversion2 -o gcc-pp.opts-03 -finline-limit 0 -fif-
	conversion -fif-conversion2.s
	gcc-pp.c -02 -finline-limit=36000 -fpic -o gcc-pp.opts-
gcc_r-2	02finline-limit_36000fpic.s
gcc_r-3	gcc-smaller.c -03 -fipa-pta -o gcc-smaller.opts-03fipa-
	pta.s
gcc_r-4	ref32.c -05 -o ref32.opts-05.s ref32.c -03 -fselective-scheduling -fselective-scheduling2
gcc_r-5	-o ref32.opts-03fselective-schedulingfselective-
	scheduling2.s
bwaves_r-1	bwaves_1 < bwaves_1.in
bwaves_r-2	bwaves_2 < bwaves_2.in
bwaves_r-3	bwaves_3 < bwaves_3.in
bwaves_r-4	bwaves_4 < bwaves_4.in
x264_r-1	pass 1stats x264_stats.logbitrate 1000frames 1000 -o BuckBunny New.264 BuckBunny.yuv 1280x720
	pass 2stats x264_stats.logbitrate 1000dumpyuv
x264_r-2	200frames 1000 -o BuckBunny_New.264 BuckBunny.yuv
	1280x720
x264_r-3	seek 500dumpyuv 200frames 1250 -o BuckBunny New.264 BuckBunny.yuv 1280x720
	cld.tar.xz 160
xz_r-1	19cf30ae51eddcbefda78dd06014b4b96281456e078ca7c13e1c0c9e6a
	aea8dff3efb4ad6b0456697718cede6bd5454852652806a657bb56e07d
	61128434b474 59796407 61004416 6
	cpu2006docs.tar.xz 250 055ce243071129412e9dd0b3b69a21654033a9b723d874b2015c774fac
xz_r-2	1553d9713be561ca86f74e4f16f22e664fc17a79f30caa5ad2c04fbc44
	7549c2810fae 23047774 23513385 6e
xz_r-3	input.combined.xz 250
	a841f68f38572a49d86226b7ff5baeb31bd19dc637a922a972b2e6d125 7a890f6a544ecab967c313e370478c74f760eb229d4eef8a8d2836d233
	d3e9dd1430bf 40401484 41217675 7
perlbench s-1	-I./lib checkspam.pl 2500 5 25 11 150 1 1 1 1
perlbench s-2	
-	
perlbench_s-3	-I./lib splitmail.pl 6400 12 26 16 100 0
gcc_s-1	gcc-pp.c -05 -fipa-pta -o gcc-pp.opts-05fipa-pta.s
gcc_s-2	gcc-pp.c -05 -finline-limit=1000 -fselective-scheduling - fselective-scheduling2 -o gcc-pp.opts-05 -finline-
	limit_1000fselective-schedulingfselective-
	scheduling2.s
gcc_s-3 bwaves s-1	gcc-pp.c -05 -finline-limit=24000 -fgcse -fgcse-las -
	fgcse-lm -fgcse-sm -o gcc-pp.opts-05finline- limit_24000fgcsefgcse-lasfgcse-lmfgcse-sm.s
	bwaves 1 < bwaves 1.in
bwaves_s-1 bwaves s-2	
	bwaves_2 < bwaves_2.inpass 1stats x264 stats.logbitrate 1000frames
x264_s-1	1000 -o BuckBunny_New.264 BuckBunny.yuv 1280x720
	pass 2stats x264_stats.logbitrate 1000dumpyuv
x264_s-2	200frames 1000 -o BuckBunny_New.264 BuckBunny.yuv
	1280x720
x264_s-3	seek 500dumpyuv 200frames 1250 -o BuckBunny New.264 BuckBunny.yuv 1280x720
xz_s-1	cpu2006docs.tar.xz 6643
	055ce243071129412e9dd0b3b69a21654033a9b723d874b2015c774fac
	1553d9713be561ca86f74e4f16f22e664fc17a79f30caa5ad2c04fbc44
	7549c2810fae 1036078272 1111795472 4 cld.tar.xz 1400
xz_s-2	cid.tar.xz 1400 19cf30ae51eddcbefda78dd06014b4b96281456e078ca7c13e1c0c9e6a
	aea8dff3efb4ad6b0456697718cede6bd5454852652806a657bb56e07d
	61128434b474 536995164 539938872 8

Table 2: The input arguments of benchmarks which have several *ref* inputs for SPEC CPU 2017.