The Digital Forest

Part 1: The Password Guardian Device

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Story

Deep within the heart of the Digital Forest lies an ancient **Password Guardian Device**, a critical system that safeguards the secrets of the forest. This device displays a four-digit code on a seven-segment display, ensuring that only the worthy can access the treasures in the forest.

However, the logic controlling this device has malfunctioned, putting the entire forest at risk. As an expert FPGA engineer, you have been asked to design a new control logic to restore the device's functionality and secure the forest once more.

Mission Objectives

You must design an FPGA-based control logic to meet the following requirements:

Part 1: Counting Logic

1. Counting Mode:

- When Switch[0] and Switch[1] are both 0, pressing Button[0] will increment the number displayed on the four-digit seven-segment display by 1.
- The count should loop from 0000 to 9999 and then back to 0000.

2. Reset Mode:

• When **only Switch**[0] **is set to 1**, the display should reset to 0000, regardless of other inputs.

3. Freeze Mode:

- When only Switch[1] is set to 1, the display should freeze at its current value.
- Pressing Button[0] in this state should have no effect.
- Additionally, **LED**[0] should light up to indicate the system is in freeze mode.

4. Lockdown Mode:

- When **Switch[0]** and **Switch[1]** are both 1, the display should freeze at 0000.
- LED[0] should remain lit, indicating the system is in lockdown mode.

Part 2: Random Password Generation

In this part, the logic will be extended to allow random password generation on the seven-segment display.

1. Random Password Mode:

- When Switch[0] and Switch[1] are both 0, pressing Button[0] should generate a new pseudo-random number between 0000 and 9999 as password.
- Ensure that the password appears instantly on the display with no flickering.

2. Security Mode:

- When only Switch[0] is set to 1, the display should set to 0000 to protect the password, regardless of other inputs.
- The password displayed before entering Security Mode should be saved. When exiting Security Mode (Switch[0] is reset to 0), the display should restore the previously saved password.

3. Freeze Mode:

- When **only Switch**[1] **is set to 1**, the password should freeze at its current value.
- Pressing Button[0] in this state should have no effect.
- Additionally, **LED**[0] should light up to indicate the system is in freeze mode.

4. Lockdown Mode:

- When **Switch[0]** and **Switch[1]** are both 1, the display should freeze at 0000 to enforce lockdown.
- The password displayed before entering Lockdown Mode should be saved. When the device is switched from Lockdown Mode to Freeze Mode, the display should restore the previously saved password.
- LED[0] should remain lit, indicating the system is in lockdown mode.

Constraints

- All the specified logic must be implemented on an FPGA development board using a hardware description language (HDL).
- The implementation should use **switches** and **buttons** as input devices, and **seven-segment displays** and **LED** as output devices.

Challenge Yourself

The fate of the Digital Forest depends on you! Step into the role of the guardian engineer and use your FPGA skills to restore order to the system. Prove yourself as the hero of the Digital Forest!