If EA Pin is set to LOW than for all MFRC522 devices the upper 4 bits of the device bus address are reserved by NXP and set to 0101(bin). The remaining 3 bits (ADR_0, ADR_1, ADR_2) of the Slave Address can freely configured by the customer in order to prevent collisions with other I2C devices.

If EA Pin is set to HIGH than ADR_0 to ADR_5 can be completely specified at the external pins according to Table Table 135 "Connection Scheme for detecting the different Interface Types". ADR_6 is always set to logic 0.

	UART	SPI	I2C
A1	0	0	1
A0	0	1	EA

Date	Rev	Change descriptions	Changer	Approver