

Q2]

Q

① A macro name is an abbreviation which stands for some related lines of code.

Macros are useful for the following purposes:

- i) To simply and reduce the amount of repetitive coding
- ii) To reduce error caused by repetitive coding
- iii) To make an assembly program more readable

② A macro call leads to macro expansion. During macro expansion, the macro statement is replaced by sequence of assembly statements. A macro consists of a name, a set of formal parameters and a body of code.

③ A macro is a unit of specification for program generation through expansion

④ Macro Definition

eg:-

MACRO

start definition
 Name of macro

INCR \$MEM_VAL, \$INC_VAL, \$REG

MOVER \$REG \$MEM_VAL

ADD \$REG \$INC_VAL

MOVER \$REG \$MEM_VAL

MEND

sequence of instructions

End

INCR A, B ← macro call

Macro call : Consists of a name optionally followed by an actual parameter list must be the same as the number of parameters. INCR A, B is an example of macro call used in the example here. If macro has no formal parameters list its call must have no actual parameter list.

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⑤ Macro Expansion

- Replacement of macro call by corresponding sequence of instructions is called as macro expansion.
- To expand a macro, the name of the macro is placed in the operation field and no special directives are necessary.
- The ~~as~~ assembly code sequence INCR A, B, AREG is an example of the macro call.
- The assembler recognizes INCR as the name of the macro expands the macro, and places a copy of the macro definition (along with the parameter substitutions) in the preceding label field denote the expanded code.

Macro call

```
START 100
A DS
B DS
INCR A, B, AREG
PRINT A
STOP
END
```

Macro definition

```
MACRO
INCR &A, &B, AREG
MOVER REG &A
ADD REG &B
MOVEM REG &A
MEND
```

Expanded code

```
START 100
A DS 1
B DS 1
MOVER REG A
ADD REG B
MOVEM REG A
PRINT A
STOP
END
```

A and B are actual parameters
 &A and &B are formal parameters.

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- ⑥ Macro name in the mnemonic field leads to expansion only in a subroutine name in call statement in the program leads to execution
- ⑦ Macros are completely handled by the assembler during assembly time whereas subroutines are completely handled by the hardware at runtime
- ⑧ The hardware knows nothing about macros while the assembler knows nothing about subroutines
- ⑨ The subroutine call instruction is assembled in the usual way and treated by the assembler as any other instruction
- ⑩ Macro processing increases the size of the resulting code but results in faster expansion of program for expanded programs whereas for subroutines do not result into bulk object codes but has substantial overheads of control transfer during execution.
- ⑪ Hardware executes the subroutine call instruction so, it has to know how to save the return address and how to branch to the subroutine.

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Q2]

A]

string: ddede

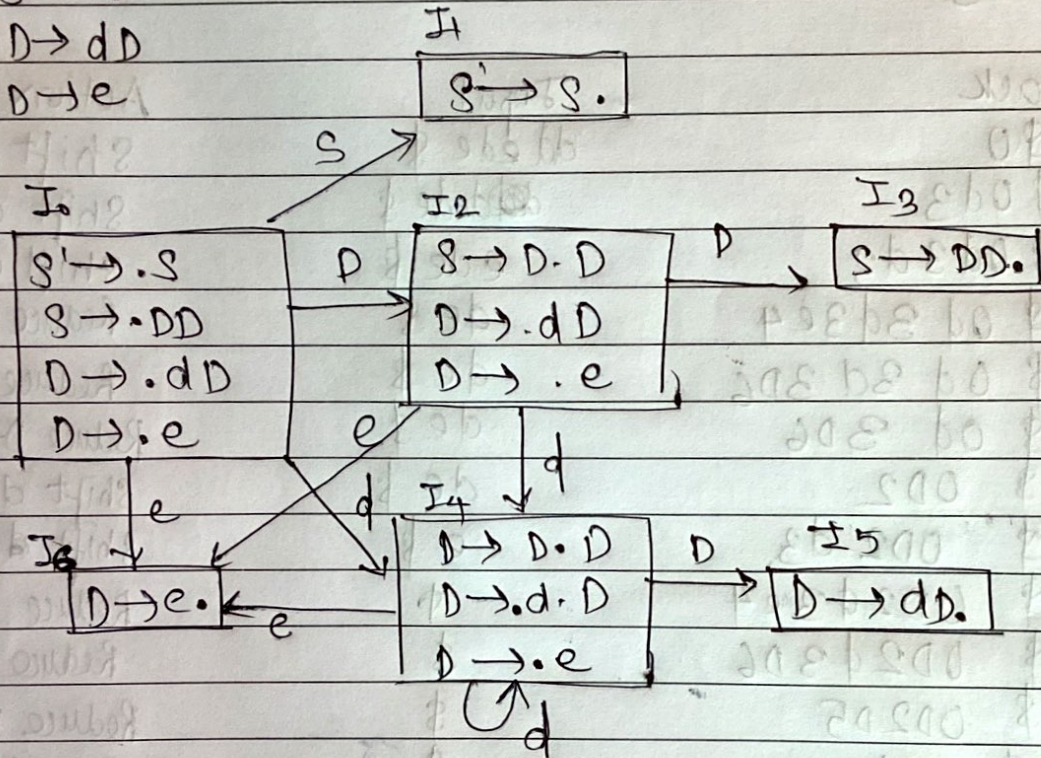
$S' \rightarrow S$

Step 1: Augment the grammar

$S \rightarrow DD$

$D \rightarrow dD$

$D \rightarrow e$



$S' = \{ \$ \}$

$S = \{ \$ \}$

$D = \{ d, e, \$ \}$

Design LR(0) parsing table

		d	e	\$	S	D
0	s3	s4			1	2
1				Accept		
2	s2	s4				5
3	s3	s4				6
4	r3	r3		r3		
5				r1		
6	r2	r2		r2		

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Step 4. Algorithm.

LR parsing table

string: ddede

Stack	Input	Action
\$0	ddede\$	Shift d3
\$0d3	ddede\$	Shift d3
\$0d3d3	ede\$	Shift e4
\$0d3d3e4	de\$	Reduce D \rightarrow e
\$0d3d3D6	de\$	Reduce D \rightarrow dD
\$0d3D6	de\$	Reduce D \rightarrow dD
\$0D2	de\$	Shift d3
\$0D2d3	e\$	Shift e4
\$0D2d3e4	\$	Reduce D \rightarrow e
\$0D2d3D6	\$	Reduce D \rightarrow dD
\$0D2D5	\$	Reduce D \rightarrow dD
\$0\$1	\$	Accept.

Algo

repeat forever

{

let 'x' be stack top number

let 'a' be input symbol

if $M[x, a] = \text{Accept}$ then Accept & Breakelse if $M[x, a] = S_i$ then Shift 'a' and Push 'i'else if $M[x, a] = r_j$ then reduce using r_j &
perform GOTO

else ERROR();

{