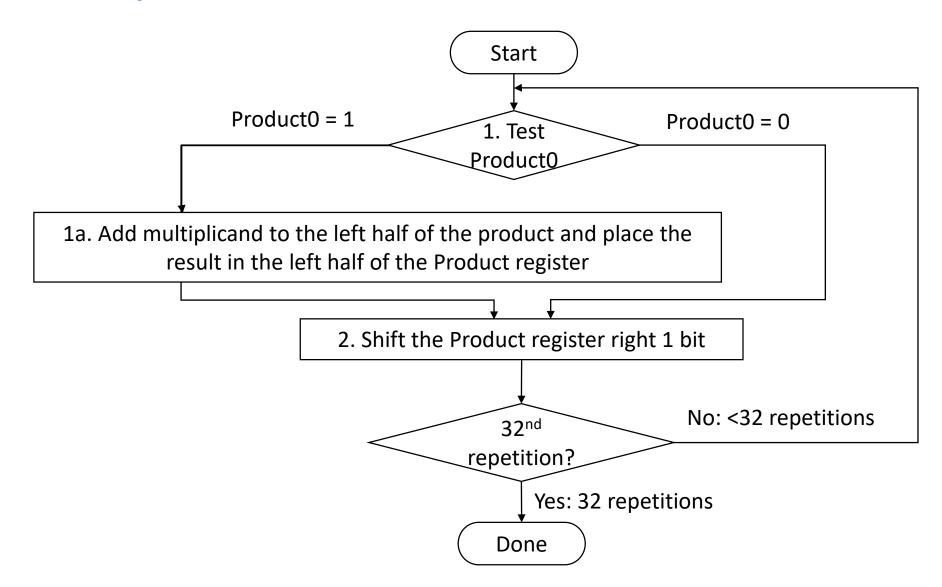
Flowcharts for Refined Version of Hardware

Multiplication



Division

