



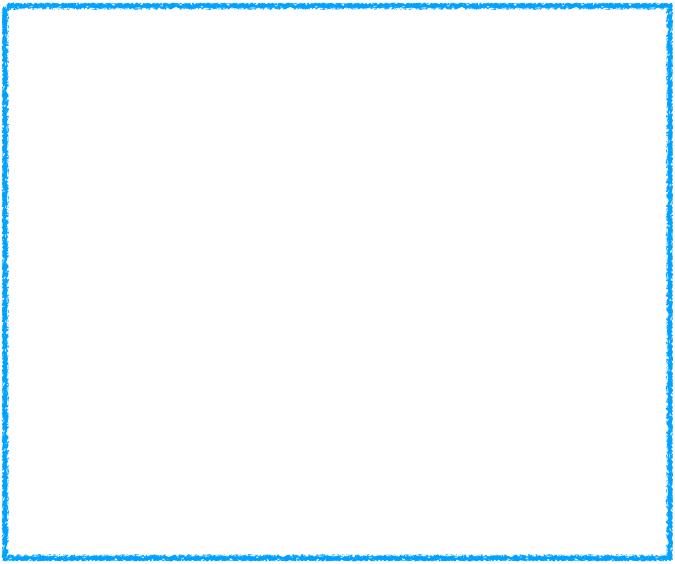


# en trame.

# pc\_begin pc range



# Length



# DWARF instructions

### 



# ength



# DWARF instructions

en trame hdr

# fde\_count

pc\_begin
fde\_offset

pc\_begin
fde\_offset

pc\_begin
fde\_offset

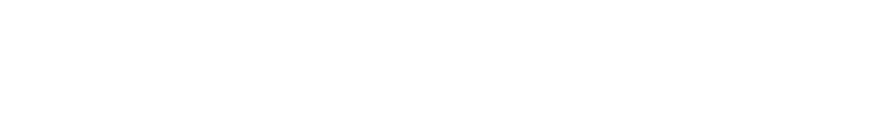


# 

### instructions

### instructions







### DW CFA advance loc: 5

0x12cb: CFA=RBP+16: RBP=[CFA-16], RIP=[CFA-8]

### DW CFA advance loc: 3

### LSDA Address: 000000000000221c

### DW CFA advance loc1: 102

0x12c8: CFA=RSP+16: RBP=[CFA-16], RIP=[CFA-8]

# DW CFA def cfa register: RBP

### CFA DW nop:

0x12d0: CFA=RBP+16: RBX=[CFA-24], RBP=[CFA-16], RIP=[CFA-8]

## DW CFA offset: RBP -16

000000cc 00000024 00000048 FDE cie=000000088 pc=000012c3...00001337

0x1336: CFA=RSP+8: RBX=[CFA-24], RBP=[CFA-16], RIP=[CFA-8]

## DW CFA offset: RBX -24

## DW CFA def cfa: RSP +8

## DW CFA def cfa offset: +16

DWARF32 Format:

0x12c3: CFA=RSP+8: RIP=[CFA-8]