

Design Project

Pseudo-Random Bit Sequence Generator

Prepared by:
Steven Blair
Alex Kapustka
Jeffrey Lee
Alex Wleklinski

*For Access to Files Not Included with This Report
(i.e., Schematics, Layouts, Waveforms, etc.) See:
https://github.com/red-135/ECE482_DesignProject
(Files Maintained by Steven Blair, smbclair2@illinois.edu)*

ECE 482
Digital IC Design
December 7, 2016