RTC chip

3.3V   IC1

1	32KHZ	SCL	16	SCL
2	VCC	SDA	15	SDA
3	SQW/INT	VBAT	14	VBAT
4	/RST	GND	13
5	NC	NC7	12
6	NC1	NC6	11
7	NC2	NC5	10
8	NC3	NC4	9

DS3231

GND   GND

Alternative footprint

3.3V   IC2

1	32KHZ	SCL	8	SCL
2	VCC	SDA	7	SDA
3	SQW	VBAT	6	VBAT
4	RST	GND	5

DS3231M

GND

Caps and pullups

3.3V   3.3V   3.3V   3.3V

C1   C2   R1   R2
100nF   1uF   10K   10K
                SCL   SDA

GND   GND

Right connector (inside mount)

3.3V

	J3		J4	
	1		1	
SDA1	2		2	
SCL1	3		3	
	4		4	
	5		5	
	6		6	
3.3V	7		7	
	8		8	
	9		9	
	10		10	
	11		11	
	12		12	
	13		13	
SDA0	14		14	SCL0
	15		15	
	16		16	
	17		17	
	18		18	
	19		19	
	20		20	

J3   J4

GND   GND

Battery holder

VBAT   BAT2

BS-8-1   GND

VBAT   1   LIPO1
        2
GND   Molex VERT 1.25mm pitch

I2C bus selection

SDA1 SJ1 SDA0   SCL1 SJ2 SCL0
      1   3   1   3
      2   2
SDA   SCL

I2C1 is connected by default

Pi Zero sized RTC-SHIM for Pi's 40-pin extension slot
(c) Andriy Golovnya

TITLE: RTC-zero-SHIM

Document Number:
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REV:
1.0

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