```
abc
                                                                                          ×
                                        ButtonToLEDs.v
📳 | 🐽 📅 | 🏗 🕮 | 🖪 🗗 🐿 | 🐧 🖫 | 🤣 | 257 📃
 1
   □module Counter26BitEnableReset (
 2
         input clock,
         input clear,
         input enable,
 4
5
6
7
8
         input reset,
         output reg[25:0] out);
         always @(negedge clock, posedge clear)
 9
             if (clear)
10
                out \ll 0;
             else if (reset)
11
12
                out <= 0;
13
14
15
            else if (enable)
                out <= out + 1;
16
     endmodule
17
18
    □module Comparator26Bit (
19
         input [25:0] in_1,
input [25:0] in_2,
20
21
22
23
24
25
26
27
         output out);
         assign out = (in_1 == in_2);
     endmodule
    □module ButtonToPulse (
28
29
30
         input clock,
         input clear,
         input button,
31
         output pulse);
32
33
         wire[25:0] out;
34
35
36
37
         Counter26BitEnableReset counter(clock, clear, ~button, button, out);
         Comparator26Bit comparator(out, 26'b1, pulse);
```

38

endmodule

```
□module Counter8BitEnable (
10
11
          input clock,
12
          input clear,
          input enable,
output reg[7:0] out);
13
14
15
16
17
          always @(posedge clear, negedge clock)
              if (clear)
              out <= 0;
else if (enable)</pre>
18
19
50
51
52
53
                  out \leftarrow out + 1;
      endmodule
54
55
    □module ButtonToLEDs (
          input clock,
56
57
          input clear,
          input button,
58
          output[7:0] out);
59
50
          wire x;
51
          ButtonToPulse pulse(clock, clear, button, x);
Counter8BitEnable counter(clock, clear, x, out);
32
53
34
35
      endmodule
66
57
58
59
70
71
```