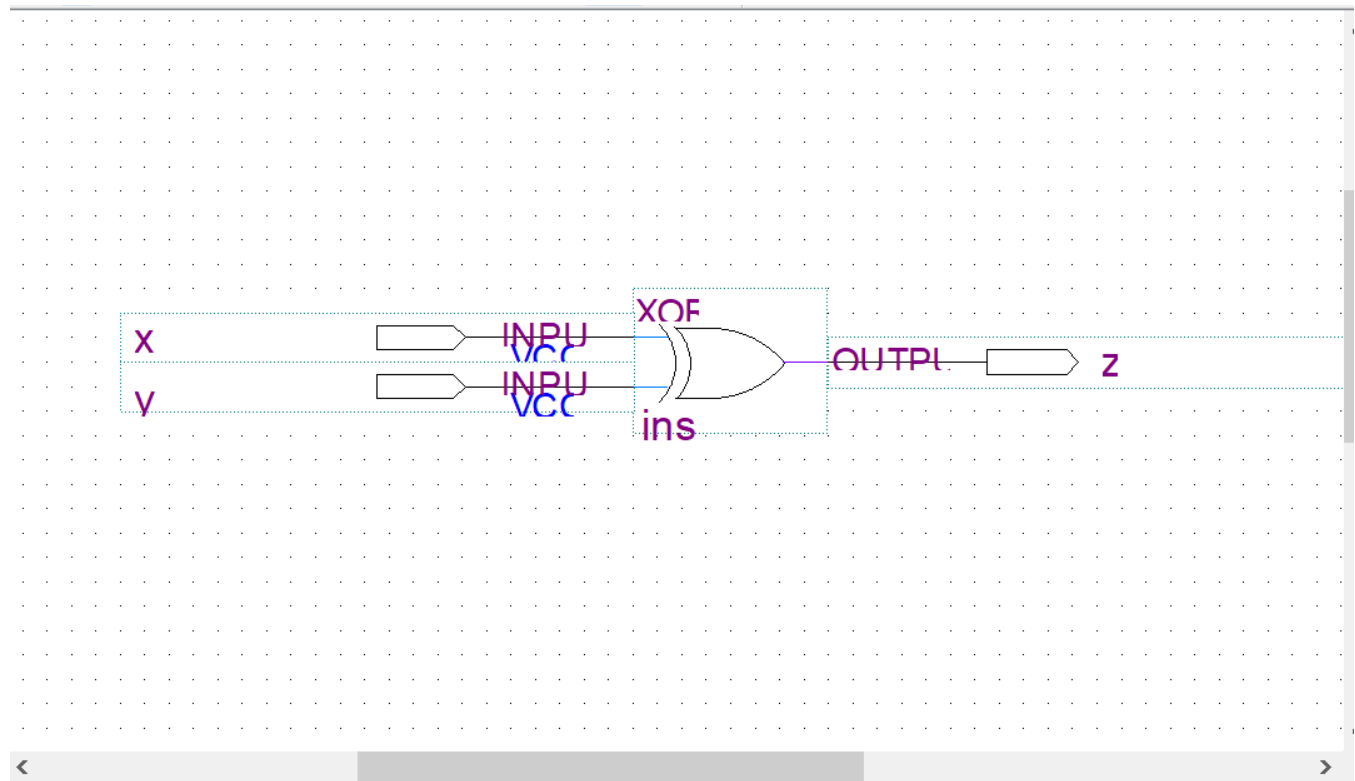


Circuit 1

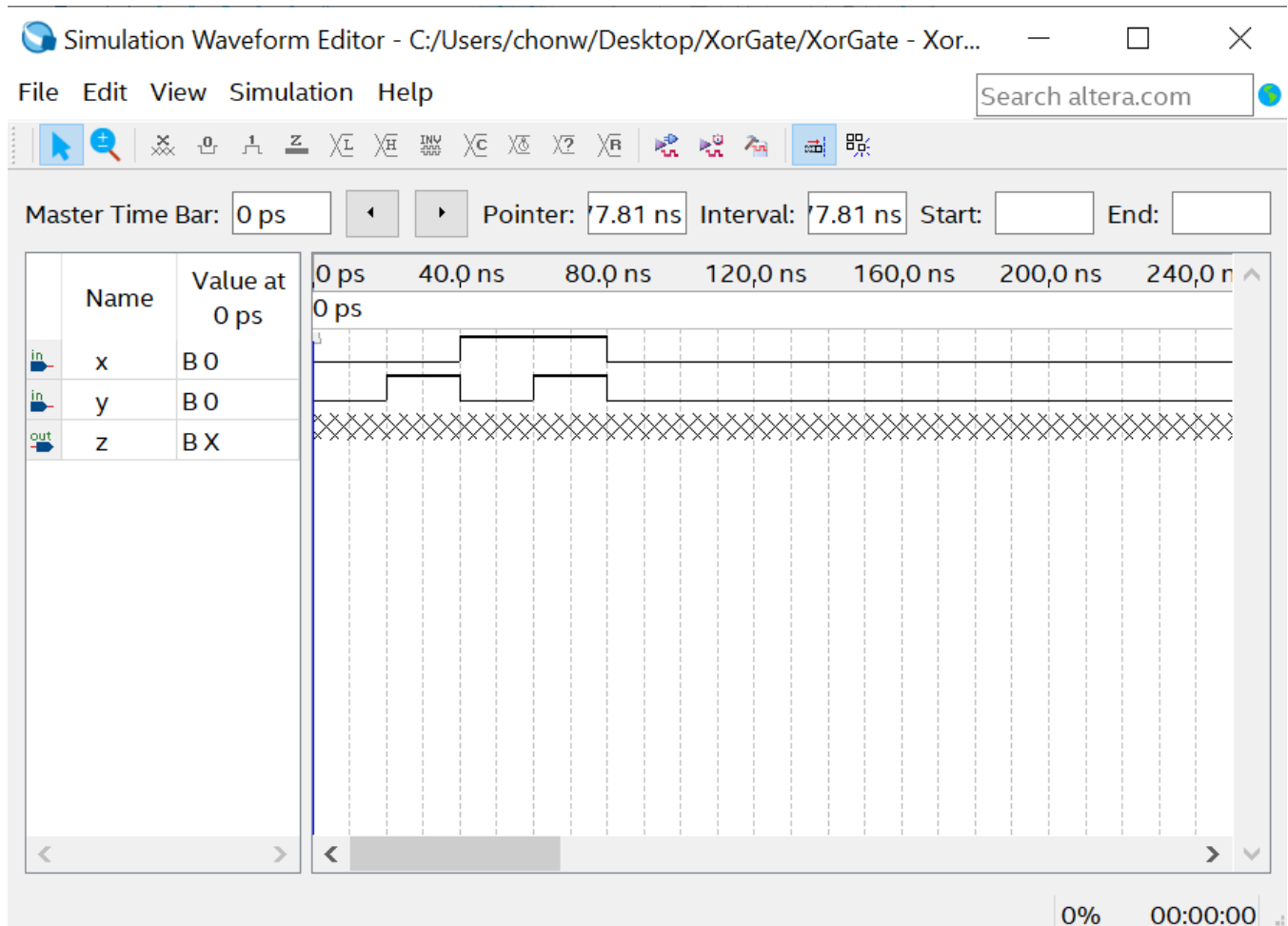
Design

Take screenshots of your all designs and paste them here.



Output

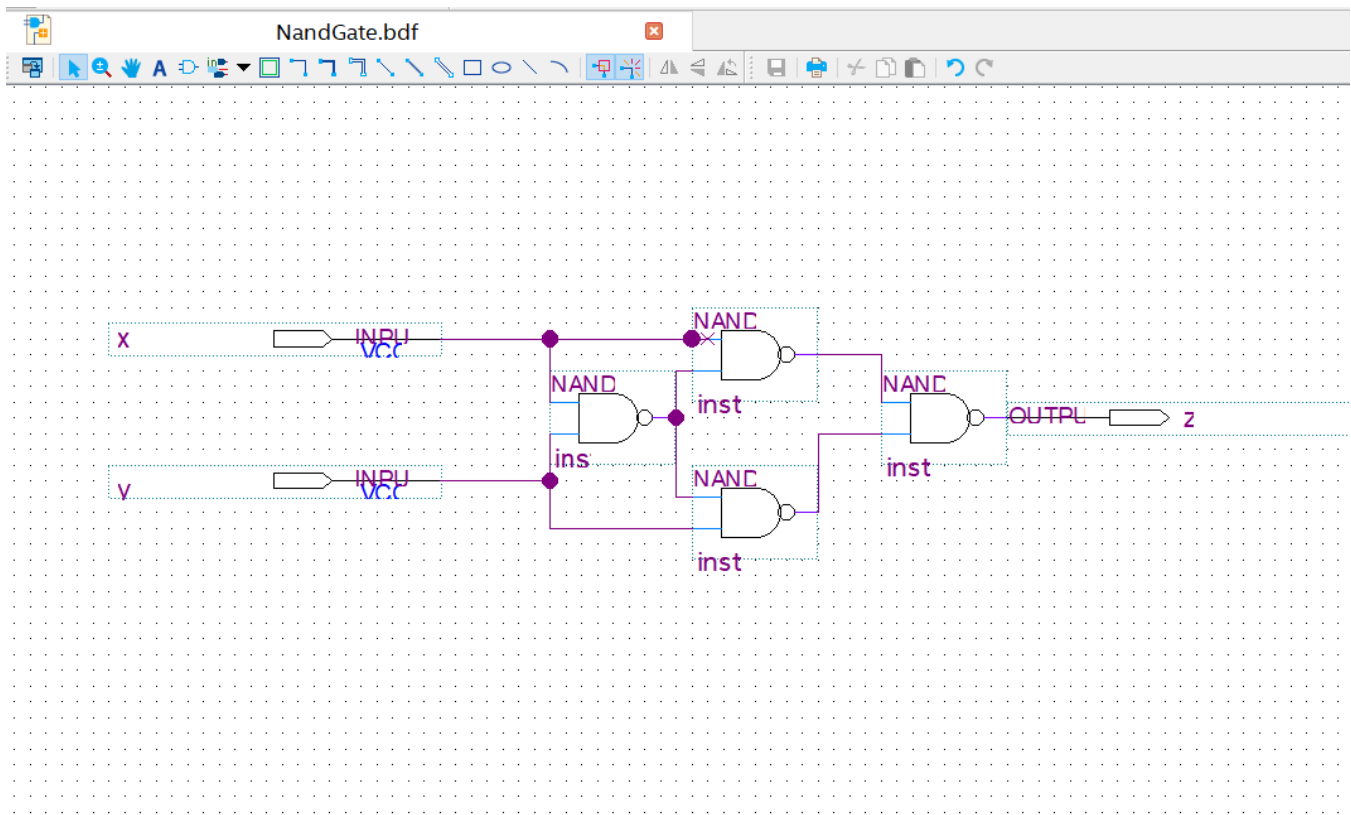
Take screenshots of your simulations for all your designs and paste them here. You should show that you tested all the possible inputs from the Truth Table.



[Circuit 2](#)

Design

Take screenshots of your all designs and paste them here.



Output

Take screenshots of your simulations for all your designs and paste them here. You should show that you tested all the possible inputs from the Truth Table.

