Xilinx Vivado installation tutorial

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Intro. to Xilinx Vivado

- The tool which we used in this class was usually Modelsim, but it is currently unavailable due to US federal regulations. We don't know when it will be available again. As a result, we choose to use Xilinx Vivado this semester.

- Xilinx Vivado is a design suite for Xilinx FPGA design flow. It has many functions to implement a design entry on a FPGA, and supports many simulation types. In this course, we only need to use the behavioral simulation to complete the homework.

Note

- Xilinx Vivado is the same as Modelsim, which they only support Windows and Linux operating systems. For those who use MAC OS, we recommend you to install virtual machine with Windows or Linux OS and install Xilinx Vivado.

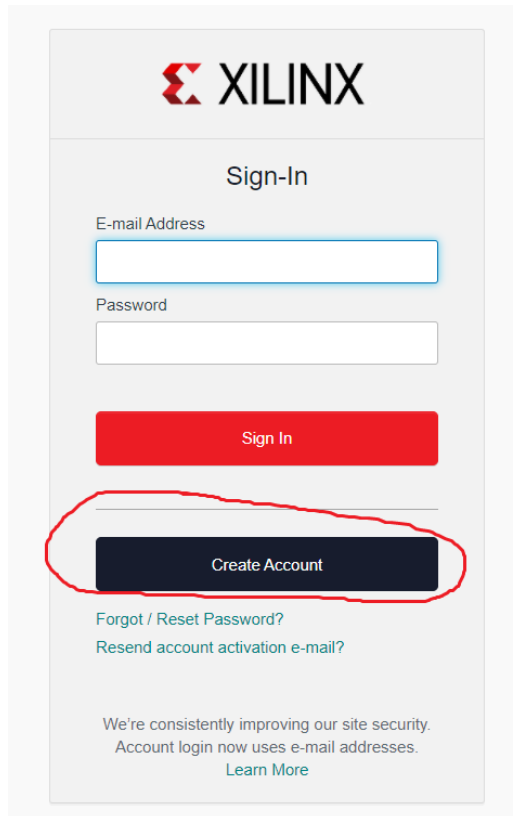- The following installation tutorial is for Windows. You can install it on Linux OS, too.
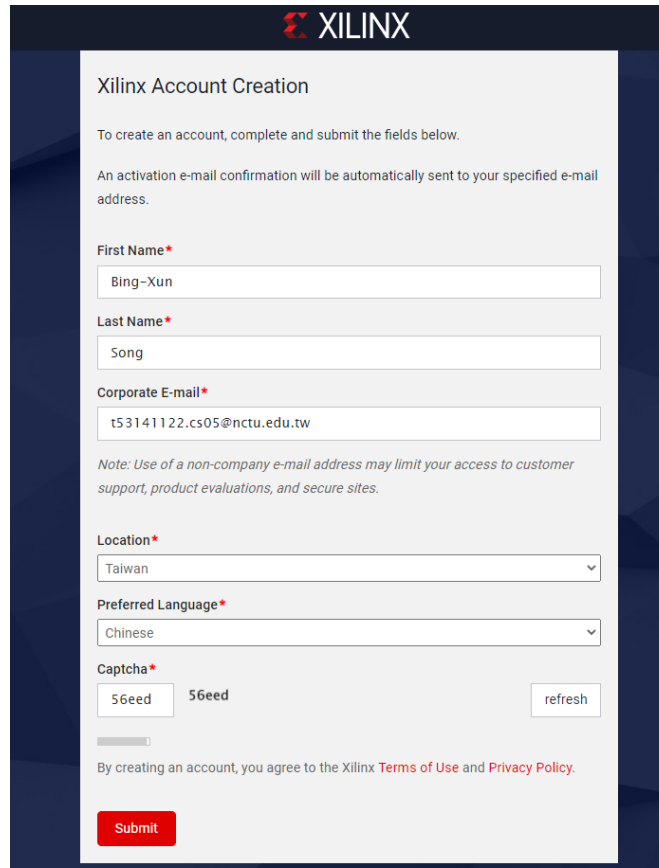
Step1

- Goto
https://www.xilinx.com/support/download/index.html/content/xilinx/en/downloadNav/vivado-design-tools/2020-2.html
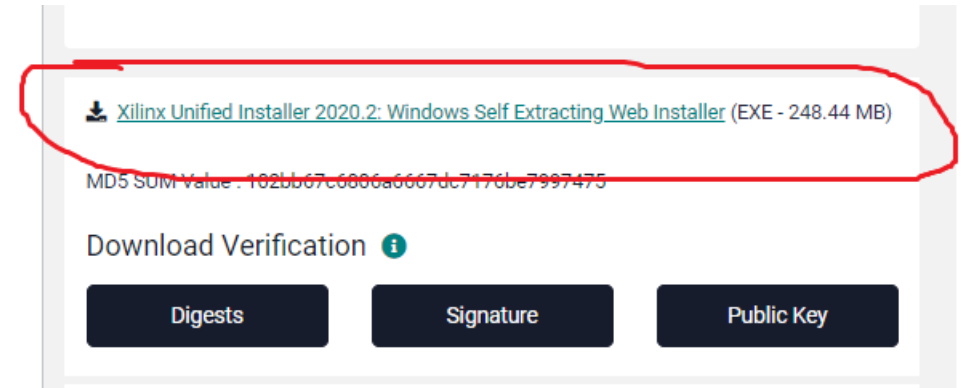
Step2

- Create and active an account, and download the Xilinx Unified Installer 2020.2: Windows Self Extracting Web Installer.

Step 3

- Get US Government Export Approval

Step 4

- If it doesn't start downloading, close the page and press the link, fill the approval again.
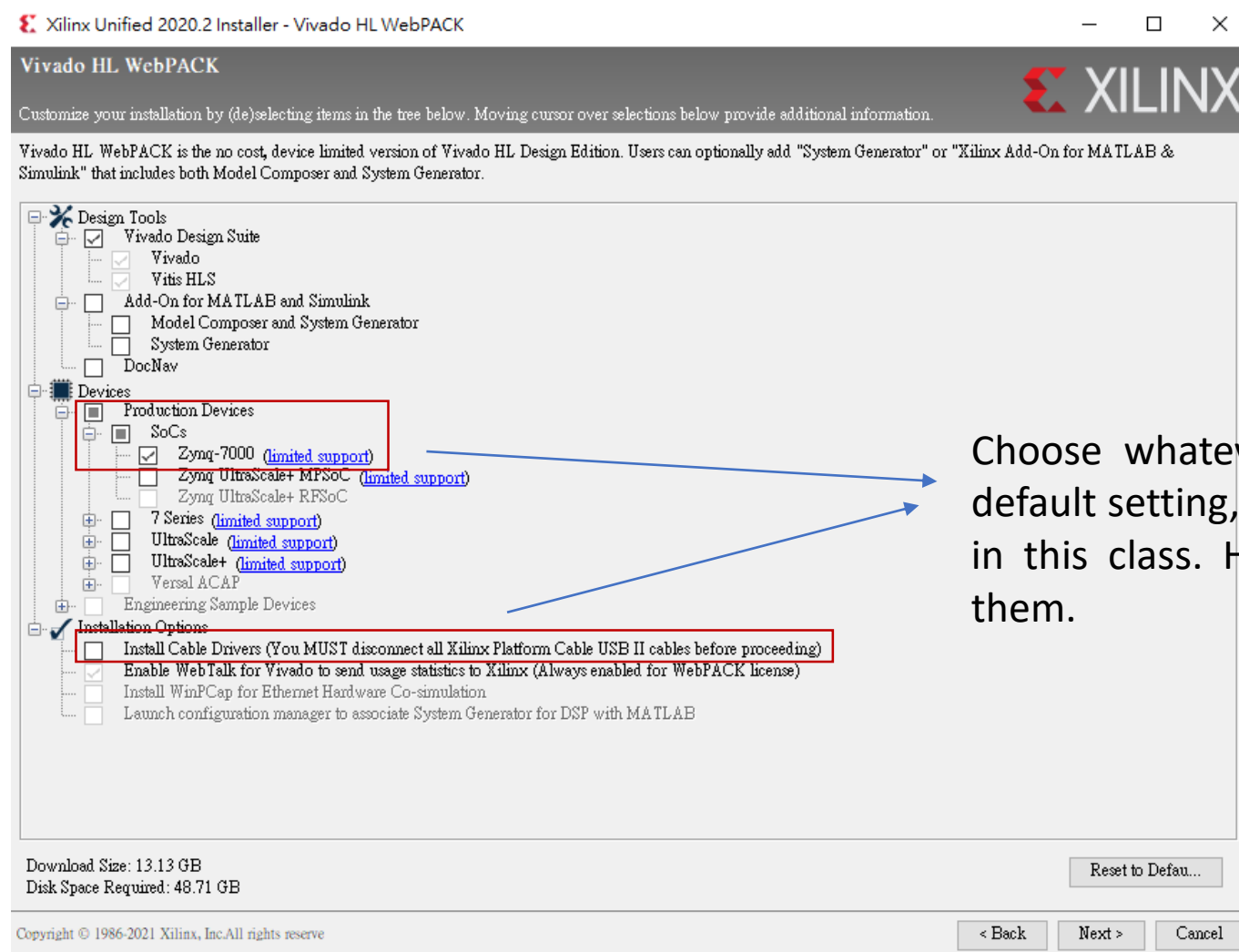
Step 5

- Execute the file you download and choose the option like the following pictures.
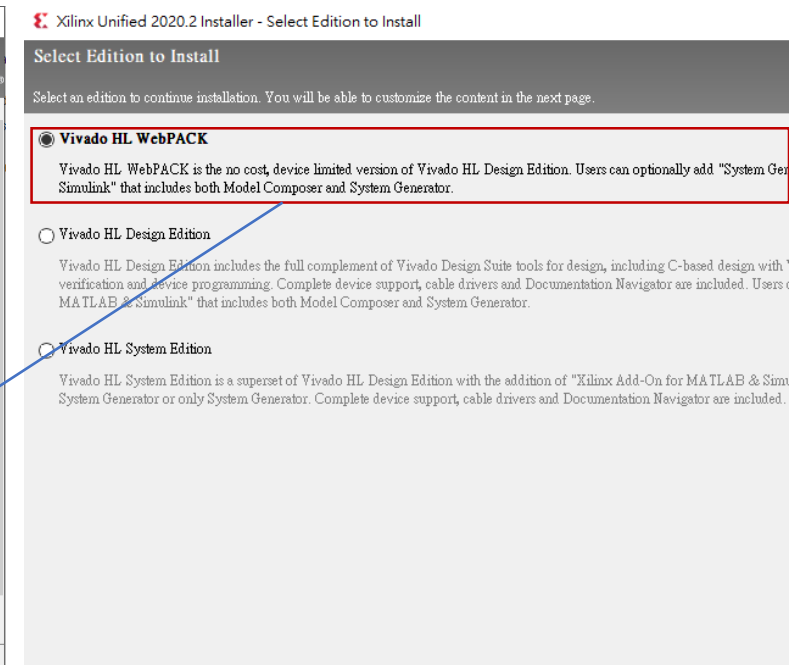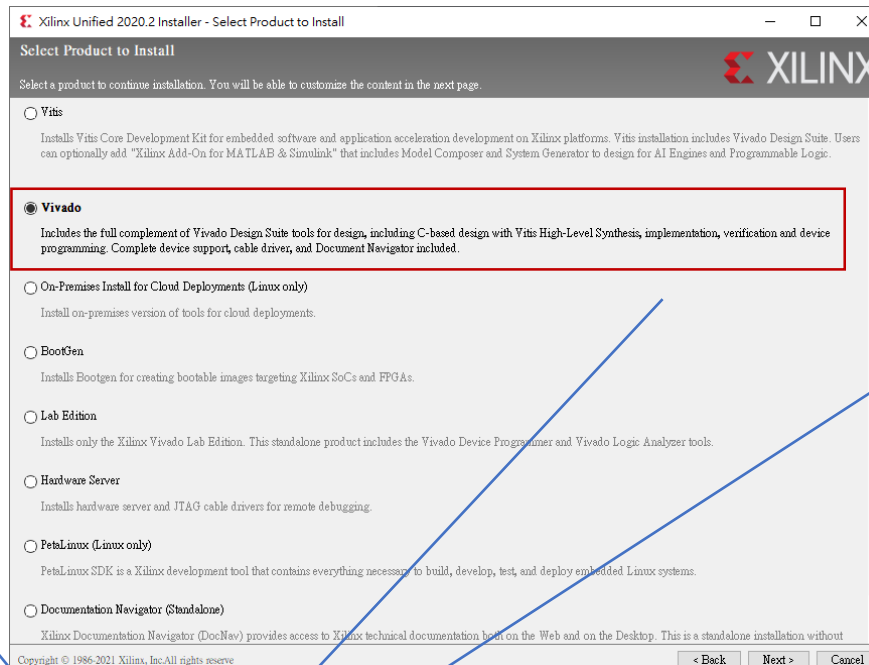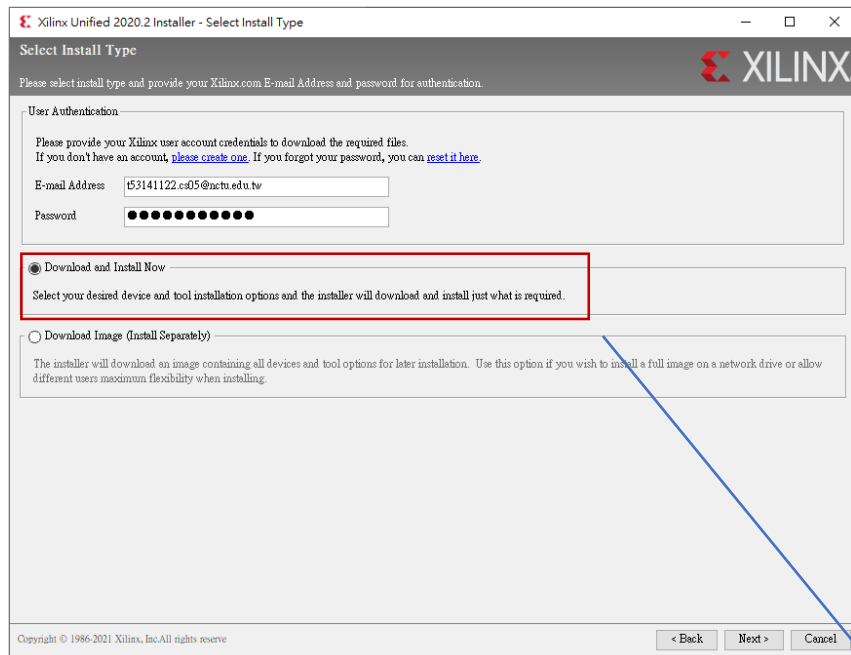
Must choose

- We only need behavioral simulation function, so you can pick the device arbitrarily. Here I select Zynq-7000.

- You can decide your own installation directory.

Step 6

- Finally, wait the installer to install the software. Note that the installer needs to take much time to install it.

QA

- If you have any question, feel free to ask us via email or post on the Facebook discussion forum (prefer).