Basics Electronics (22EC13)

Tutorial-1

1	a.A silicon transistor has its Ico=6nA and α =0.98. If I B=0 and VcE=4V then I c =					
	b.An NPN transistor has $I_{co}=30$ nA, $I_{B}=0$, $V_{CE}=4V$ and $I_{C}=30$ uA. The value of $\beta=$					
2	The measured terminal voltages (in Volts) of different NPN transistors are given in Table-					
	1. For each of the transistors, find VBE, VCB and identify the region of operation.					
			VE	VB	Vc	1
		Q1	0	0.7	0.7	1
		Q2	0	0.8	0.1	1
		Q3	-2.7	-2.0	0	1
		Q4	0	0	0.3	
		Q5	0.7	0.7	0]
				Table-1		
3	a. Find the values of β th	at corres	pond to α	value of 0.5, (0.8, 0.96, 0.98 at	nd 0.996.
	b. Find the values of α th	iat corres	spona to B	value of 40, 6	ou, 80, 99, 149 a	ına 249.
4				9		
•					5V	
	10.15	Ť		1 200		
	3.9k \$2.k B=60 B=80					
	7000			1		
		-9v		-50		
	Fig	1		Fig 2		
	, , , , , , , , , , , , , , , , , , ,	. 1				
	In the fixed base current biasing circuits of fig 1 and 2.Determine the operating po					
5	the silicon transistor.	ormino U	Ic Ve Ve a	nd Ic		
	In the circuit of fig 3, determine Vc, VE, VB and Ic.					
	V _B 199K ≥ 202K V _B V _C V _C					
	ラド					
			F	ig 3		
6	In the circuit of fig 4, fin	d Ve, Ie, Ie				

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	T ^{IOV}					
	₹ K					
	B=50					
	₹ lok					
	1					
	-lov					
	Fig 4					
7	In the inverter circuit of fig 5, determine the maximum value of R_B , so that the circuit					
	could be used an inverter.					
	T ¹² V					
	<i>≨3</i> 9κ					
	R _B					
	5v					
	1					
	÷					
	Fig 5					
8	In the inverter circuit of fig 6, determine the minimum value of β , so that the circuit					
	could be used an inverter.					
	• 5V					
	. ₹ 2.3K					
	220K , 5					
	M					
	3v 'Y					
	<u>_</u>					
	Fig 6					
9	Determine the operating point of silicon transistor in the voltage divider biasing circuit fig 7.					
	Also determine $S_{(ICO)}$.					
	lev lev					
	≥3K =33K					
	↓ ↓ β = l 0 0					
	₹ISK ₹27K					
	<u>_</u> _					
	Eio 7					
10	Fig 7 In an RC coupled CE amplifier, R_1 =50K Ω , R_2 =25K Ω , R_C = R_L =2K Ω , β =99 and I_E =2mA.					
10	Determine the voltage gain, input impedance and output impedance of the amplifier.					
	0 0 , 1 : F::: :::-F:::					

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- a)Three amplifiers of voltage gains 20dB, 26dB and 32dB are cascaded to obtain an outputvoltage of 2V. Calculate the input voltage needed.
 - b) An amplifier having a power gain of 17dB delivers a power output of 40W to a load of $1K\Omega$. Calculate i) the input power needed and (ii) the input voltage needed, if the voltage gain of the amplifier is 38dB.
- Calculate the voltage gain vo/vi , input impedance Zi and output impedance Zo for the circuits of fig 8 and 9

