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Raspberry Pi

The **Raspberry Pi** (/pai/) is a series of small single-board computers developed in the United Kingdom by the Raspberry Pi Foundation to promote teaching of basic computer science in schools and in developing countries. [6][7][8] The original model became far more popular than anticipated, [9] selling outside its target market for uses such as robotics. It does not include peripherals (such as keyboards and mice) or cases. However, some accessories have been included in several official and unofficial bundles. [9]

The organisation behind the Raspberry Pi consists of two arms. The first two models were developed by the Raspberry Pi Foundation. After the Pi Model B was released, the Foundation set up Raspberry Pi Trading, with <u>Eben Upton</u> as CEO, to develop the third model, the B+. Raspberry Pi Trading is responsible for developing the technology while the Foundation is an educational charity to promote the teaching of basic computer science in schools and in developing countries.

According to the Raspberry Pi Foundation, more than 5 million Raspberry Pis were sold by February 2015, making it the best-selling British computer. [10] By November 2016 they had sold 11 million units, [11][12] and 12.5m by March 2017, making it the third best-selling "general purpose computer". [13] In July 2017, sales reached nearly 15 million. [14] In March 2018, sales reached 19 million. [15]

Most Pis are made in a <u>Sony</u> factory in <u>Pencoed</u>, Wales^[16], while others are made in China and Japan. $^{[17]}$

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Raspberry Pi 4 Model B

Also known as	RPi
Release date	24 February 2012 (Original)
	24 June 2019 (Current)
Introductory price	US\$35 (1 GB), US\$45 (2 GB), US\$55 (4 GB) ^[1]
Operating system	Ubuntu FreeBSD Linux

NetBSD OpenBSD Plan 9 RISC OS

Windows 10 ARM64 Windows 10 IoT Core^[2]

System-on- Broadcom BCM2711 chip used

CPU 1.5 GHz 64/32-bit quadcore ARM Cortex-A72

Memory 1, 2, or 4 GB LPDDR4-3200

RAM^[3]

 Storage
 MicroSDHC slot

 Graphics
 Broadcom VideoCore VI 500 MHz^[4]

 Power
 5V 3A (for full power

delivery to USB devices)^[5]

Website raspberrypi.org (https://ras

pberrypi.org)

References **Further reading External links**

Generations of released models

Several generations of Raspberry Pis have been released. All models feature a Broadcom system on a chip (SoC) with an integrated ARM-compatible central processing unit (CPU) and on-chip graphics processing unit (GPU).

Processor speed ranges from 700 MHz to 1.4 GHz for the Pi 3 Model B+ or 1.5 GHz for the Pi 4; on-board memory ranges from 256 MB to 1 GB random-access memory (RAM), with up to 4 GB available on the Pi 4. Secure Digital (SD) cards in MicroSDHC form factor (SDHC on early models) are used to store the operating system and program memory. The boards have one to five USB ports. For video output, HDMI and composite video are supported, with a standard 3.5 mm tip-ring-sleeve jack for audio output. Lower-level output is provided by a number of GPIO pins, which support common protocols like I²C. The B-models have an 8P8C Ethernet port and the Pi 3, Pi 4 and Pi Zero W have on-board Wi-Fi 802.11n and Bluetooth. Prices range from US\$5 to \$55.

The first generation (Raspberry Pi 1 Model B) was released in February 2012, followed by the simpler and cheaper Model A. In 2014, the Foundation released a board with an improved design, Raspberry Pi 1 Model B+. These boards are approximately credit-card sized and represent the standard mainline form-factor. Improved A+ and B+ models were released a year later. A "Compute Module" was released in April 2014 for embedded applications. The Raspberry Pi 2, which added more RAM, was released in February 2015.

A Raspberry Pi Zero with smaller size and reduced input/output (I/O) and general-purpose input/output (GPIO) capabilities was released in November 2015 for US\$5. On 28 February 2017, the Raspberry Pi Zero W was launched, a version of the Zero with Wi-Fi and Bluetooth capabilities, for US\$10. [18][19] On 12 January 2018, the Raspberry Pi Zero WH was launched, a version of the Zero W with pre-soldered GPIO headers. [20]

Raspberry Pi 3 Model B was released in February 2016 with a 1.2 GHz 64-bit quad core processor, on-board 802.11n Wi-Fi, Bluetooth and USB boot capabilities. [21] On Pi Day 2018 the Raspberry Pi 3 Model B+ was launched with a faster 1.4 GHz processor and a three-times faster gigabit Ethernet (throughput limited to ca. 300 Mbit/s by the internal USB 2.0 connection) or 2.4 / 5 GHz dual-band 802.11ac Wi-Fi (100 Mbit/s).[15] Other features are Power over Ethernet (PoE) (with the add-on PoE HAT), USB boot and network boot (an SD card is no longer required).

Raspberry Pi 4 Model B was released in June 2019^[1] with a 1.5 GHz 64-bit quad core ARM Cortex-A72 processor, on-board 802.11ac Wi-Fi, Bluetooth 5, full gigabit Ethernet (throughput not limited), two USB 2.0 ports, two USB 3.0 ports, and dual monitor support (4K resolution). The Pi 4 is also powered via a USB-C port, enabling additional power to be provided to downstream peripherals, when used with an appropriate PSU. The Raspberry Pi 4 has a design flaw where third-party e-marked USB cables, such as those used on Apple MacBooks, incorrectly identify it and refuse to provide power. This is expected to be corrected in a future board revision. [22][23] Tom's Hardware tested 14

different cables and found that 11 of them turned on and powered the Pi without issue. [24]



The Raspberry Pi Zero, a US\$5 model first introduced in 2015



The Raspberry Pi 3 B+, introduced in 2018



The Raspberry Pi 4 B, introduced in 2019

Family	Model	Form Factor	Ethernet	Wireless	GPIO	Released	Discontinued
	В	Standard	Yes		26 nin	2012	Yes
Raspberry Pi 1 Raspberry Pi 2 Raspberry Pi Zero Raspberry Pi 3 Raspberry Pi 4	Α	(85.60 × 56.5 mm)	No		26-pin	2013	Yes
	B+		Yes	No		2014	
,	A+	Compact (65 × 56.5 mm)	No			2014	
Raspberry Pi 2	В	Standard	Yes	No		2015	
D D'7	Zero	Zero	No	No	40-pin	2015	
Raspberry Pi Zero	W/WH	(65 × 30 mm)		Yes		2017	
	В	Standard	Yes	No No No Yes 40 S S Yes S S		2016	
Raspberry Pi 1 Raspberry Pi 2 Raspberry Pi Zero Raspberry Pi 3	A+	Compact	No	Yes		2018	
	B+	Standard	Yes			2018	
Raspberry Pi 2 Raspberry Pi Zero Raspberry Pi 3	B (1GB)						
	B (2GB)	Standard	Yes	Yes		2019 ^[25]	
	B (4GB)						

Hardware

The Raspberry Pi hardware has evolved through several versions that feature variations in memory capacity and peripheral-device support.

This block diagram describes Model B and B+; Model A, A+, and the Pi Zero are similar, but lack the Ethernet and USB hub components. The Ethernet adapter is internally connected to an additional USB port. In Model A, A+, and the Pi Zero, the USB port is connected directly to the system on a chip (SoC). On the Pi 1 Model B+ and later models the USB/Ethernet chip contains a five-port USB hub, of which four ports are available, while

the Pi 1 Model B only provides two. On the Pi Zero, the USB port is also connected directly to the SoC, but it uses a <u>micro USB</u> (OTG) port. Unlike all other Pi models, the 40 pin GPIO connector is omitted on the Pi Zero with solderable through holes only in the pin locations. The Pi Zero WH remedies this.

RAM I/O ++ CPU/GPU ++ USB hub t t t Ethernet 2x USB

Processor

All SoCs used in Raspberry Pis are custom-developed under collaboration of <u>Broadcom</u> and <u>Raspberry Pi</u> Foundation.

The Broadcom BCM2835 SoC used in the first generation Raspberry $Pi^{[26]}$ includes a 700 MHz ARM1176JZF-S processor, VideoCore IV graphics processing unit (GPU), and RAM. It has a level 1 (L1) cache of 16 KB and a level 2 (L2) cache of 128 KB. The level 2 cache is used primarily by the GPU. The SoC is stacked underneath the RAM chip, so only its edge is visible. The ARM1176JZ(F)-S is the same CPU used in the original iPhone, although at a higher clock rate, and mated with a much faster GPU.

The earlier V1.1 model of the Raspberry Pi 2 used a Broadcom BCM2836 SoC with a 900 MHz 32-bit, quad-core ARM Cortex-A7 processor, with 256 KB shared L2 cache. [29] The Raspberry Pi 2 V1.2 was upgraded to a Broadcom BCM2837 SoC with a 1.2 GHz 64-bit quad-core ARM Cortex-A53 processor, [30] the same SoC which is used on the Raspberry Pi 3, but underclocked (by default) to the same 900 MHz CPU clock speed as the V1.1. The BCM2836 SoC is no longer in production as of late 2016.



The Raspberry Pi 2B uses a 32-bit 900 MHz quad-core ARM Cortex-A7 processor.

The Raspberry Pi 3 Model B uses a Broadcom BCM2837 SoC with a 1.2 GHz 64-bit quad-core $\underline{ARM\ Cortex-A53}$ processor, with 512 KB shared L2 cache. The Model A+ and B+ are 1.4 $\underline{GHz}^{[31][32][33]}$

The Raspberry Pi 4 uses a Broadcom BCM2711 SoC with a 1.5 GHz 64-bit quad-core ARM Cortex-A72 processor, with 1MB shared L2 cache. [34][35]

The Raspberry Pi Zero and Zero W use the same Broadcom BCM2835 SoC as the first generation Raspberry Pi, although now running at 1 GHz CPU clock speed. [36]

Performance

While operating at 700 MHz by default, the first generation Raspberry Pi provided a real-world performance roughly equivalent to 0.041 GFLOPS. [37][38] On the CPU level the performance is similar to a 300 MHz Pentium II of 1997–99. The GPU provides 1 Gpixel/s or 1.5 Gtexel/s of graphics processing or 24 GFLOPS of general purpose computing performance. The graphical capabilities of the Raspberry Pi are roughly equivalent to the performance of the Xbox of 2001.

Raspberry Pi 2 V1.1 included a quad-core <u>Cortex-A7 CPU</u> running at 900 MHz and 1 GB RAM. It was described as 4–6 times more powerful than its predecessor. The GPU was identical to the original. [29] In parallelised benchmarks, the Raspberry Pi 2 V1.1 could be up to 14 times faster than a Raspberry Pi 1 Model B+. [39]

The Raspberry Pi 3, with a quad-core ARM Cortex-A53 processor, is described as having ten times the performance of a Raspberry Pi 1. [40] Benchmarks showed the Raspberry Pi 3 to be approximately 80% faster than the Raspberry Pi 2 in parallelised tasks. [41]

Overclocking

Most Raspberry Pi systems-on-chip could be overclocked to 800 MHz, and some to 1000 MHz. There are reports the Raspberry Pi 2 can be similarly overclocked, in extreme cases, even to 1500 MHz (discarding all safety features and over-voltage limitations). In the Raspbian Linux distro the overclocking options on boot can be done by a software command running "sudo raspi-config" without voiding the warranty. In those cases the Pi automatically shuts the overclocking down if the chip temperature reaches 85 °C (185 °F), but it is possible to override automatic over-voltage and overclocking settings (voiding the warranty); an appropriately sized heat sink is needed to protect the chip from serious overheating.

Newer versions of the <u>firmware</u> contain the option to choose between five overclock ("turbo") presets that when used, attempt to maximise the performance of the SoC without impairing the lifetime of the board. This is done by monitoring the core temperature of the chip and the <u>CPU load</u>, and dynamically adjusting <u>clock speeds</u> and the <u>core voltage</u>. When the demand is low on the CPU or it is running too hot the performance is <u>throttled</u>, but if the CPU has much to do and the chip's temperature is acceptable, performance is temporarily increased with clock speeds of up to 1 GHz, depending on the board version and on which of the turbo settings is used.

The seven overclock presets are:

- none; 700 MHz ARM, 250 MHz core, 400 MHz SDRAM, 0 overvolting
- modest; 800 MHz ARM, 250 MHz core, 400 MHz SDRAM, 0 overvolting,
- medium; 900 MHz ARM, 250 MHz core, 450 MHz SDRAM, 2 overvolting,
- high; 950 MHz ARM, 250 MHz core, 450 MHz SDRAM, 6 overvolting,
- turbo; 1000 MHz ARM, 500 MHz core, 600 MHz SDRAM, 6 overvolting,
- Pi 2; 1000 MHz ARM, 500 MHz core, 500 MHz SDRAM, 2 overvolting,
- Pi 3; 1100 MHz ARM, 550 MHz core, 500 MHz SDRAM, 6 overvolting. In system information the CPU speed will appear as 1200 MHz. When idling, speed lowers to 600 MHz. [42][43]

In the highest (turbo) preset the SDRAM clock was originally 500 MHz, but this was later changed to 600 MHz because 500 MHz sometimes causes SD card corruption. Simultaneously in high mode the core clock speed was lowered from 450 to 250 MHz, and in medium mode from 333 to 250 MHz.

The CPU on the first and second generation Raspberry Pi board did not require cooling, such as a heat sink or $\underline{\underline{fan}}$, even when overclocked, but the Raspberry Pi 3 may generate more heat when overclocked. [44]

RAM

The early designs of the Raspberry Pi Model A and B boards included only 256 MB of random access memory (RAM). Of this, the early beta Model B boards allocated 128 MB to the GPU by default, leaving only 128 MB for the CPU. In the early 256 MB releases of models A and B, three different splits were possible. The default split was 192 MB for the CPU, which should be sufficient for standalone 1080p video decoding, or for simple 3D processing. 224 MB was for Linux processing only, with only a 1080p framebuffer, and was likely to fail for any video or 3D. 128 MB was for heavy 3D processing, possibly also with video decoding, I46] In comparison, the Nokia 701 uses 128 MB for the Broadcom VideoCore IV. I47]

The later Model B with 512 MB RAM, was initially released with new standard memory split files (arm256_start.elf, arm384_start.elf, arm496_start.elf) with 256 MB, 384 MB, and 496 MB CPU RAM, and with 256 MB, 128 MB, and 16 MB video RAM, respectively. But about one week later, the foundation released a new version of start.elf that could read a new entry in config.txt (gpu_mem=xx) and could dynamically assign an amount of RAM (from 16 to 256 MB in 8 MB steps) to the GPU, obsoleting the older method of splitting memory, and a single start.elf worked the same for 256 MB and 512 MB Raspberry Pis. [48]

The Raspberry Pi 2 has 1 GB of RAM. The Raspberry Pi 3 has 1 GB of RAM in the B and B+ models, and 512 MB of RAM in the A+ model. [49][50][51] The Raspberry Pi Zero and Zero W have 512 MB of RAM.

The Raspberry Pi 4 has a choice of 1, 2 or 4 GB of RAM, set at time of manufacture. [52] The Raspberry Pi 4 Model B Safety and User guide mentions "Raspberry Pi 4 Model B 1 GB, 2 GB, 4 GB + 8 GB variants" but the 8GB model was not available as of the 2019 launch. [53]

Networking

The Model A, A+ and Pi Zero have no Ethernet circuitry and are commonly connected to a network using an external user-supplied USB Ethernet or Wi-Fi adapter. On the Model B and B+ the Ethernet port is provided by a built-in USB Ethernet adapter using the SMSC LAN9514 chip.^[54] The Raspberry Pi 3 and Pi Zero W (wireless) are equipped with 2.4 GHz WiFi 802.11n (150 Mbit/s) and Bluetooth 4.1 (24 Mbit/s) based on the Broadcom BCM43438 FullMAC chip with no official support for monitor mode but implemented through unofficial firmware patching^[55] and the Pi 3 also has a 10/100 Mbit/s Ethernet port. The Raspberry Pi 3B+ features dual-band IEEE 802.11b/g/n/ac WiFi, Bluetooth 4.2, and Gigabit Ethernet (limited to approximately 300 Mbit/s by the USB 2.0 bus between it and the SoC). The Raspberry Pi 4 has full gigabit Ethernet (throughput is not limited as it is not funnelled via the USB chip.)

Special-purpose features

The Pi Zero can be used as a $\underline{\text{USB}}$ device or "USB gadget", plugged into another computer via a USB port on another machine. It can be configured in multiple ways, for example to show up as a serial device or an ethernet device. [56] Although originally requiring software patches, this was added into the mainline Raspbian distribution in May 2016. [56]

The Pi 3 can boot from USB, such as from a flash drive. [57] Because of firmware limitations in other models, the Pi 2B v1.2, 3A+, 3B, and 3B+ are the only boards that can do this.

Peripherals

Although often pre-configured to operate as a <u>headless computer</u>, the Raspberry Pi may also optionally be operated with any generic <u>USB computer keyboard and mouse. [58]</u> It may also be used with USB storage, USB to MIDI converters, and *virtually* any other device/component with USB capabilities, depending on the installed device drivers in the underlying operating system (many of which are included by default).

Other peripherals can be attached through the various pins and connectors on the surface of the Raspberry Pi. [59]

The Model 2B boards incorporate four USB ports for connecting peripherals.

Video

The video controller can generate standard modern TV resolutions, such as HD and Full HD, and higher or lower monitor resolutions as well as older NTSC or PAL standard CRT TV resolutions. As shipped (i.e., without custom overclocking) it can support the following resolutions: 640×350 EGA; 640×480 VGA; 800×600 SVGA; 1024×768 XGA; 1280×720 720p HDTV; 1280×768 WXGA variant; 1280×800 WXGA variant; 1280×1024 SXGA; 1366×768 WXGA variant; 1400×1050 SXGA+; 1600×1200 UXGA; 1680×1050 WXGA+; 1920×1080 1080p HDTV; 1920×1200 WUXGA.

Higher resolutions, up to 2048×1152 , may work $^{[61][62]}$ or even 3840×2160 at 15 Hz (too low a frame rate for convincing video). Allowing the highest resolutions does not imply that the GPU can decode video formats at these resolutions; in fact, the Pis are known to not work reliably for $\underline{\text{H.265}}$ (at those high resolutions), commonly used for very high resolutions (however, most common formats up to Full HD do work).

Although the Raspberry Pi 3 does not have H.265 decoding hardware, the CPU is more powerful than its predecessors, potentially fast enough to allow the decoding of H.265-encoded videos in software. $^{[64]}$ The GPU in the Raspberry Pi 3 runs at higher clock frequencies of 300 MHz or 400 MHz, compared to previous versions which ran at 250 MHz. $^{[65]}$

The Raspberry Pis can also generate $\underline{576i}$ and $\underline{480i}$ composite video signals, as used on old-style (CRT) TV screens and less-expensive monitors through standard connectors – either RCA or 3.5 mm phono connector depending on model. The television signal standards supported are $\underline{PAL-BGHID}$, $\underline{PAL-M}$, $\underline{PAL-N}$, \underline{NTSC} and $\underline{NTSC-J}$.



The early Raspberry Pi 1 Model A, with an HDMI port and a standard RCA composite video port for older displays

Lack of real-time clock

None of the Raspberry Pi models have a built-in <u>real-time clock</u>. When booting, the time is set either manually, or configured from a previously saved state at shutdown to provide relative consistency for the <u>file system</u>. The <u>Network Time Protocol</u> is used to update the system time when connected to a network.

A real-time hardware clock with battery backup, such as the DS1307, may be added.

Specifications

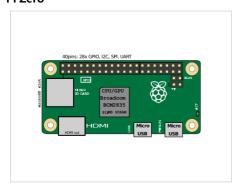
				Wikipedia							
		Mode	I A				Mod	el B			
Version	RPi 1 Model A	RPi 1 Model A+	RPi 3 Model A+	RPi 1 Model B	RPi 1 Model B+	RPi 2 Model B	RPi 2 Model B v1.2	RPi			
Release date	February 2013 ^[67]	November 2014 ^[68]	November 2018	April–June 2012	July 2014 ^[69]	February 2015 ^[29]	October 2016 ^[70]	Febr 2016			
Target price (USD)	\$25 ^[67]	\$20 ^[68]	\$25	\$35[77]	\$25 ^[78]	\$35					
Instruction set	ARMv6Z (32-bit)	ı	ARMv8 (64-bit)	ARMv6Z (32-bit)	1	<u>ARMv8</u> -A (6	<u>ARMv8</u> -A (64/32-b				
SoC	Broadcom BCM2	.835 ^[26]	Broadcom BCM2837B0 ^[15]	Broadcom BCM2835 ^[26]	Broadcom BCM2835 ^[26]						
FPU	VFPv2; NEON no	t supported	VFPv4 + NEON	VFPv2; NEON not support	ed	VFPv3 + NEON	\\\FP\\\A + \\\F(
CPU	1× <u>ARM11</u> 76JZF	-S 700 MHz	4× <u>Cortex-A53</u> 1.4 GHz	1× <u>ARM11</u> 76JZF-S 700 MI	Hz	4× Cortex-A7 900 MHz	4× <u>Cortex-</u> <u>A53</u> 900 MHz	4× <u>C</u>			
GPU	OpenGL ES 2.0 (I	om <u>VideoCore</u> IV @ 250 MHz (BCM2837: 3D part of GPU @ 300 MHz, video part of GPU @ 400 MHz) ^{[80][81]} L ES 2.0 (BCM2835, BCM2836: 24 GFLOPS / BCM2837: 28.8 GFLOPS) 2 and <u>VC-1</u> (with license), ^[82] 1080p30 H.264/MPEG-4 AVC high-profile decoder and encoder ^[26] (BCM2837:									
Memory (SDRAM)	256 MB (shared with GPU)	512 MB (sha	ared with GPU) as of 4 May	2016. Older boards had 256	MB (shared with	1 GB (share	d with GPU)				
USB 2.0 ports ^[58] 1 (direct from BCM2835 chip) 1 (direct from BCM2837B0 chip)				2 (via on-board 3-port USB hub) ^[85]	4 (via on-board 5-port USB hub) ^{[54][69][86]}						
USB 3.0 ports	0										
Video input	15-pin MIPI cam	era interface (CSI) connector, used with t	he Raspberry Pi camera or R	aspberry Pi NoIR ca	amera ^[87]					
Video outputs	Video outputs HDMI (rev 1.3),composite video (RCA jack), MIPI (3.5 mm CT display interface (DSI) for raw LCD panels panels total control to			HDMI (rev 1.3), composite video (RCA jack), MIPI display interface (DSI) for raw LCD panels	HDMI (rev 1.3), composite video (3.5 m (DSI) for raw LCD panels			3.5 mm CTIA sty			
Audio inputs	As of revision 2 l	ooards via <u>I²S</u> [95]								
Audio outputs	Analog via 3.5 m	m phone jack	; digital via HDMI and, as o	f revision 2 boards, <u>I²S</u>							
On-board storage ^[58]	SD, MMC, SDIO card slot (3.3 V with card power only)	MicroSDHC	slot ^[69]	SD, MMC, SDIO card slot	MicroSDHC slot		MicroSDHC	slot, U			
Ethernet (8P8C) ^[58]			None	10/100 Mbit/s USB adapter on the USB h	ub ^[85]			10/10			
WiFi IEE 802.11 wireless	None ^[97]		b/g/n/ac dual band 2.4/5 GHz	None				b/g/r band			
Bluetooth	-		4.2 BLE	None				4.1 B			
Low-level peripherals	8× GPIO ^[99] plus the following, which can also be used as GPIO: UART, I²C bus, SPI bus with two chip selects, I²S audio ^[100] +3.3 V, +5 V, ground ^[80] [101]		lus the same specific nd HAT ID bus	8× GPIO plus the following, which can also be used as GPIO: <u>UART</u> , I ² C bus, <u>SPI</u> bus with two chip selects, I ² S audio +3.3 V, +5 V, ground. An additional 4× <u>GPIO</u> are available on the P5 pad if the user is willing to make solder connections	17× <u>GPIO</u> plus th	us the same specific functions, an					
Power ratings	300 mA (1.5 W) ^[104]	200 mA (1 W) ^[105]		700 mA (3.5 W)	200 mA (1 W) average when idle, 350 mA (1.75 W)	220 mA (1.7 when idle, 8 (4.1 W) may under stres	ximum	300 r avera idle, (6.7 \			
	·	1	1	1	ı			0/00			

				,				
					maximum under stress (monitor, keyboard and mouse connected) ^[106]	keyboard a connected)	nd mouse [106]	maxii unde (mon keybi mous WiFi conn
Power source	5 V via MicroUSE	or GPIO head	der					
Size	85.60 mm × 56.5 mm (3.370 in × 2.224 in), excluding protruding connectors	65 mm × 56.5 mm × 10 mm (2.56 in × 2.22 in × 0.39 in), same as HAT board	65 mm x 56.5 mm	85.60 mm × 56.5 mm (3.3 connectors	70 in × 2.224 in), e	xcluding pro	truding	85.60
Weight	31 g (1.1 oz)	23 g (0.81 oz)		45 g (1.6 oz)				
Console	Adding a USB ne	twork interfac	e via tethering ^[97] or a seria	al cable with optional GPIO	oower connector ^[1]	12]		
Generation	1	1 +	3+	1	1 +	2	2 ver 1.2	3
Obsolescence Statement	n/a	in production until at least January 2022	in production until at least January 2023	n/a	in production until at least January 2022	n/a	in production until at least January 2022	in pro until Janua
Туре		Model	I A				Mode	el B

^{*} all interfaces are via 200-pin DDR2 SO-DIMM connector.

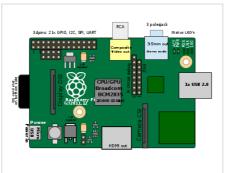
Connectors

Pi Zero



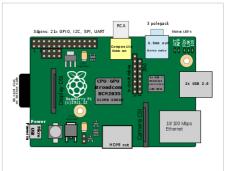
Location of connectors and main ICs

Model A



Location of connectors and main ICs on Raspberry Pi 1 Model A

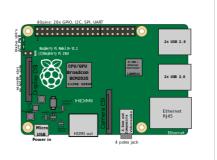
Model B



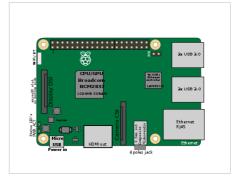
Location of connectors and main ICs on Raspberry Pi 1 Model B revision 1.2



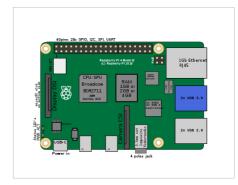
Location of connectors and main ICs on Raspberry Pi 1 Model A+ revision 1.1



Location of connectors and main ICs on Raspberry Pi 1 Model B+ revision 1.2 and Raspberry Pi 2



Location of connectors and main ICs on Raspberry Pi 3



Location of connectors and main ICs on Raspberry Pi 4

General purpose input-output (GPIO) connector

Raspberry Pi 1 Models A+ and B+, Pi 2 Model B, Pi 3 Models A+, B and B+, Pi 4, and Pi Zero, Zero W, and Zero WH GPIO J8 have a 40-pin pinout. [113][114] Raspberry Pi 1 Models A and B have only the first 26 pins. [115][116][117]

In the Pi Zero and Zero W the 40 GPIO pins are unpopulated, having the through-holes exposed for soldering instead. The Zero WH (Wireless + Header) has the header pins preinstalled.

GPIO#	2nd func.	Pin#		Pin#	2nd func.	GPIO#
	+3.3 V	1	Г	2	+5 V	
2	SDA1 (I ² C)	3		4	+5 V	
3	SCL1 (I ² C)	5		6	GND	
4	GCLK	7		8	TXD0 (UART)	14
	GND	9		10	RXD0 (UART)	15
17	GEN0	11		12	GEN1	18
27	GEN2	13		14	GND	
22	GEN3	15		16	GEN4	23
	+3.3 V	17		18	GEN5	24
10	MOSI (SPI)	19		20	GND	
9	MISO (SPI)	21		22	GEN6	25
11	SCLK (SPI)	23		24	CEO_N (SPI)	8
	GND	25		26	CE1_N (SPI)	7
	(Pi 1 i	Models .	Αċ	and B st	top here)	
0	ID_SD (I ² C)	27		28	ID_SC (I ² C)	1
5	N/A	29		30	GND	
6	N/A	31		32		12
13	N/A	33		34	GND	
19	N/A	35		36	N/A	16
26	N/A	37		38	Digital IN	20
	GND	39		40	Digital OUT	21

Model B rev. 2 also has a pad (called P5 on the board and P6 on the schematics) of 8 pins offering access to an additional 4 GPIO connections. [118] These GPIO pins were freed when the four board version identification links present in revision 1.0 were removed. [119]

GPIO#	2nd func.	Pin#	Pin#	2nd func.	GPIO#
	+5 V	1	2	+3.3 V	
28	GPIO_GEN7	3	4	GPIO_GEN8	29
30	GPIO_GEN9	5	6	GPIO_GEN10	31
	GND	7	8	GND	

Models A and B provide GPIO access to the ACT status LED using GPIO 16. Models A+ and B+ provide GPIO access to the ACT status LED using GPIO 47, and the power status LED using GPIO 35.

Accessories

- Gertboard A Raspberry Pi Foundation sanctioned device, designed for educational purposes, that expands the Raspberry Pi's GPIO pins to allow interface with and control of LEDs, switches, analog signals, sensors and other devices. It also includes an optional Arduino compatible controller to interface with the Pi. [120]
- Camera On 14 May 2013, the foundation and the distributors RS Components & Premier Farnell/Element 14 launched the Raspberry Pi camera board alongside a firmware update to accommodate it.^[121] The camera board is shipped with a <u>flexible flat cable</u> that plugs into the <u>CSI</u> connector which is located between the Ethernet and HDMI ports. In Raspbian, the user must enable the use of the camera board by running Raspi-config and selecting the camera option. The camera module costs €20 in Europe (9 September 2013).^[122] It can produce <u>1080p</u>, <u>720p</u> and <u>640x480p</u> video. The dimensions are 25 mm × 20 mm × 9 mm.^[122] In May 2016, v2 of the camera came out, and is an 8 megapixel camera.
- Infrared Camera In October 2013, the foundation announced that they would begin producing a camera module without an infrared filter, called the Pi NoIR.^[123]
- Official Display On 8 September 2015, The foundation and the distributors RS Components & Premier Farnell/Element 14 launched the Raspberry Pi Touch Display^[124]
- HAT (Hardware Attached on Top) expansion boards Together with the Model B+, inspired by the <u>Arduino shield</u> boards, the interface for HAT boards was devised by the Raspberry Pi Foundation. Each HAT board carries a small EEPROM (typically a CAT24C32WI-GT3)^[125] containing the relevant details of the board, ^[126] so that the Raspberry Pi's OS is informed of the HAT, and the technical details of it, relevant to the OS using the HAT. ^[127] Mechanical details of a HAT board, which uses the four mounting holes in their rectangular formation, are available online. ^[128][129]

Software

Operating systems

The Raspberry Pi Foundation provides Raspbian, a Debian-based (32-bit) Linux distribution for download, as well as third-party Ubuntu, Windows 10 IoT Core, RISC OS, and specialised media centre distributions. [130] It promotes Python and Scratch as the main programming languages, with support for many other languages. [131] The default firmware is closed source, while an unofficial open source is available. [132][133][134] Many other operating systems can also run on the Raspberry Pi. Third-party operating systems available via the official website include Ubuntu MATE, Windows 10 IoT Core, RISC OS and specialised distributions for the Kodi media centre and classroom management. [135] The formally verified microkernel seL4 is also supported. [136]

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Various operating systems for the Raspberry Pi can be installed on a MicroSD, MiniSD or SD card, depending on the board and available adapters; seen here is the MicroSD slot located on the bottom of a Raspberry Pi 2

board

Other operating systems (not Linux-based)

- Broadcom VCOS Proprietary operating system which includes an abstraction layer designed to integrate with existing kernels, such as ThreadX (which is used on the VideoCore4 processor), providing drivers and middleware for application development. In case of Raspberry Pi this includes an application to start the ARM processor(s) and provide the publicly documented API over a mailbox interface, serving as its firmware. An incomplete source of a Linux port of VCOS is available as part of the reference graphics driver published by Broadcom. [137]
- RISC OS Pi (a special cut down version RISC OS Pico, for 16 MB cards and larger for all models of Pi 1 & 2, has also been made available.)
- FreeBSD^{[138][139]}
- NetBSD^{[140][141]}
- OpenBSD (only on 64-bit platforms, such as Raspberry Pi 3)^[142]
- Plan 9 from Bell Labs^{[143][144]} and Inferno^[145] (in beta)
- Windows 10 IoT Core a zero-price edition of Windows 10 offered by Microsoft that runs natively on the Raspberry Pi 2. [146]
- <u>Haiku</u> an open source <u>BeOS</u> clone that has been compiled for the Raspberry Pi and several other ARM boards. [147] Work on Pi 1 began in 2011, but only the Pi 2 will be supported. [148]
- HelenOS a portable microkernel-based multiserver operating system; has basic Raspberry Pi support since version 0.6.0^[149]

Other operating systems (Linux-based)

- Android Things an embedded version of the Android operating system designed for IoT device development.
- <u>Arch Linux ARM</u> a port of <u>Arch Linux</u> for <u>ARM</u> processors.
- openSUSE^[150]
- SUSE Linux Enterprise Server 12 SP2^[151]
- SUSE Linux Enterprise Server 12 SP3 (Commercial support)^[151]
- Gentoo Linux^[152]

- Lubuntu^[153]
- Xubuntu^[153]
- Devuan
- CentOS for Raspberry Pi 2 and later
- RedSleeve (a RHEL port) for Raspberry Pi 1
- <u>Slackware ARM</u> version 13.37 and later runs on the Raspberry Pi without modification. [154][155][156][157] The 128–496 MB of available memory on the Raspberry Pi is at least twice the minimum requirement of 64 MB needed to run Slackware Linux on an ARM or i386 system. [158] (Whereas the majority of Linux systems boot into a <u>graphical user interface</u>, Slackware's default user environment is the <u>textual shell</u> / <u>command line interface</u>. [159]) The <u>Fluxbox</u> window manager running under the <u>X Window System</u> requires an additional 48 MB of RAM. [160]
- Kali Linux a Debian-derived distro designed for digital forensics and penetration testing.
- SolydXK a light Debian-derived distro with Xfce.
- Ark OS designed for website and email self-hosting.
- Sailfish OS with Raspberry Pi 2 (due to use ARM Cortex-A7 CPU; Raspberry Pi 1 uses different ARMv6 architecture and Sailfish requires ARMv7.)^[161]
- Tiny Core Linux a minimal <u>Linux</u> operating system focused on providing a base system using <u>BusyBox</u> and <u>FLTK</u>. Designed to run primarily in RAM.
- <u>Alpine Linux</u> a <u>Linux distribution</u> based on <u>musl</u> and <u>BusyBox</u>, primarily designed for "<u>power users</u> who appreciate security, simplicity and resource efficiency".
- Void Linux a rolling release Linux distribution which was designed and implemented from scratch, provides images based on musl or glibc.
- Fedora supports Pi 2 and later since Fedora 25 (Pi 1 is supported by some unofficial derivatives, e.g. listed here.).

Name										Мо	del A				Mode	el B				Compi Modu		
	Focus	Kernel	UserSpace	1	1 +	3	1	1 +	2	2 ver 1.2	3	3+	1	3	3 lite	PCB ver 1.2						
OpenWrt	Networking	Linux	GNU/Debian	Yes ^[162]	?	?	Yes ^[163]	Yes ^[164]	Yes ^[165]	?	Partial ^[166]	?	?	?	?	?						

Driver APIs

Raspberry Pi can use a VideoCore IV GPU via a binary blob, which is loaded into the GPU at boot time from the SD-card, and additional software, that initially was closed source. [168] This part of the driver code was later released. [169] However, much of the actual driver work is done using the closed source GPU code. Application software makes calls to closed source run-time libraries (OpenMax, OpenGL ES or OpenVG), which in turn call an open source driver inside the Linux kernel, which then calls the closed source VideoCore IV GPU driver code. The API of the kernel driver is specific for these closed libraries. Video applications use OpenMAX, 3D applications use OpenGL ES and 2D applications use OpenVG, which both in turn use EGL. OpenMAX and EGL use the open source kernel driver in turn. [170]

OpenMax OpenMa

Scheme of the implemented APIs: OpenMAX, OpenGL ES and OpenVG

Firmware

The official firmware is a freely redistributable [171] binary blob, that is proprietary software. [147] A minimal proof-of-concept open source firmware is also available, mainly aimed at initializing and starting the ARM cores as well as performing minimal startup that is required on the ARM side. It is also capable of booting a very minimal Linux kernel, with patches to remove the dependency on the mailbox interface being responsive. It is known to work on Raspberry Pi 1, 2 and 3, as well as some variants of Raspberry Pi Zero. [172]

Third-party application software

- AstroPrint AstroPrint's wireless 3D printing software can be run on the Pi 2.^[173]
- <u>C/C++</u> Interpreter Ch Released 3 January 2017, C/C++ interpreter Ch and Embedded Ch are released free for non-commercial use for Raspberry Pi, ChIDE is also included for the beginners to learn C/C++.^[174]
- Mathematica & the Wolfram Language These were released free as a standard part of the Raspbian NOOBS image in November 2013.^{[175][176][177]} As of 12 February 2020, the version is Mathematica 12.0.^[178] Programs can be run either from a command line interface or from a Notebook interface. There are Wolfram Language functions for accessing connected devices.^[179] There is also a Wolfram Language desktop development kit allowing development for Raspberry Pi in Mathematica from desktop machines, including features from the loaded Mathematica version such as image processing and machine learning.^{[180][181][182]}
- Minecraft Released 11 February 2013, a modified version that allows players to directly alter the world with computer code. [183]
- RealVNC Since 28 September 2016, Raspbian includes RealVNC's remote access server and viewer software. ^{[184][185][186]} This includes a new capture technology which allows directly-rendered content (e.g. Minecraft, camera preview and omxplayer) as well as non-X11 applications to be viewed and controlled remotely. ^{[187][188]}
- UserGate Web Filter On 20 September 2013, Florida-based security vendor Entensys announced porting UserGate Web Filter to Raspberry Pi platform. [189]
- Steam Link On 13 December 2018, Valve released official Steam Link game streaming client for the Raspberry Pi 3 and 3 B+. [190][191]

Software development tools

Arduino IDE – for programming an Arduino.

- Algoid for teaching programming to children and beginners.
- BlueJ for teaching Java to beginners.
- Greenfoot Greenfoot teaches object orientation with Java. Create 'actors' which live in 'worlds' to build games, simulations, and other graphical programs.
- Julia an interactive and cross-platform programming language/environment, that runs on the Pi 1 and later. [192] IDEs for Julia, such as Juno, are available. See also Pi-specific Github repository JuliaBerry.
- Lazarus^[193] a Free Pascal RAD IDE
- <u>LiveCode</u> an educational RAD IDE descended from <u>HyperCard</u> using English-like language to write event-handlers for WYSIWYG widgets runnable on desktop, mobile and Raspberry Pi platforms.
- Ninja-IDE a cross-platform integrated development environment (IDE) for Python.
- Processing an IDE built for the electronic arts, new media art, and visual design communities with the purpose of teaching the fundamentals of computer programming in a visual context.
- Scratch a cross-platform teaching IDE using visual blocks that stack like Lego, originally developed by MIT's Life Long Kindergarten group. The Pi version is very heavily optimised^[194] for the limited computer resources available and is implemented in the Squeak Smalltalk system. The latest version compatible with The 2 B is 1.6.
- Squeak Smalltalk a full-scale open Smalltalk.
- TensorFlow an artificial intelligence framework developed by Google. The Raspberry Pi Foundation worked with Google to simplify the installation process through pre-built binaries. [195]
- Thonny a Python IDE for beginners.
- V-Play Game Engine a cross-platform development framework that supports mobile game and app development with the V-Play Game Engine, V-Play apps, and V-Play plugins.
- Xojo a cross-platform RAD tool that can create desktop, web and console apps for Pi 2 and Pi 3.
- C-STEM Studio a platform for hands-on integrated learning of computing, science, technology, engineering, and mathematics (C-STEM) with robotics.
- Erlang a functional language for building concurrent systems with light-weight processes and message passing.

Vulnerability to flashes of light

In February 2015, a switched-mode power supply chip, designated U16, of the Raspberry Pi 2 Model B version 1.1 (the initially released version) was found to be vulnerable to flashes of light, [196] particularly the light from xenon camera flashes and green [197] and red laser pointers. However, other bright lights, particularly ones that are on continuously, were found to have no effect. The symptom was the Raspberry Pi 2 spontaneously rebooting or turning off when these lights were flashed at the chip. Initially, some users and commenters suspected that the electromagnetic pulse (EMP) from the xenon flash tube was causing the problem by interfering with the computer's digital circuitry, but this was ruled out by tests where the light was either blocked by a card or aimed at the other side of the Raspberry Pi 2, both of which did not cause a problem. The problem was narrowed down to the U16 chip by covering first the system on a chip (main processor) and then U16 with Blu-Tack (an opaque poster mounting compound). Light being the sole culprit, instead of EMP, was further confirmed by the laser pointer tests, [197] where it was also found that less opaque covering was needed to shield against the laser pointers than to shield against the xenon flashes. [196] The U16 chip seems to be bare silicon without a plastic cover (i.e. a chip-scale package or wafer-level package), which would, if present, block the light. Unofficial workarounds include covering U16 with opaque material (such as electrical tape, [196][197] lacquer, poster mounting compound, or even balled-up bread [196]), putting the Raspberry Pi 2 in a case, [197] and avoiding taking photos of the top side of the board with a xenon flash. This issue was not discovered before the release of the Raspberry Pi 2 because it is not standard or common practice to test susceptibility to optical interference, [196] while commercial electronic devices are routinely subjected to tests of susceptibility to radio interference.

Reception and use

Technology writer Glyn Moody described the project in May 2011 as a "potential BBC Micro 2.0", not by replacing PC compatible machines but by supplementing them. [198] In March 2012 Stephen Pritchard echoed the BBC Micro successor sentiment in ITPRO. [199] Alex Hope, co-author of the Next Gen report, is hopeful that the computer will engage children with the excitement of programming. [200] Co-author Ian Livingstone suggested that the BBC could be involved in building support for the device, possibly branding it as the BBC Nano. [201] The Centre for Computing History strongly supports the Raspberry Pi project, feeling that it could "usher in a new era". [202] Before release, the board was showcased by ARM's CEO Warren East at an event in Cambridge outlining Google's ideas to improve UK science and technology education. [203]



powered by a Raspberry Pi 3

Harry Fairhead, however, suggests that more emphasis should be put on improving the educational software available on existing hardware, using tools such as <u>Google App Inventor</u> to return programming to schools, rather than adding new hardware choices. [204] Simon Rockman, writing in a <u>ZDNet</u> blog, was of the opinion that teens will have "better things to do", despite what happened in the 1980s. [205]

In October 2012, the Raspberry Pi won T3's Innovation of the Year award, [206] and futurist Mark Pesce cited a (borrowed) Raspberry Pi as the inspiration for his ambient device project MooresCloud. [207] In October 2012, the British Computer Society reacted to the announcement of enhanced specifications by stating, "it's definitely something we'll want to sink our teeth into." [208]

In June 2017, Raspberry Pi won the Royal Academy of Engineering MacRobert Award. [209] The citation for the award to the Raspberry Pi said it was "for its inexpensive credit card-sized microcomputers, which are redefining how people engage with computing, inspiring students to learn coding and computer science and providing innovative control solutions for industry." [210]

Clusters of hundreds of Raspberry Pis have been used for testing programs destined for supercomputers^[211]

Community

The Raspberry Pi community was described by Jamie Ayre of <u>FLOSS</u> software company <u>AdaCore</u> as one of the most exciting parts of the project.^[212] Community blogger Russell Davis said that the community strength allows the Foundation to concentrate on documentation and teaching.^[212] The community developed a <u>fanzine</u> around the platform called <u>The MagPi</u>^[213] which in 2015, was handed over to the Raspberry Pi Foundation by its volunteers to be continued in-house.^[214] A series of community <u>Raspberry Jam</u> events have been held across the UK and around the world.^[215]

Education

As of January 2012, enquiries about the board in the United Kingdom have been received from schools in both the state and private sectors, with around five times as much interest from the latter. It is hoped that businesses will sponsor purchases for less advantaged schools. [216] The CEO of Premier Farnell said that the government of a country in the Middle East has expressed interest in providing a board to every schoolgirl, to enhance her employment prospects. [217][218]

In 2014, the Raspberry Pi Foundation hired a number of its community members including ex-teachers and software developers to launch a set of free learning resources for its website. ^[219] The Foundation also started a teacher training course called Picademy with the aim of helping teachers prepare for teaching the new computing curriculum using the Raspberry Pi in the classroom. ^[220]

In 2018, NASA launched the JPL Open Source Rover Project, which is a scaled down of Curiosity rover and uses a Raspberry Pi as the control module, to encourage students and hobbyists to get involved in mechanical, software, electronics, and robotics engineering. [221]

Home automation

There are a number of developers and applications that are using the Raspberry Pi for home automation. These programmers are making an effort to modify the Raspberry Pi into a cost-affordable solution in energy monitoring and power consumption. Because of the relatively low cost of the Raspberry Pi, this has become a popular and economical alternative to the more expensive commercial solutions. [222]

Industrial automation

In June 2014, Polish industrial automation manufacturer TECHBASE released ModBerry, an industrial computer based on the Raspberry Pi Compute Module. The device has a number of interfaces, most notably RS-485/232 serial ports, digital and analogue inputs/outputs, CAN and economical 1-Wire buses, all of which are widely used in the automation industry. The design allows the use of the Compute Module in harsh industrial environments, leading to the conclusion that the Raspberry Pi is no longer limited to home and science projects, but can be widely used as an Industrial IoT solution and achieve goals of Industry 4.0.^[223]

In March 2018, SUSE announced commercial support for SUSE Linux Enterprise on the Raspberry Pi 3 Model B to support a number of undisclosed customers implementing industrial monitoring with the Raspberry Pi. [224]

Commercial products

OTTO is a digital camera created by Next Thing Co. It incorporates a Raspberry Pi Compute Module. It was successfully crowd-funded in a May 2014 Kickstarter campaign. [225]

Slice is a <u>digital media player</u> which also uses a Compute Module as its heart. It was crowd-funded in an August 2014 Kickstarter campaign. The software running on Slice is based on Kodi. [226]

Astro Pi

A project was launched in December 2014 at an event held by the UK Space Agency. The Astro Pi competition was officially opened in January and was opened to all primary and secondary school aged children who were residents of the United Kingdom. During his mission, British ESA astronaut Tim Peake deployed the computers on board the International Space Station. [227] He loaded the winning code while in orbit, collected the data generated and then sent this to Earth where it was distributed to the winning teams. Covered themes during the competition included Spacecraft Sensors, Satellite Imaging, Space Measurements, Data Fusion and Space Radiation.

The organisations involved in the Astro Pi competition include the UK Space Agency, UKspace, Raspberry Pi, ESERO-UK and ESA.

History

In 2006, early concepts of the Raspberry Pi were based on the <u>Atmel ATmega644</u> microcontroller. Its schematics and <u>PCB</u> layout are publicly available. [228] Foundation <u>trustee Eben Upton</u> assembled a group of teachers, academics and computer enthusiasts to devise a computer to inspire children. [216] The computer is inspired by Acorn's <u>BBC Micro</u> of 1981. [229][230] The Model A, Model B and Model B+ names are references to the original models of the British educational <u>BBC Micro</u> computer, developed by <u>Acorn Computers</u>. [231] The first ARM prototype version of the computer was mounted in a package the same size as a <u>USB memory stick</u>. [232] It had a USB port on one end and an HDMI port on the other.

The Foundation's goal was to offer two versions, priced at US\$25 and \$35. They started accepting orders for the higher priced Model B on 29 February 2012, $^{[233]}$ the lower cost Model A on 4 February 2013, $^{[234]}$ and the even lower cost (US\$20) A+ on 10 November 2014, $^{[68]}$ On 26 November 2015, the cheapest Raspberry Pi yet, the Raspberry Pi Zero, was launched at US\$5 or £4. $^{[235]}$



An early alpha-test board in operation using different layout from later beta and production boards

Pre-launch

- July 2011: Trustee <u>Eben Upton</u> publicly approached the <u>RISC OS Open community</u> in July 2011 to enquire about assistance with a port. [236] Adrian Lees at Broadcom has since worked on the port, [237][238] with his work being cited in a discussion regarding the graphics drivers. [239] This port is now included in NOOBS.
- August 2011 50 alpha boards are manufactured. These boards were functionally identical to the planned Model B, [240] but they were physically larger to accommodate debug headers. Demonstrations of the board showed it running the LXDE desktop on Debian,

- Quake 3 at 1080p, [241] and Full HD MPEG-4 video over HDMI. [242]
- October 2011 A version of RISC OS 5 was demonstrated in public, and following a year of development the port was released for general consumption in November 2012. [243][244][245][246]
- December 2011 Twenty-five Model B Beta boards were assembled and tested^[247] from one hundred unpopulated PCBs.^[248] The component layout of the Beta boards was the same as on production boards. A single error was discovered in the board design where some pins on the CPU were not held high; it was fixed for the first production run.^[249] The Beta boards were demonstrated booting Linux, playing a 1080p movie trailer and the Rightware Samurai OpenGL ES benchmark.^[250]
- Early 2012 During the first week of the year, the first 10 boards were put up for auction on eBay. [251][252] One was bought anonymously and donated to the museum at The Centre for Computing History in Cambridge, England. [202][253] The ten boards (with a total retail price of £220) together raised over £16,000, [254] with the last to be auctioned, serial number No. 01, raising £3,500. [255] In advance of the anticipated launch at the end of February 2012, the Foundation's servers struggled to cope with the load placed by watchers repeatedly refreshing their browsers. [256]

Launch

- 19 February 2012 The first proof of concept SD card image that could be loaded onto an SD card to produce a preliminary operating system is released. The image was based on <u>Debian</u> 6.0 (Squeeze), with the <u>LXDE</u> desktop and the <u>Midori browser</u>, plus various programming tools. The image also runs on <u>QEMU</u> allowing the Raspberry Pi to be emulated on various other platforms. [257][258]
- 29 February 2012 Initial sales commence 29 February 2012^[259] at 06:00 UTC;. At the same time, it was announced that the model A, originally to have had 128 MB of RAM, was to be upgraded to 256 MB before release. ^[233] The Foundation's website also announced: "Six years after the project's inception, we're nearly at the end of our first run of development although it's just the beginning of the Raspberry Pi story. ^[260] The web-shops of the two licensed manufacturers selling Raspberry Pi's within the United Kingdom, Premier Farnell and RS Components, had their websites stalled by heavy web traffic immediately after the launch (RS Components briefly going down completely). ^[261] Unconfirmed reports suggested that there were over two million expressions of interest or preorders. ^[263] The official Raspberry Pi Twitter account reported that Premier Farnell sold out within a few minutes of the initial launch, while RS Components took over 100,000 pre orders on day one. ^[233] Manufacturers were reported in March 2012 to be taking a "healthy number" of pre-orders. ^[212]
- March 2012 Shipping delays for the first batch were announced in March 2012, as the result of installation of an incorrect Ethernet port, [264][265] but the Foundation expected that manufacturing quantities of future batches could be increased with little difficulty if required. [266] "We have ensured we can get them [the Ethernet connectors with magnetics] in large numbers and Premier Farnell and RS Components [the two distributors] have been fantastic at helping to source components," Upton said. The first batch of 10,000 boards was manufactured in Taiwan and China. [267][268]
- 8 March 2012 Release Raspberry Pi Fedora Remix, the recommended Linux distribution, ^[269] developed at <u>Seneca College</u> in Canada. ^[270]
- March 2012 The Debian port is initiated by Mike Thompson, former CTO of Atomz. The effort was largely carried out by Thompson and Peter Green, a volunteer Debian developer, with some support from the Foundation, who tested the resulting binaries that the two produced during the early stages (neither Thompson nor Green had physical access to the hardware, as boards were not widely accessible at the time due to demand). [271] While the preliminary proof of concept image distributed by the Foundation before launch was also Debian-based, it differed from Thompson and Green's Raspbian effort in a couple of ways. The POC image was based on then-stable Debian Squeeze, while Raspbian aimed to track then-upcoming Debian Wheezy packages. [258] Aside from the updated packages that would come with the new release, Wheezy was also set to introduce the armhf architecture, [272] which became the raison d'être for the Raspbian effort. The Squeeze-based POC image was limited to the armel architecture, which was, at the time of Squeeze's release, the latest attempt by the Debian project to have Debian run on the newest ARM embedded-application binary interface (EABI). [273] The armhf architecture in Wheezy intended to make Debian run on the ARM VFP hardware floating-point unit, while armel was limited to emulating floating point operations in software. [274][275] Since the Raspberry Pi included a VFP, being able to make use of the hardware unit would result in performance gains and reduced power use for floating point operations. [271] The armhf effort in mainline Debian, however, was orthogonal to the work surrounding the Pi and only intended to allow Debian to run on ARMv7 at a minimum, which would mean the Pi, an ARMv6 device, would not benefit. [272] As a result, Thompson and Green set out to build the 19,000 Debian packages for the device using a custom build cluster.

Post-launch

- 16 April 2012 Reports appear from the first buyers who had received their Raspberry Pi. [276][277]
- 20 April 2012 The schematics for the Model A and Model B are released. [278]
- 18 May 2012 The Foundation reported on its blog about a prototype <u>camera module</u> they had tested. [279] The prototype used a 14-megapixel module.
- 22 May 2012 Over 20,000 units had been shipped. [280]
- July 2012 Release of Raspbian. [281]
- 16 July 2012 It was announced that 4,000 units were being manufactured per day, allowing Raspberry Pis to be bought in bulk. [282][283]
- 24 August 2012 Hardware accelerated video (<u>H.264</u>) encoding becomes available after it became known that the existing license also covered encoding. Formerly it was thought that encoding would be added with the release of the announced camera module. [284][285] However, no stable software exists for hardware H.264 encoding. At the same time the Foundation released two additional codecs that can be bought separately, MPEG-2 and Microsoft's VC-1. Also it was announced that the Pi will implement CEC, enabling it to be controlled with the television's remote control. [82]
- 5 September 2012 The Foundation announced a second revision of the Raspberry Pi Model B.^[287] A revision 2.0 board is announced, with a number of minor corrections and improvements.^[288]
- 6 September 2012 Announcement that in future the bulk of Raspberry Pi units would be manufactured in the UK, at <u>Sony</u>'s manufacturing facility in <u>Pencoed</u>, Wales. The Foundation estimated that the plant would produce 30,000 units per month, and would create about 30 new jobs. [289][290]

- 15 October 2012 It is announced that new Raspberry Pi Model Bs are to be fitted with 512 MB instead of 256 MB RAM. [291]
- 24 October 2012 The Foundation announces that "all of the VideoCore driver code which runs on the ARM" had been released as free software under a BSD-style license, making it "the first ARM-based multimedia SoC with fully-functional, vendor-provided (as opposed to partial, reverse engineered) fully open-source drivers", although this claim has not been universally accepted. [169] On 28 February 2014, they also announced the release of full documentation for the VideoCore IV graphics core, and a complete source release of the graphics stack under a 3-clause BSD license [292][293]
- October 2012 It was reported that some customers of one of the two main distributors had been waiting more than six months for
 their orders. This was reported to be due to difficulties in sourcing the CPU and conservative sales forecasting by this distributor. [294]
- 17 December 2012 The Foundation, in collaboration with IndieCity and Velocix, opens the Pi Store, as a "one-stop shop for all your Raspberry Pi (software) needs". Using an application included in <u>Raspbian</u>, users can browse through several categories and download what they want. Software can also be uploaded for moderation and release. [295]
- 3 June 2013 "New Out of Box Software" or <u>NOOBS</u> is introduced. This makes the Raspberry Pi easier to use by simplifying the installation of an operating system. Instead of using specific software to prepare an SD card, a file is unzipped and the contents copied over to a FAT formatted (4 GB or bigger) SD card. That card can then be booted on the Raspberry Pi and a choice of six operating systems is presented for installation on the card. The system also contains a recovery partition that allows for the quick restoration of the installed OS, tools to modify the config.txt and an online help button and web browser which directs to the Raspberry Pi Forums. [296]
- October 2013 The Foundation announces that the one millionth Pi had been manufactured in the United Kingdom. [297]
- November 2013: they announce that the two millionth Pi shipped between 24 and 31 October. [298]
- 28 February 2014 On the day of the second anniversary of the Raspberry Pi, Broadcom, together with the Raspberry Pi foundation, announced the release of full documentation for the VideoCore IV graphics core, and a complete source release of the graphics stack under a 3-clause BSD license. [292][293]
- 7 April 2014 The official Raspberry Pi blog announced the Raspberry Pi Compute Module, a device in a 200-pin DDR2 <u>SO-DIMM</u>-configured memory module (though not in any way compatible with such RAM), intended for consumer electronics designers to use as the core of their own products. ^[79]
- June 2014 The official Raspberry Pi blog mentioned that the three millionth Pi shipped in early May 2014 [299]
- 14 July 2014 The official Raspberry Pi blog announced the Raspberry Pi Model B+, "the final evolution of the original Raspberry Pi. For the same price as the original Raspberry Pi model B, but incorporating numerous small improvements people have been asking for". [69]
- 10 November 2014 The official Raspberry Pi blog announced the Raspberry Pi Model A+. [68] It is the smallest and cheapest (US\$20) Raspberry Pi so far and has the same processor and RAM as the Model A. Like the A, it has no Ethernet port, and only one USB port, but does have the other innovations of the B+, like lower power, micro-SD-card slot, and 40-pin HAT compatible GPIO.
- 2 February 2015 The official Raspberry Pi blog announced the Raspberry Pi 2. Looking like a Model B+, it has a 900 MHz quad-core ARMv7 Cortex-A7 CPU, twice the memory (for a total of 1 GB) and complete compatibility with the original generation of Raspberry Pis. [300]
- 14 May 2015 The price of Model B+ was decreased from US\$35 to \$25, purportedly as a "side effect of the production optimizations" from the Pi 2 development. [301] Industry observers have skeptically noted, however, that the price drop appeared to be a direct response to the CHIP, a lower-priced competitor discontinued in April 2017. [302]



Raspberry Pi Compute Module



Raspberry Pi Model B

- 26 November 2015 The Raspberry Pi Foundation launched the Raspberry Pi Zero, the smallest and cheapest member of the Raspberry Pi family yet, at 65 mm × 30 mm, and US\$5. The Zero is similar to the Model A+ without camera and LCD connectors, while smaller and uses less power. It was given away with the Raspberry Pi magazine Magpi No. 40 that was distributed in the UK and US that day the MagPi was sold out at almost every retailer internationally due to the freebie. [76]
- 29 February 2016 Raspberry Pi 3 with a BCM2837 1.2 GHz 64-bit quad processor based on the ARMv8 Cortex-A53, with built-in Wi-Fi BCM43438 802.11n 2.4 GHz and Bluetooth 4.1 Low Energy (BLE). Starting with a 32-bit Raspbian version, with a 64-bit version later to come if "there is value in moving to 64-bit mode". In the same announcement it was said that a new BCM2837 based Compute Module was expected to be introduced a few months later.^[303]
- February 2016 The Raspberry Pi Foundation announces that they had sold eight million devices (for all models combined), making it the best-selling UK personal computer, ahead of the Amstrad PCW.^{[304][305]} Sales reached ten million in September 2016.^[9]
- 25 April 2016 Raspberry Pi Camera v2.1 announced with 8 Mpixels, in normal and NoIR (can receive IR) versions. The camera uses the Sony IMX219 chip with a resolution of 3280 × 2464. To make use of the new resolution the software has to be updated. [306]
- 10 October 2016 NEC Display Solutions announces that select models of commercial displays to be released in early 2017 will incorporate a Raspberry Pi 3 Compute Module. [307]
- 14 October 2016 Raspberry Pi Foundation announces their co-operation with NEC Display Solutions. They expect that the Raspberry Pi 3 Compute Module will be available to the general public by the end of 2016. [308]
- 25 November 2016 11 million units sold. [11]
- 16 January 2017 Compute Module 3 and Compute Module 3 Lite are launched. [74]
- 28 February 2017 Raspberry Pi Zero W with WiFi and Bluetooth via chip scale antennas launched. [309][310]
- 14 March 2018 On Pi Day, Raspberry Pi Foundation introduced Raspberry Pi 3 Model B+ with improvements in the Raspberry Pi 3B computers performance, updated version of the Broadcom application processor, better wireless Wi-Fi and Bluetooth performance and addition of the 5 GHz band.^[311]
- 15 November 2018 Raspberry Pi 3 Model A+ launched. [312]
- 28 January 2019 Compute Module 3+ (CM3+/Lite, CM3+/8GB, CM3+/16GB and CM3+/32GB) launched. [75]
- 24 June 2019 Raspberry Pi 4 Model B launched. [313]
- 10 December 2019 30 million units sold; [314] sales are about 6 million per year. [315][316]

See also

Plug computer

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Further reading

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- Getting Started with Raspberry Pi, Matt Richardson and Shawn Wallace; 2013; ISBN 978-1449344214.
- Raspberry Pi User Guide, Eben Upton and Gareth Halfacree; 2014; ISBN 978-1118921661.
- Hello Raspberry Pi!; Ryan Heitz; 2016; ISBN 978-1617292453.

External links

- Official website (https://www.raspberrypi.org) *
- Raspberry Pi Wiki, supported by the RPF (https://elinux.org/RaspberryPiBoard)
- The MagPi newsletter (http://www.themagpi.com)
- Raspberry Pi pinout (https://www.panu.it/raspberry/) board GPIO pinout
- Raspberry Pi component map (http://raspmap.everpi.net)
- RaspberryPi Boards: Hardware versions/revisions (https://elinux.org/RaspberryPi_Boards)
- ARM1176JZF-S (ARM11 CPU Core) Technical Reference Manual (http://infocenter.arm.com/help/topic/com.arm.doc.ddi0301h/DDI030 1H arm1176jzfs r0p7 trm.pdf), ARM Holdings.

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