Computer Architecture MP 2 Report

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1 Introduction

This report presents the design and implementation of an RGB LED controller circuit for the iceBlinkPico FPGA board. The controller creates a smooth and continuous transition through the color spectrum by modulating the duty cycles of three PWM signals that drive the red, green, and blue LED components. The circuit implements a full 360 degree color wheel, providing an aesthetically pleasing visual demonstration of digital control systems and PWM techniques.

2 Design Overview

The system architecture consists of three primary components:

- 1. Main RGB LED Controller Module (rgb_led_controller.sv): Manages the color transitions by generating appropriate PWM duty cycle values for each color channel based on the current position in the color wheel.
- 2. **PWM Generator Module (pwm.sv)**: Three instances of this module convert the 8-bit duty cycle values into PWM signals for each color channel (red, green, and blue).
- 3. **Top-level Module (top.sv)**: Interfaces the controller with the physical hardware, including the necessary configuration for the RGB LED on the iceBlinkPico board.

3 Circuit Operation

3.1 Color Transition Algorithm

The design implements a full RGB color wheel by dividing it into six 60-degree segments, each representing a transition between two primary colors:

- Segment 0 (0°-60°): Red = 100%, Blue = 0%, Green ramps from 0% to 100% (Red \rightarrow Yellow)
- Segment 1 (60°-120°): Green = 100%, Blue = 0%, Red ramps from 100% to 0% (Yellow \rightarrow Green)
- Segment 2 (120°-180°): Green = 100%, Red = 0%, Blue ramps from 0% to 100% (Green \rightarrow Cyan)
- Segment 3 (180°-240°): Blue = 100%, Red = 0%, Green ramps from 100% to 0% (Cyan \rightarrow Blue)
- Segment 4 (240°-300°): Blue = 100%, Green = 0%, Red ramps from 0% to 100% (Blue \rightarrow Magenta)
- Segment 5 (300°-360°): Red = 100%, Green = 0%, Blue ramps from 100% to 0% (Magenta \rightarrow Red)

This approach ensures smooth transitions between colors while maintaining high color saturation in operation.

3.2 Timing Control

The color wheel cycle is controlled by the following parameters:

$$CLK_FREQ = 12,000,000 Hz$$
 (12 MHz clock from the iceBlinkPico board) (1)

$$TOTAL_STEPS = 360$$
 steps to complete a full color wheel (1 step per degree) (2)

$$STEP_SIZE = 60$$
 steps per segment (matching the 60-degree segments) (3)

$$CYCLES_PER_STEP = \frac{CLK_FREQ}{TOTAL_STEPS} = 33,333 \text{ clock cycles per step}$$
 (4)

These parameters determine how quickly the LED cycles through the color spectrum. The step counter increments after CYCLES_PER_STEP clock cycles, and the color values are updated based on the current step position in the color wheel.

3.3 PWM Implementation

Each color channel uses an 8-bit PWM implementation:

- Resolution: 8 bits (256 brightness levels from 0 to 255)
- Counter Range: 0 to 255, rolling over to create the PWM period
- Duty Cycle Control: Output is high when the counter is less than the specified duty cycle value

4 Simulation Results

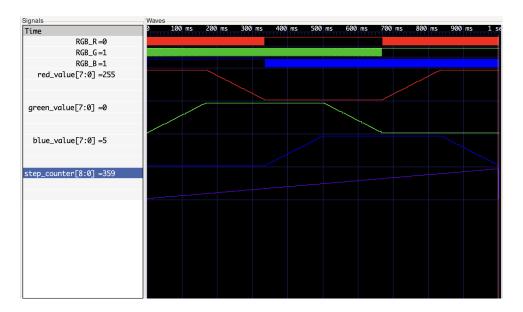


Figure 1: RGB Signal Simulation in GTKWave

The above GTKWave simulation plot demonstrates that the RGB signals change over time as the controller cycles through the color wheel. The plot shows:

- 1. The step counter gradually increasing up to 360 step size
- 2. The red, green, and blue PWM duty cycle values changing according to the current segment
- 3. How only one or two color components change at any given time while others remain at 0% or 100%

The simulation confirms the expected behavior: smooth transitions between colors, with one color ramping up while another ramps down in each segment, creating the full spectrum of colors in the RGB color wheel.

5 Hardware Implementation

The RGB LED on the iceBlinkPico board requires the PWM signals to be inverted:

```
RGB LED

assign RGB_R = ~red_led;
assign RGB_G = ~green_led;
assign RGB_B = ~blue_led;
```

The circuit uses the board's built-in 12 MHz clock as the timing reference.

6 Source Code

6.1 RGB LED Controller Module

```
_ RGB LED Controller Module (rgb_led_controller.sv) _
    module rgb_led_controller#(
        parameter CLK_FREQ = 12_000_000
    ) (
        input logic clk,
        output logic red_led,
        output logic green_led,
        output logic blue_led
    );
10
        localparam TOTAL_STEPS = 360;
11
        localparam STEP_SIZE = 60;
        localparam CYCLES_PER_STEP = CLK_FREQ / TOTAL_STEPS;
12
13
        logic [$clog2(CYCLES_PER_STEP)-1:0] cycle_counter = 0;
14
        logic [8:0] step_counter = 0; // 0 to 359
15
16
        // PWM values for RGB
17
        logic [7:0] red_value
                                 = 8'd255;
18
        logic [7:0] green_value = 8'd0;
19
        logic [7:0] blue_value = 8'd0;
20
21
        // Step counter logic
22
        always_ff @(posedge clk) begin
23
            if (cycle_counter >= CYCLES_PER_STEP - 1) begin
24
                cycle_counter <= 0;</pre>
25
                if (step_counter >= TOTAL_STEPS - 1)
26
                     step_counter <= 0;</pre>
                 else
                     step_counter <= step_counter + 1;</pre>
            \quad \text{end} \quad
            else begin
                cycle_counter <= cycle_counter + 1;</pre>
            end
34
        end
35
        always_comb begin
36
            case (step_counter / STEP_SIZE)
37
                // Segment 0: 0 to 60 degrees
                 // Red = 100%, Blue = 0%, Green ramps 0% to 100%
39
40
                     red_value = 8'd255;
41
                     blue_value = 8'd0;
42
                     green_value = ((step_counter % STEP_SIZE) * 255) / STEP_SIZE;
43
                end
44
```

```
// Segment 1: 60 to 120 degrees
46
                 // Green = 100%, Blue = 0%, Red ramps 100% down to 0%
47
                 1: begin
48
                     green_value = 8'd255;
49
                     blue_value = 8'd0;
50
                     red_value = 8'd255 - ((step_counter % STEP_SIZE) * 255) / STEP_SIZE;
51
                 end
52
53
                 // Segment 2: 120 to 180 degrees
54
                 // Green = 100%, Red = 0%, Blue ramps 0% to 100%
55
                 2: begin
                     green_value = 8'd255;
                     red_value = 8'd0;
                     blue_value = ((step_counter % STEP_SIZE) * 255) / STEP_SIZE;
59
60
61
                // Segment 3: 180 to 240 degrees
62
                 // Blue = 100%, Red = 0%, Green ramps 100% down to 0%
63
64
                     blue_value = 8'd255;
65
                     red_value = 8'd0;
66
                     green_value = 8'd255 - ((step_counter % STEP_SIZE) * 255) / STEP_SIZE;
67
                 end
68
69
                // Segment 4: 240 to 300 degrees
70
                 // Blue = 100%, Green = 0%, Red ramps 0% to 100%
71
                 4: begin
72
                     blue_value = 8'd255;
73
74
                     green_value = 8'd0;
                     red_value = ((step_counter % STEP_SIZE) * 255) / STEP_SIZE;
75
76
                 end
77
                 // Segment 5: 300 to 360 degrees
78
                 // Red = 100%, Green = 0%, Blue ramps 100% down to 0%
79
                 5: begin
80
                     red_value = 8'd255;
81
                     green_value = 8'd0;
82
                     blue_value = 8'd255 - ((step_counter % STEP_SIZE) * 255) / STEP_SIZE;
83
84
85
                 default: begin
86
                     red_value = 8'd255;
87
                     green_value = 8'd0;
88
                     blue_value = 8'd0;
89
                 end
90
             endcase
91
        end
92
93
94
        pwm red_pwm (
95
           .clk(clk),
             .duty_cycle(red_value),
97
             .pwm_out(red_led)
        );
100
        pwm green_pwm (
101
            .clk(clk),
102
             .duty_cycle(green_value),
             .pwm_out(green_led)
103
104
105
        pwm blue_pwm (
106
```

```
107 .clk(clk),
108 .duty_cycle(blue_value),
109 .pwm_out(blue_led)
110 );
111
112 endmodule
```

6.2 PWM Module

6.3 Top-level Module

```
\_ Top-level Module (top.sv) \_
    `include "pwm.sv"
    `include "rgb.sv"
    module top (
        input logic clk,
        output logic RGB_R,
        output logic RGB_G,
        output logic RGB_B
    );
        logic red_led;
10
        logic green_led;
11
        logic blue_led;
12
13
        rgb_led_controller rgb_controller (
14
            .clk(clk),
15
            .red_led(red_led),
16
            .green_led(green_led),
17
            .blue_led(blue_led)
18
19
20
        assign RGB_R = ~red_led;
21
        assign RGB_G = ~green_led;
22
        assign RGB_B = ~blue_led;
    \verb"endmodule"
```