

Arquitetura de um Sistema Computacional

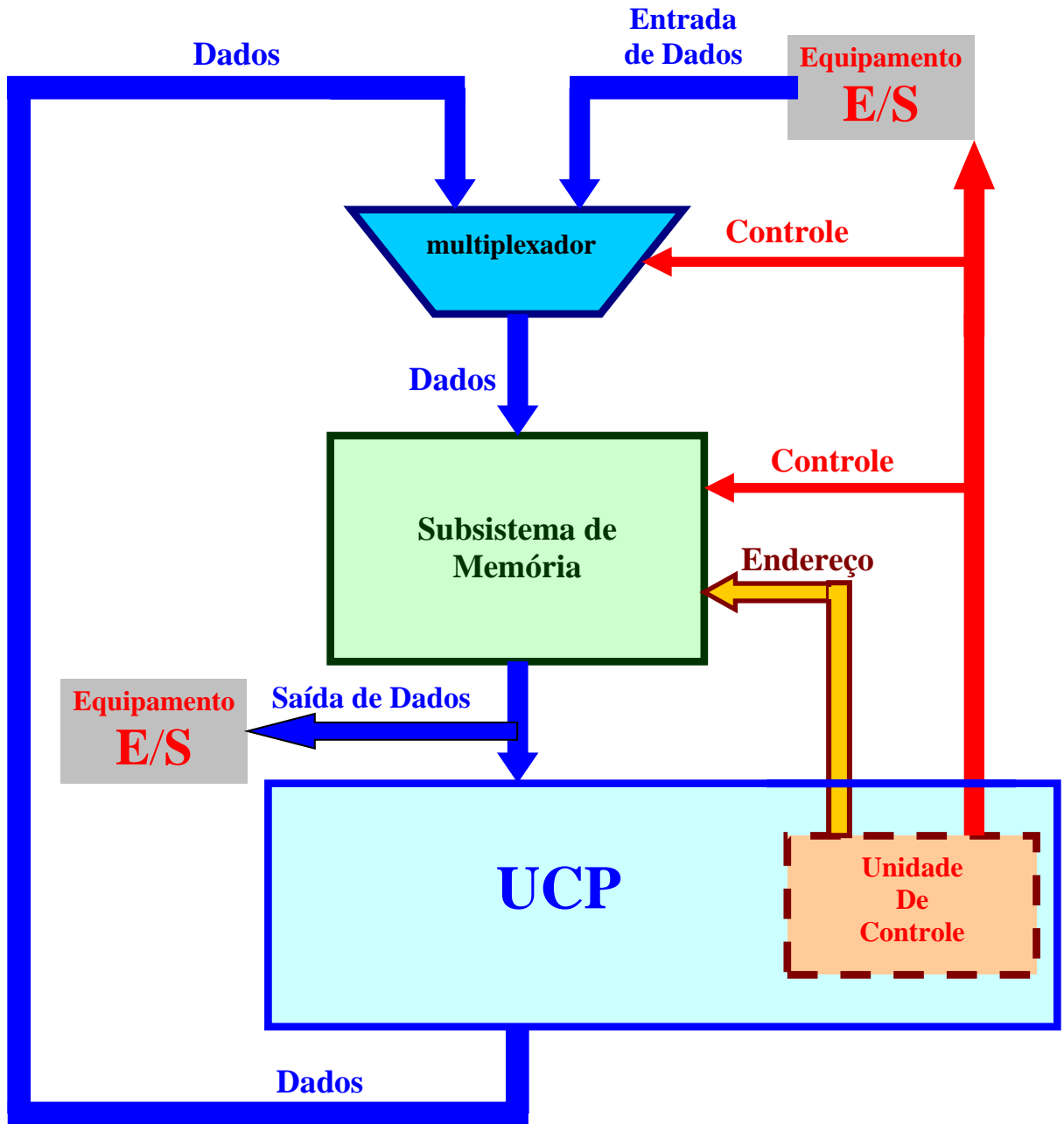


Diagrama de Blocos de uma Arquitetura Genérica

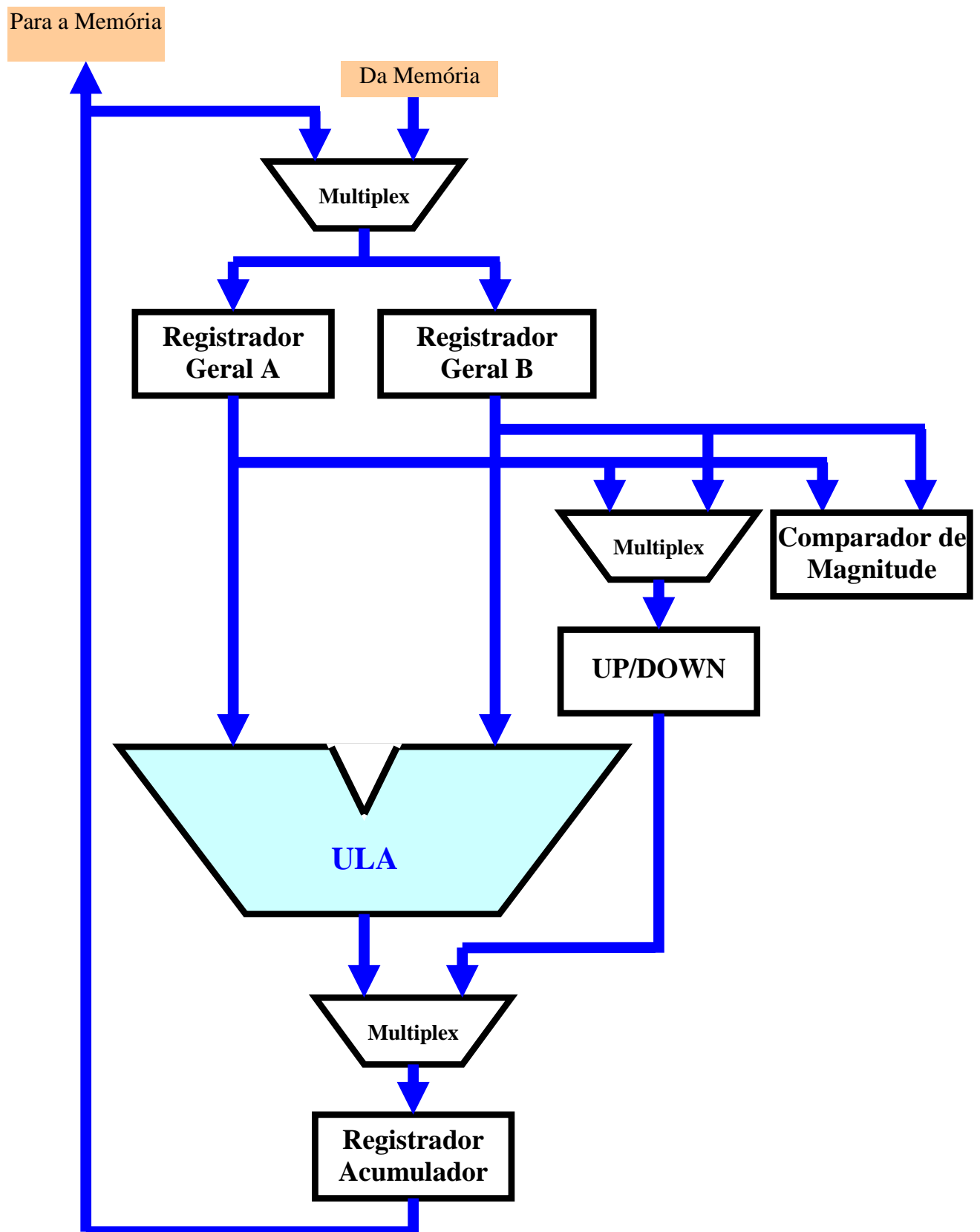


Diagrama de blocos de uma UCP genérica

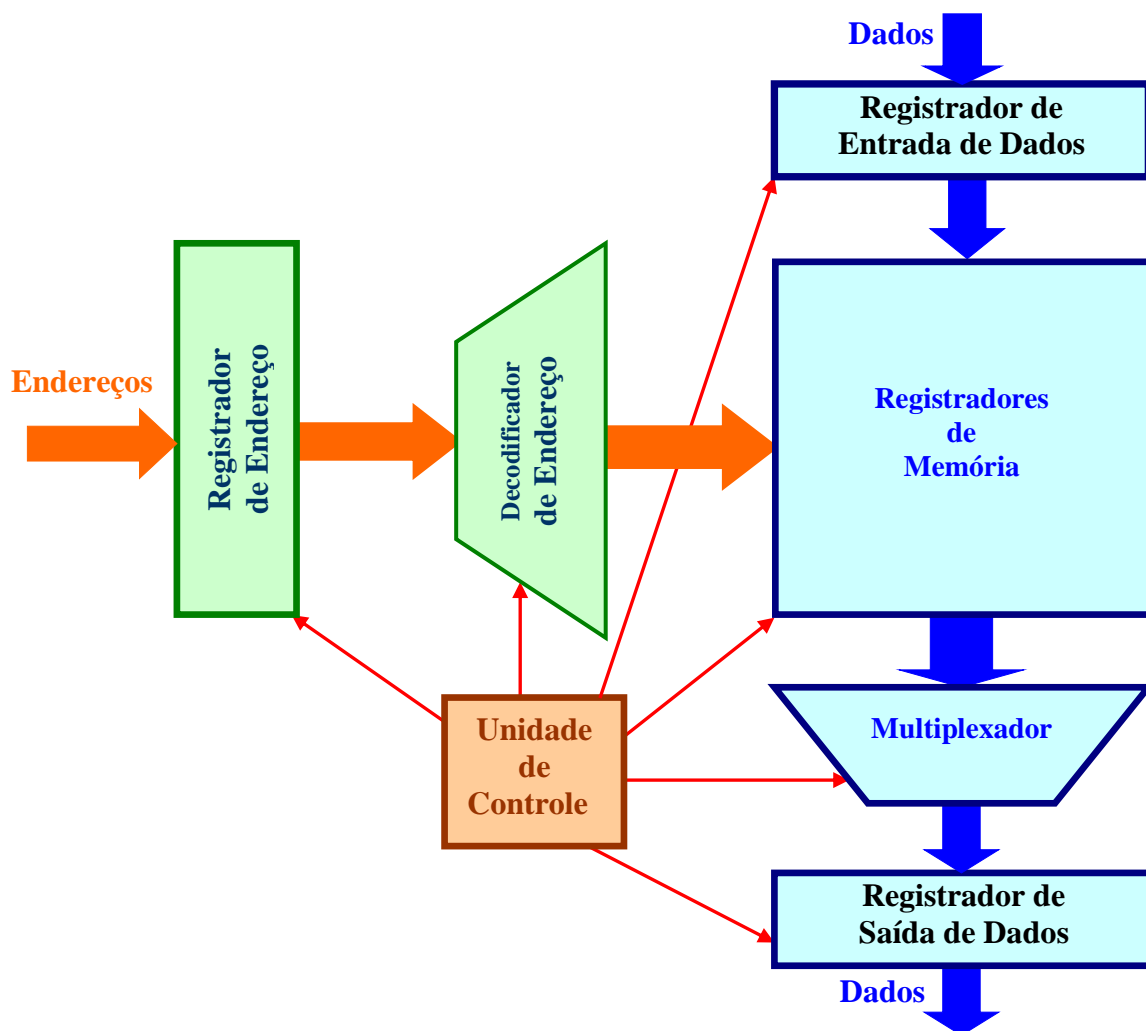
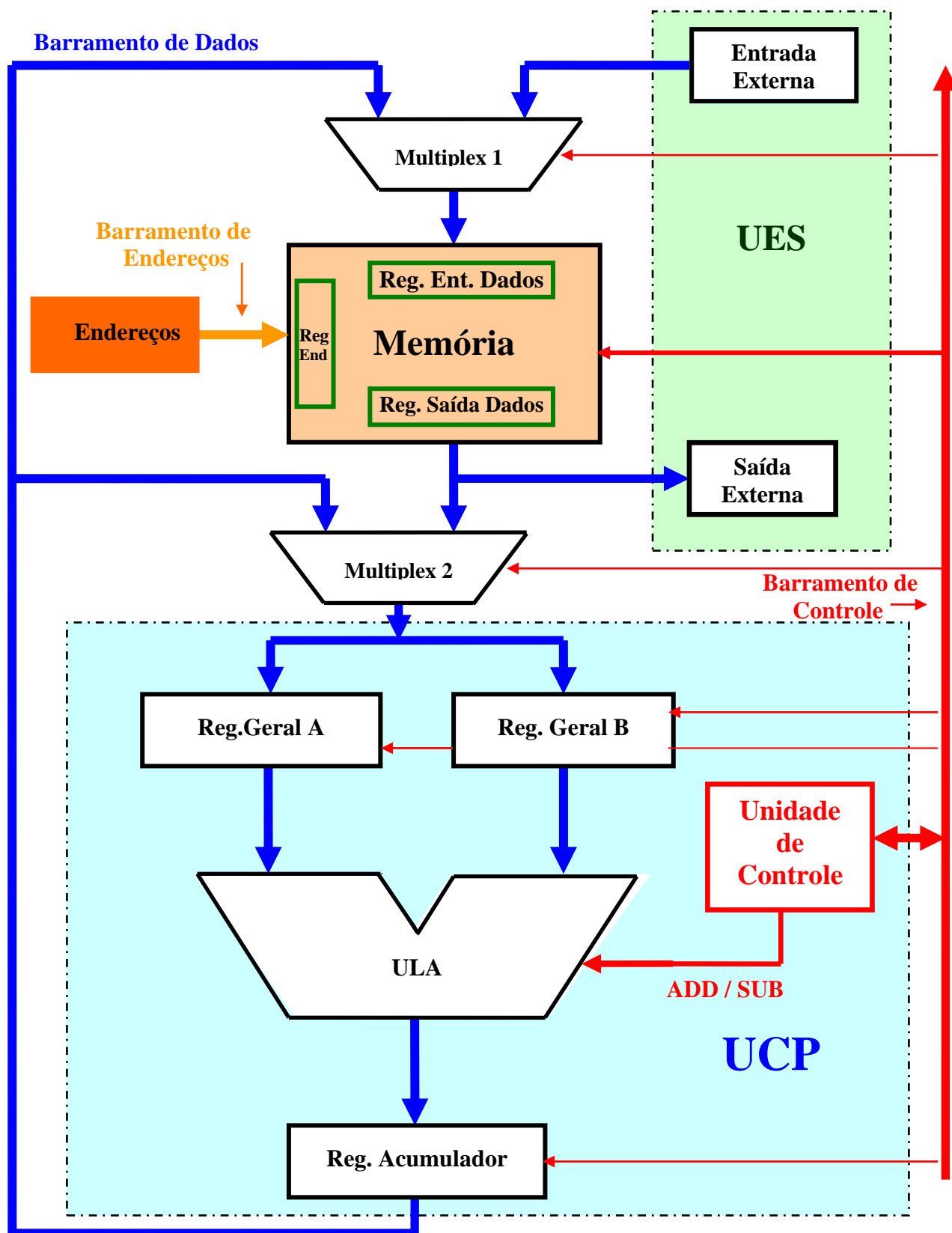


Diagrama de blocos de um Subsistema de Memória Semicondutora



Arquitetura Genérica Expandida



Subsistemas de Uma UCP