Catalina P2 Command and Options Summary

Command (1)	C sources (1 +)	Propeller Hardware Version (0+)	C library (<= 1)	Maths library (<= 1)	Other libraries (0 +)	Propeller Base Platform (<= 1)	Memory Model (<= 1)	Human/Machine Interface and Graphics Option (<= 1)	Clock Frequency Options (<= 1)	Miscellaneous Options (0 +)
								-C TTY		-C NO_SCREEN
								-C SIMPLE		-C NO_MOUSE
								-C VGA		-C NO_KEYBOARD
								-C LORES_VGA		
								-C HIRES_VGA		-C CR_ON_LF
			-lc	-lm	-ltiny	-C P2_EVAL	-C NATIVE	-C VGA_640	-C MHZ_200	-C NO_CR_TO_LF
catalina	program.c	-p1	-lci	-lma	-lserial2	-C P2D2	-C COMPACT	-C VGA_800	-C MHZ_220	-C NON_ANSI_HMI
		-p2	-lcix	-lmb	-lserial8	-C P2_EDGE	-C TINY	-C VGA_1024	-C MHZ_260	
			-lcx	-lmc	-lthreads	-C P2_CUSTOM		-C MONO		-C CLOCK
					-linterrupts			-C COLOR_1		
					-lpsram			-C COLOR_4		-C NO_FLOAT
								-C COLOR_8		-C NO_ARGS
								-C COLOR_24		-C NO_REBOOT
								-C NO_HMI		-C NO_EXIT
										-C P2_REV_A

All Command Line Options:

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-? or -h   print this help (and exit)
-B baud    baud rate to use for serial interfaces (P2 only)
-c         compile only (do not bind)
-d         output diagnostic messages
-C symbol  define a Catalina symbol (e.g. -C NO_GAMEPAD)
-D symbol  define a symbol (e.g. -D printf=tiny_printf)
-E         allowable frequency error (default is 100k
-f freq    required clock frequency (see also -F & -E
-F freq    crystal frequency (default is 20M)
-g[level]  generate debugging information (default level = 1)
-H addr    address of top of heap
-I path    path to include files (default 'C:\Program Files (x86)\Catalina\include')
-k         kill (suppress) statistics output
-l lib     search library lib when binding
-L path    path to libraries (default 'C:\Program Files (x86)\Catalina')
-M size    maximum memory size (use with -x)
-o name    name of output file (default is first file name)
-O[level]  optimize code (default level = 1)
-p ver     Propeller hardware version 1 or 2 (1 is the default)
-q         enable quick build (re-use any existing target files)
-P addr    address for Read-Write segments
-R addr    address for Read-Only segments
-S         compile to assembly code (do not bind)
-t name    name of dedicated target to use
-T path    path to target files (default 'C:\Program Files (x86)\Catalina\target')
-U symbol  undefine symbol (e.g. -U DEFAULT)
-v         verbose (output information messages)
-v -v      very verbose (more information messages)
-W option  option to pass directly to LCC
-x layout  use specified segment layout (layout = 0 .. 6, 8 .. 11)
-z         don't invoke the parallelizer on input files that follow
-Z         invoke the parallelizer on input files that follow
```