## Linux内核锁正确使用和调试

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微信群直播:

https://mp.weixin.qq.com/s/yyN\_JkJ2bo9K-fovTGLI4g

扫描二维码报名



## 麦当劳喜欢您来,喜欢您再来



# 扫描光注 Limuxer



## 大纲

- \*原子性
- \*RMW
- \*atomic
- \*锁住语义整体
- \*spinlock
- \*irq关
- \*mutex
- \*lockup detector

## +1也不是原子的(RISC vs. CISC)

■哪怕一个整数的+1,也不是原子的。要经过读-修改-写

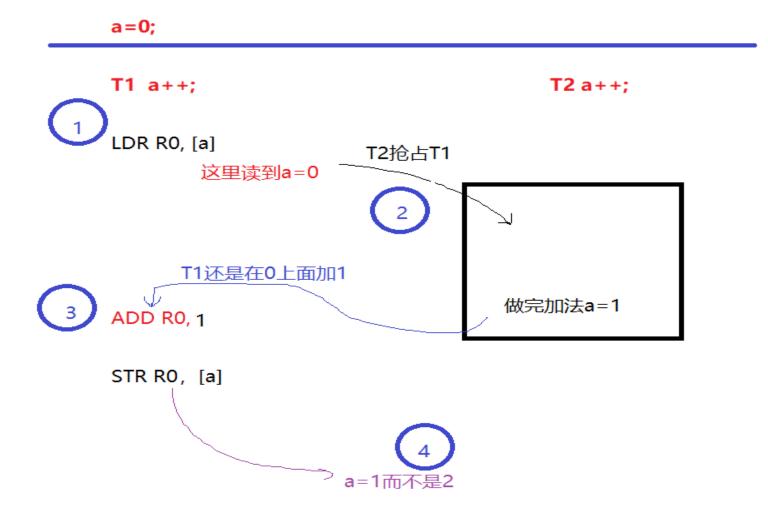
## RMW !!!

```
CSIC处理器,可以直接在内存上面做加法
main()
       int count = 20;
        * objdump:
        * movl $0x14,0x1c(%esp)
        * addl $0x50,0x1c(%esp)
       asm("addl $80, %0 \n"
               : : "m" (count) :);
       printf("%d\n", count);
```

```
RISC必须经过LDR.STR (RMW)
main()
       int count = 20:
        * objdump:
        ldr r3, [fp, #-16]
        mov r4, r3
        add r4, r4, #80
                              ; 0x50
        str r4, [fp, #-16]
        */
   asm(
       "add %0, %0, $80\n"
       : "+r"(count) : :);
       printf("%d\n", count);
```

## 读-修改-写可能引发的问题

■两个线程同时对变量a做加法



#### atomic

■ atomic保证整数操作的原子性

```
void atomic_add(int i, atomic_t *v)
void atomic_sub(int i, atomic_t *v)
void atomic_inc(atomic_t *v)
void atomic_dec(atomic_t *v)
....
```

#### 两个RIMW序列只有1个能成功

```
MOV r1, #0x1 ; load the 'lock taken' value try:

LDREX ro, [LockAddr] ; load the lock value CMP ro, #0 ; is the lock free?

STREXEQ ro, r1, [LockAddr] ; try and claim the lock CMPEQ ro, #0 ; did this succeed?

BNE try ; no – try again ; yes – we have the lock
```

## atomic 不能这么用

■看到一个结构体里面都是int,就用atomic

```
struct
        int a;
        int b;
        int c;
                                         atomic改b
```

## 设想2个学生

- 张三 男
- 李四 女

#### 改姓名和性别都加锁 一定不会出现

- 张四
- 李三

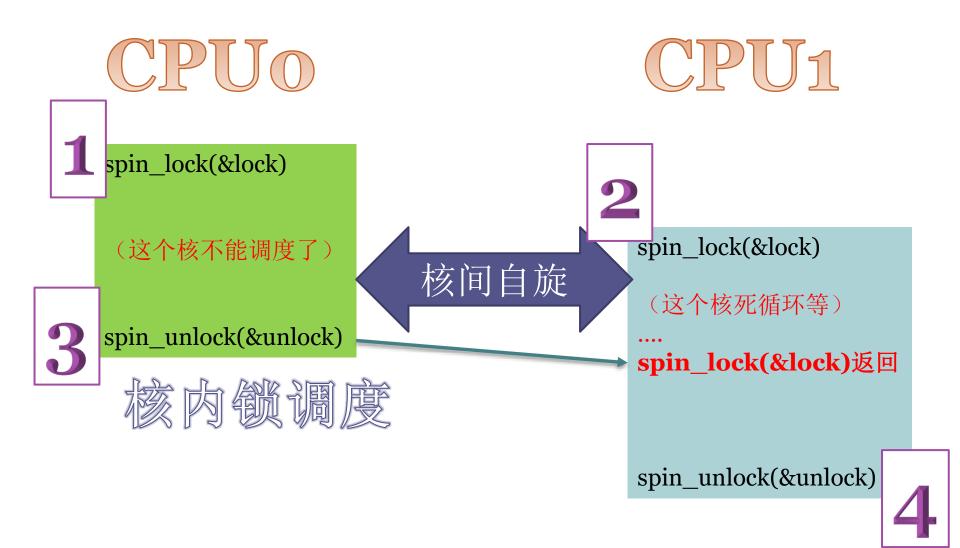
#### 但是,可能出现

- 张三 女
- 李四 男

## 一定要锁住一个语义完整的整体

### spinlock

## spinlock干了什么



## 来了中断怎么办?

## CPUO



(这个核不能调度了)

spin\_unlock(&unlock)



spin\_lock\_irqsave

关本核调度 关本核中断

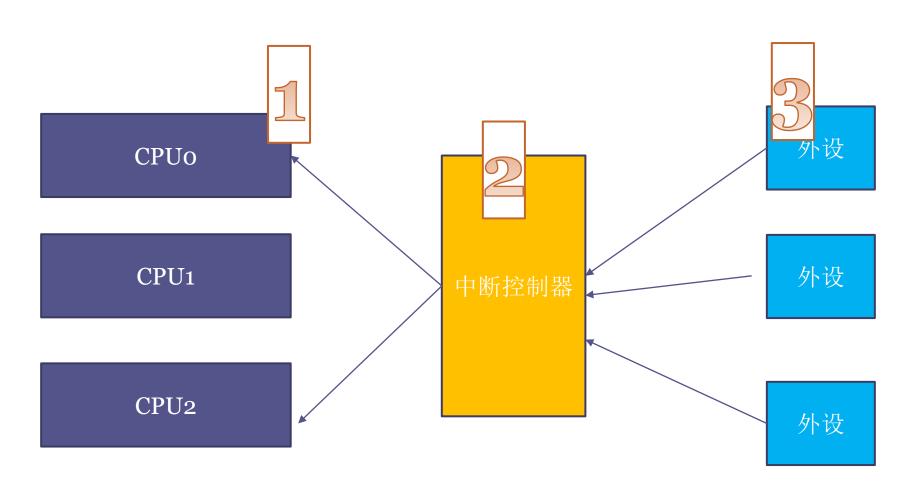
## 到处都有risk

线程与线程 线程与中断 中断与中断 此核与彼核

T1
T2
T4
TRQ0

## local\_irq\_disable/save和irq\_disable

local\_irq\_disable/save: 让本CPU不响应所有中断(指断位置1) irq\_disable: 让某中断不能发给所有CPU(指断位置2)



### 消除全部的risk

#### CPUO

#### CPU1

To

spin\_lock\_irqsave
...
spin\_unlock\_irqrestore

spin\_lock\_irqsave

•••

 $spin\_unlock\_irqrestore$ 

**T3** 

**T1** 

spin\_lock\_irqsave
...
spin\_unlock\_irqrestore

spin\_lock\_irqsave

• • •

spin\_unlock\_irqrestore

**T4** 

IRQo

spin\_lock

...

spin\_unlock

spin\_lock

• • •

spin\_unlock

IRQ<sub>1</sub>

## 区别表

	核内	核间
spin_lock	锁住调度	自旋
local_irq_disable/save	锁住调度	没有意义
spin_lock_irqsave	锁住识断锁住调度	自旋

## 调用local\_irq\_disable 多半是个bug

#### mutex

- T1拿到了mutex,如果T2拿不到,T2睡眠;
- T1释放mutex,T2被唤醒

```
mutex_lock(&lock);
....
(中间可以睡眠)
mutex_unlock(&lock);
```

## 加锁原则

同一把锁语义整体粒度最小

和统自完不

生能

单核也当成多核!

#### Lockup detector

- kernel/watchdog.c
- ✓ NMI中断 + 定时器中断+高优先级RT线程
- ✓ 用定时器中断,检测高优先级线程有无机会执行->soft lockup
- ✓ 用NMI, 检测定时器中断有无机会执行 -> hard lockup

```
Symbol: HARDLOCKUP_DETECTOR [=n]
Type : boolean
   Defined at lib/Kconfig.debug:704
   Depends on: LOCKUP_DETECTOR [=y] && !HAVE_NMI_WATCHDOG [=n] && PERF_EVENTS [=y] &&

Symbol: LOCKUP_DETECTOR [=y]
Type : boolean
Prompt: Detect Hard and Soft Lockups
   Location:
    -> Kernel hacking
(3)   -> Debug Lockups and Hangs
   Defined at lib/Kconfig.debug:680
   Depends on: DEBUG_KERNEL [=y] && !S390
```

# Lockup detector案例

# 100.291611] NMI watchdog: BUG: soft lockup - CPU#0 stp for 22s! [cat:716]

```
100.292121] Modules linked in: globalmem
100.292924] CPU: 0 PID: 716 Comm: cat Tainted: G
                                                            L 4.0.0-rc1+ #47
100.293417] Hardware name: ARM-Versatile Express
100.293784] task: 9f7cdf00 ti: 9ed32000 task.ti: 9ed32000
100.294172] PC is at loop delay+0x0/0x10
100.294499] LR is at globalmem read+0x48/0x114 [globalmem]
                                                     psr: 20000013
100.294907 pc : [<8023dc38>]
                                lr : [<7f0001d0>]
100.294907] sp : 9ed33f28 ip : 8023dc08 fp : 00000000
100.295607] r10: 7ee15fa0 r9 : 00001000 r8 : 00001000
100.295959] r7 : 9ecda000 r6 : 9ed33f80 r5 : 80659a38 r4 : 00002136
100.296375] r3 : 00000000 r2 : 00000e92 r1 : ffffffff r0 : 0000e856
100.296936] Flags: nzCv IRQs on FIQs on Mode SVC 32 ISA ARM Segment user
100.297397] Control: 10c5387d Table: 7ed7806a DAC: 00000015
100.297865] CPU: 0 PID: 716 Comm: cat Tainted: G
                                                            L 4.0.0-rc1+ #47
100.298301] Hardware name: ARM-Versatile Express
100.298667] [<80015790>] (unwind backtrace) from [<80011a10>] (show stack+0x10/0x14)
100.299162] [<80011a10>] (show stack) from [<804848e4>] (dump stack+0x74/0x90)
100.299652] [<804848e4>] (dump stack) from [<8008757c>] (watchdog timer fn+0x1a0/0x214)
100.300156] [<8008757c>] (watchdog timer fn) from [<80065d04>] ( run hrtimer.isra.19+0x54
100.300731] [<80065d04>] ( run hrtimer.isra.19) from [<80065fa8>] (hrtimer interrupt+0xd{
100.301440] [<80065fa8>] (hrtimer interrupt) from [<8001459c>] (twd handler+0x2c/0x40)
100.301964] [<8001459c>] (twd handler) from [<8005a6ac>] (handle percpu devid irq+0x68/0x8
100.302494] [<8005a6ac>] (handle percpu devid irq) from [<80056cd8>] (generic handle irq+6
100.303238] [<80056cd8>] (generic handle irg) from [<80056dd4>] ( handle domain irg+0x54/
100.304247] [<80056dd4>] ( handle domain irq) from [<80008670>] (gic handle irq+0x20/0x50
100.305211] [<80008670>] (gic handle irq) from [<80012500>] ( irq svc+0x40/0x54)
100.305725] Exception stack(0x9ed33ee0 to 0x9ed33f28)
```

### 更早课程

- 《Linux总线、设备、驱动模型》录播: <a href="http://edu.csdn.net/course/detail/5329">http://edu.csdn.net/course/detail/5329</a>
- 深入探究Linux的设备树 http://edu.csdn.net/course/detail/5627
- Linux进程、线程和调度 http://edu.csdn.net/course/detail/5995
- C语言大型软件设计的面向对象 https://edu.csdn.net/course/detail/6496

# 谢谢!