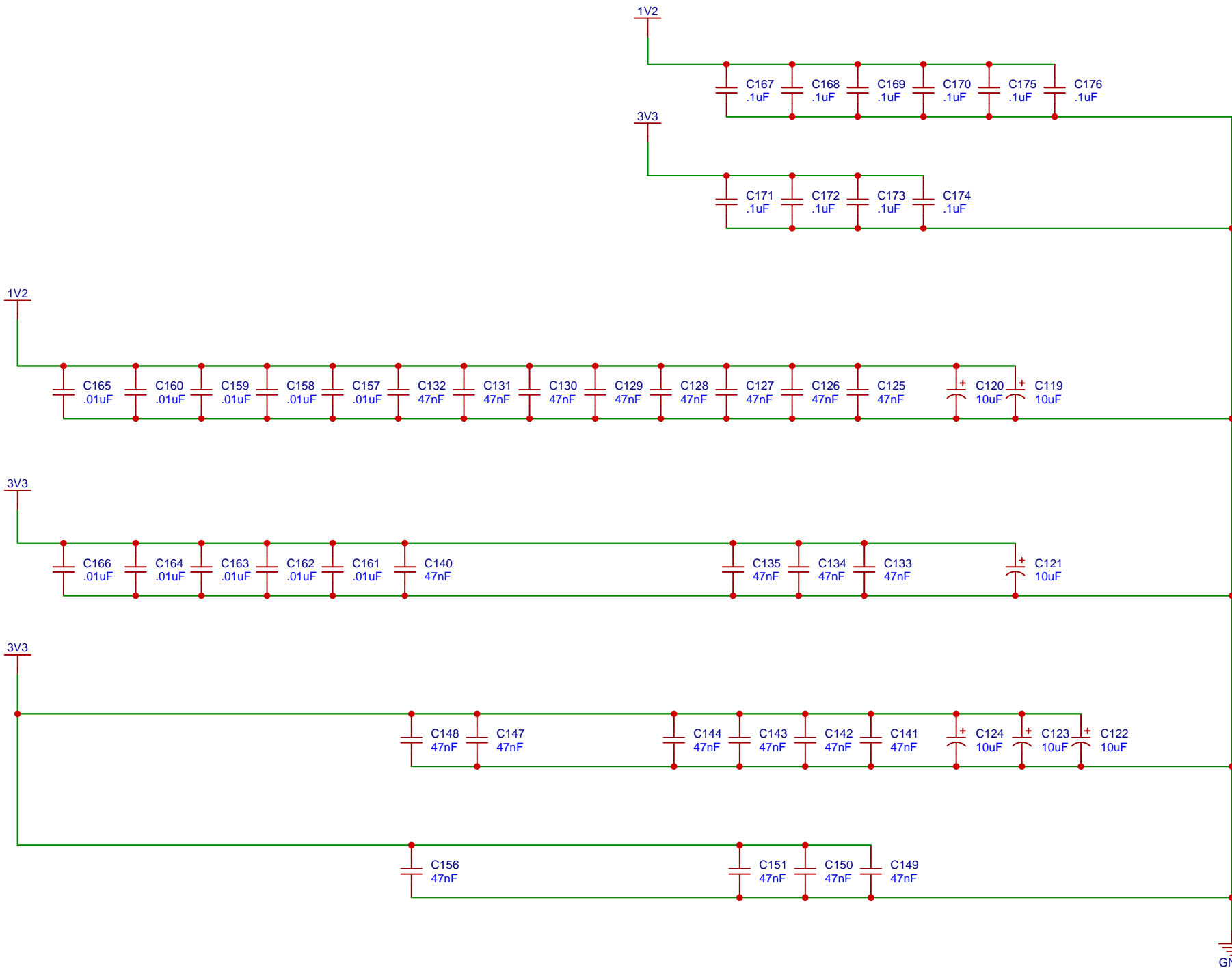
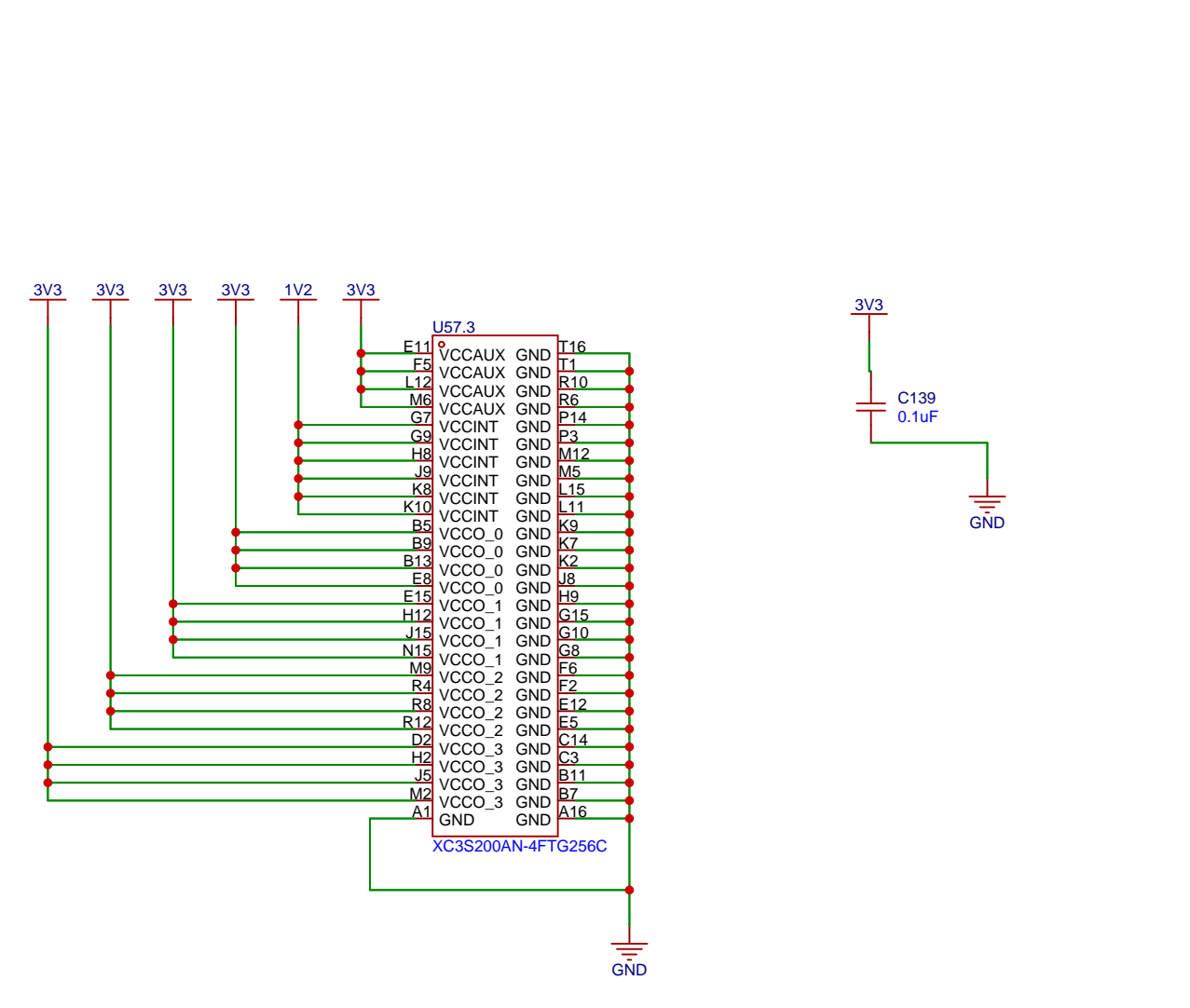
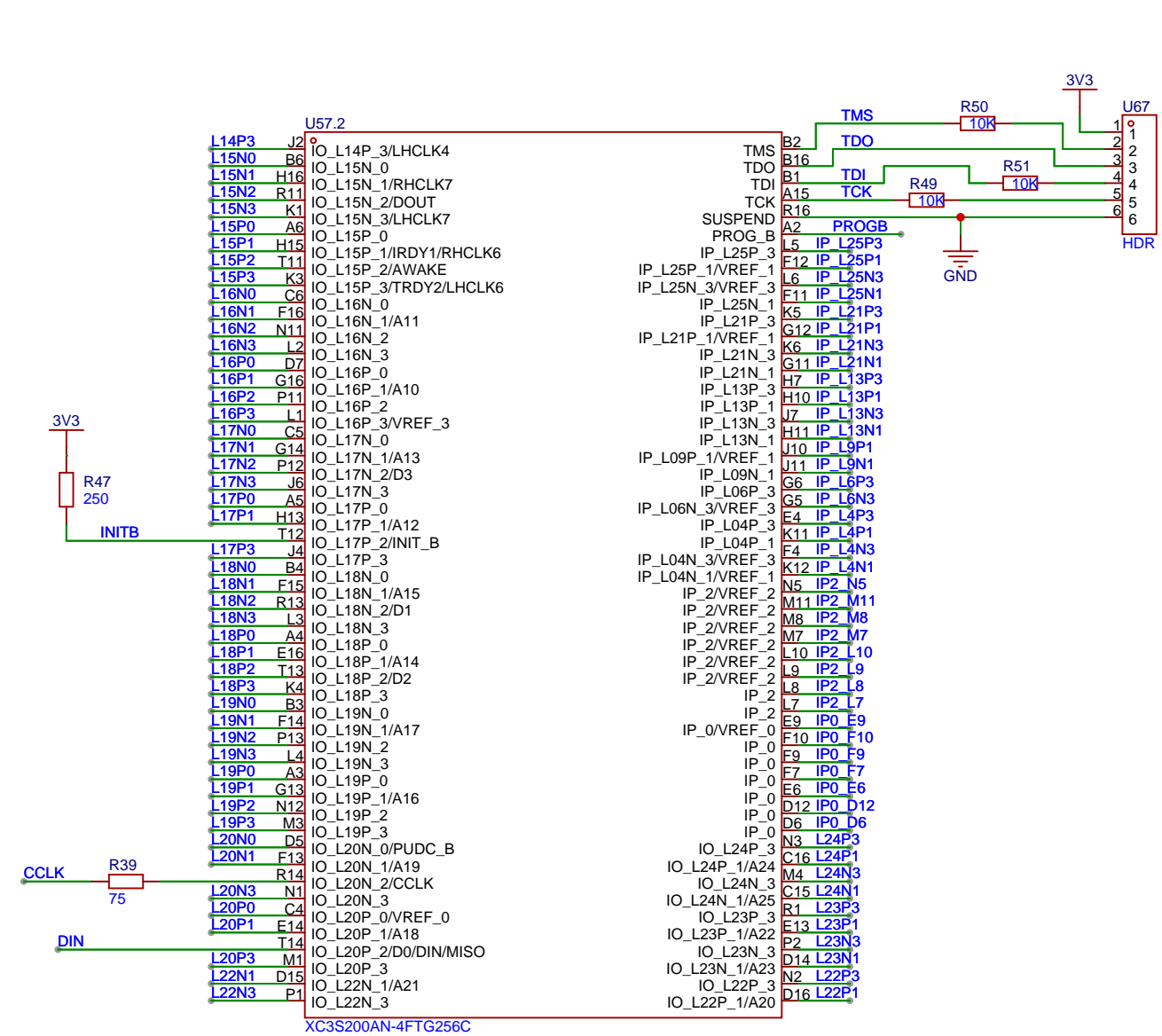
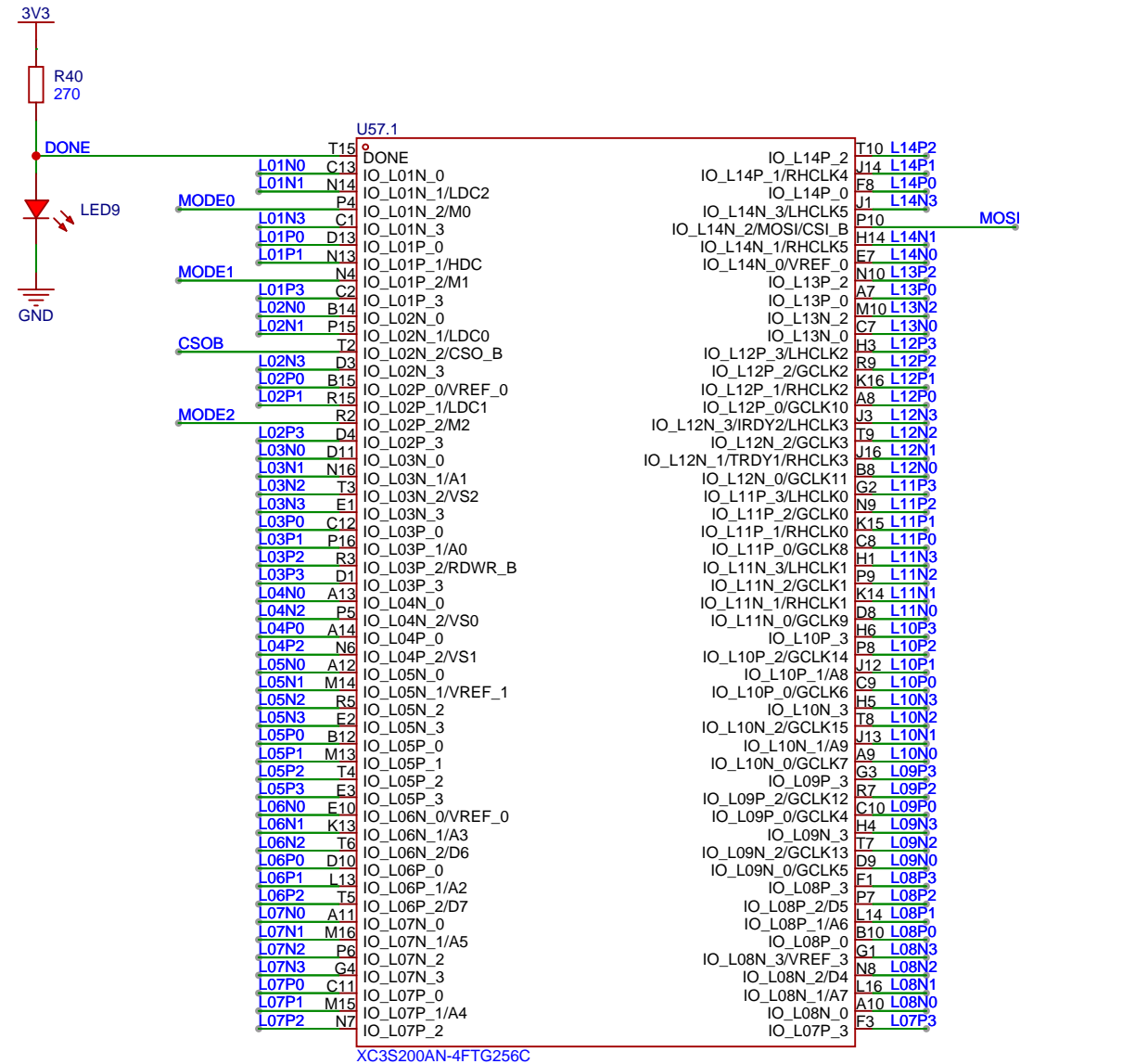
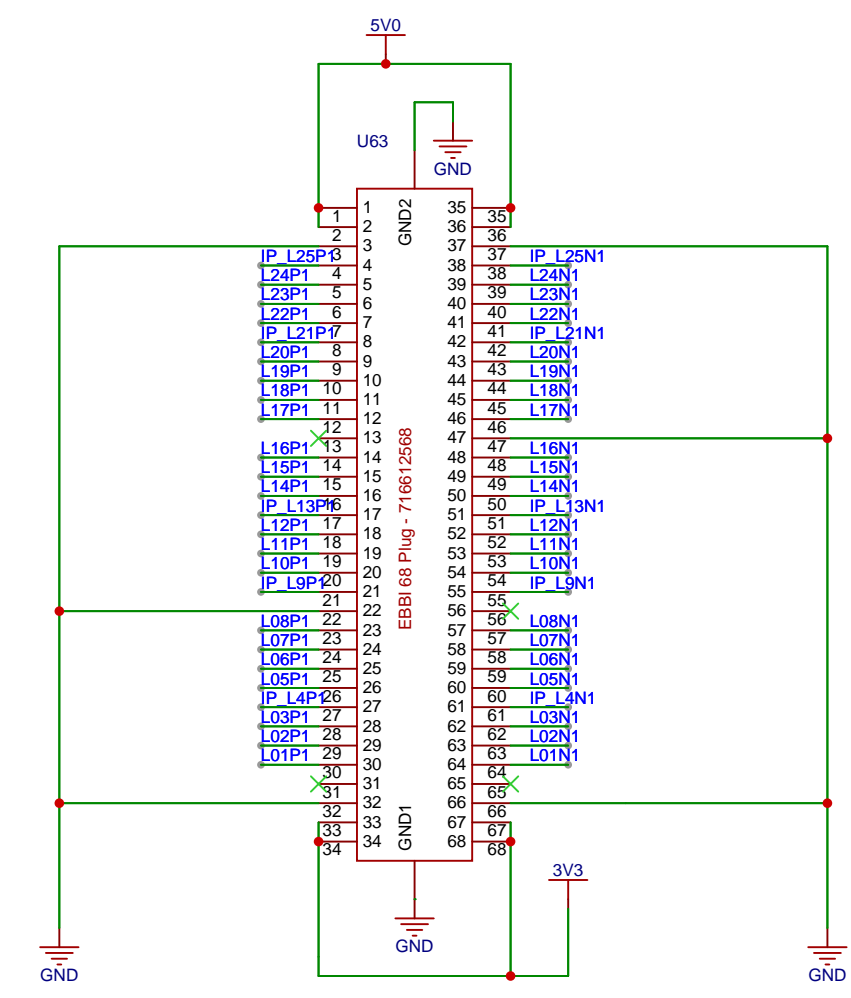
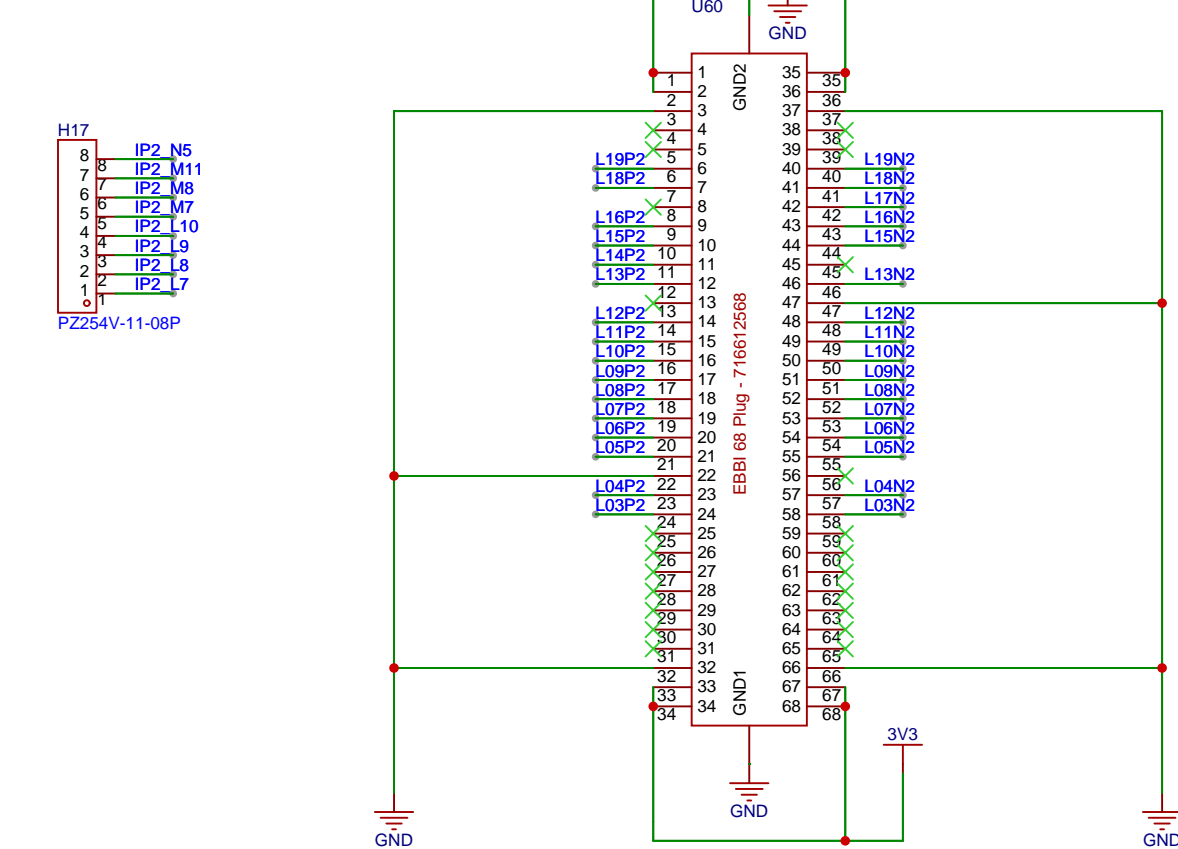
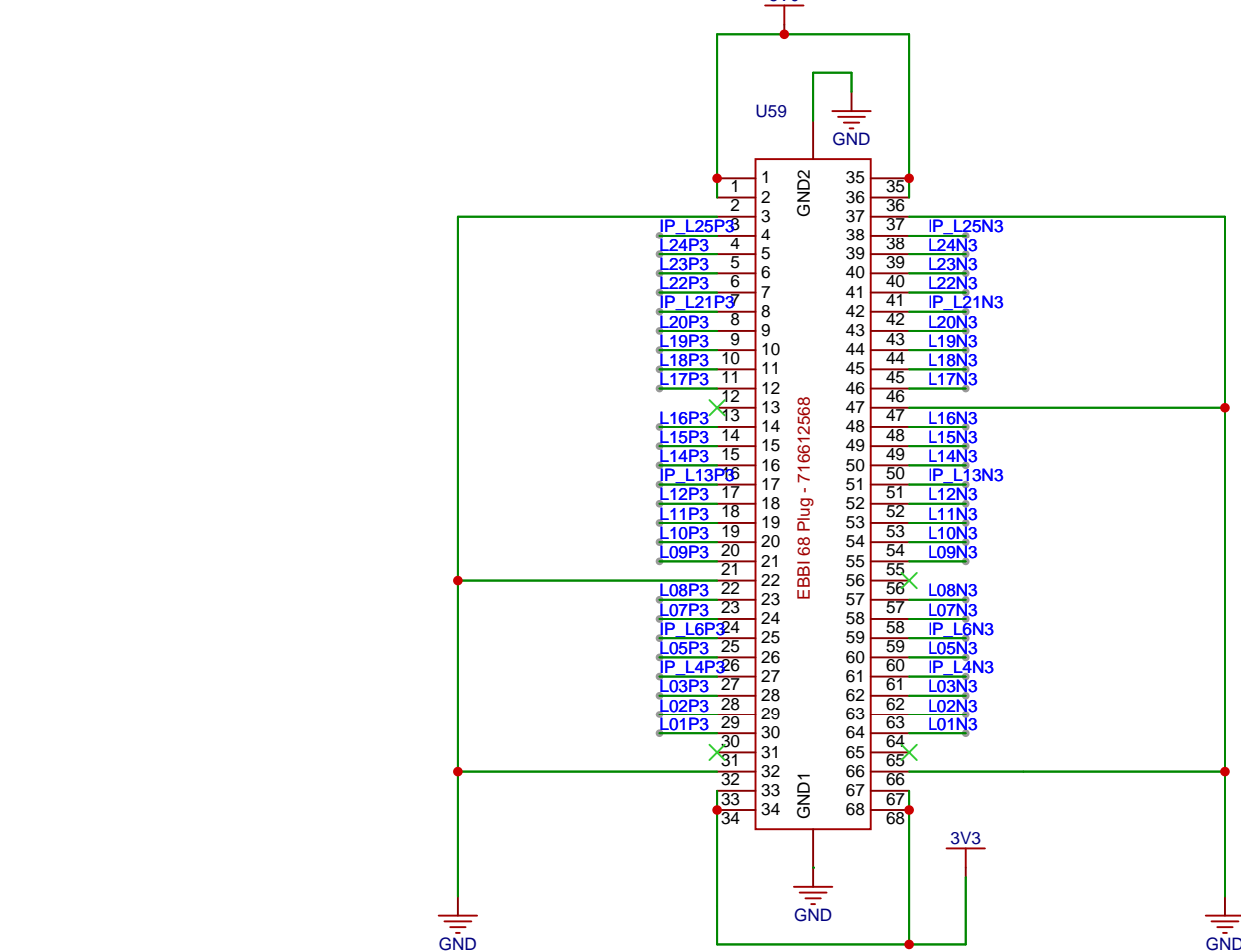
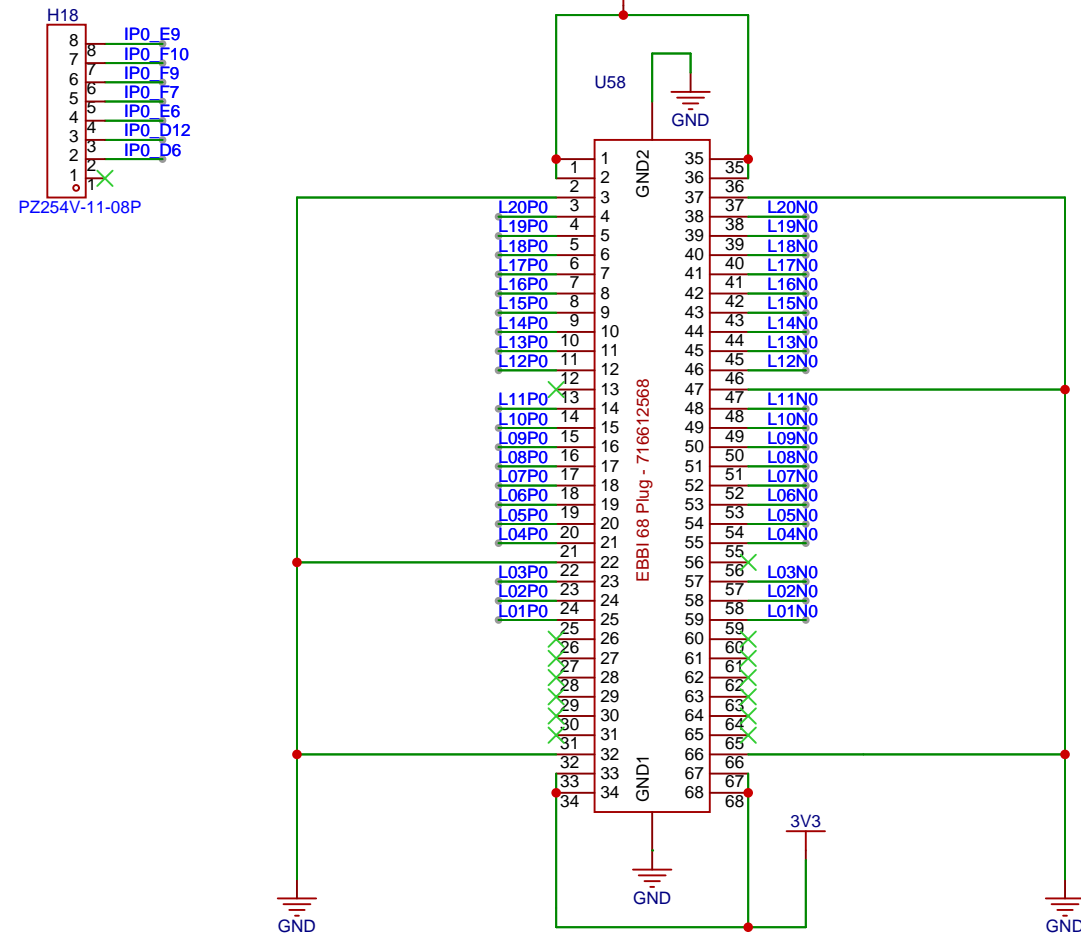


To do:  
-Length matching  
-Add CH340, TVS, fuse  
-Reset SW



UNTESTED!



|           |   |             |             |
|-----------|---|-------------|-------------|
| Schematic | Schematic1                                  | Update Date | 2024-03-20  |
| Page      | P1  | Create Date | 2024-03-17  |
| Drawn     | rehnd                                       | Part Number | JLCPCB-002  |
| Reviewed  |   |             |             |
|           | FPGA Dev Board - XILINIX XC3S200AN-4FTG256C |             |             |
|           | VER   | SIZE        | PAGE 1 OF 1 |
|           | V0.1  | A4          | EasyEDA.com |