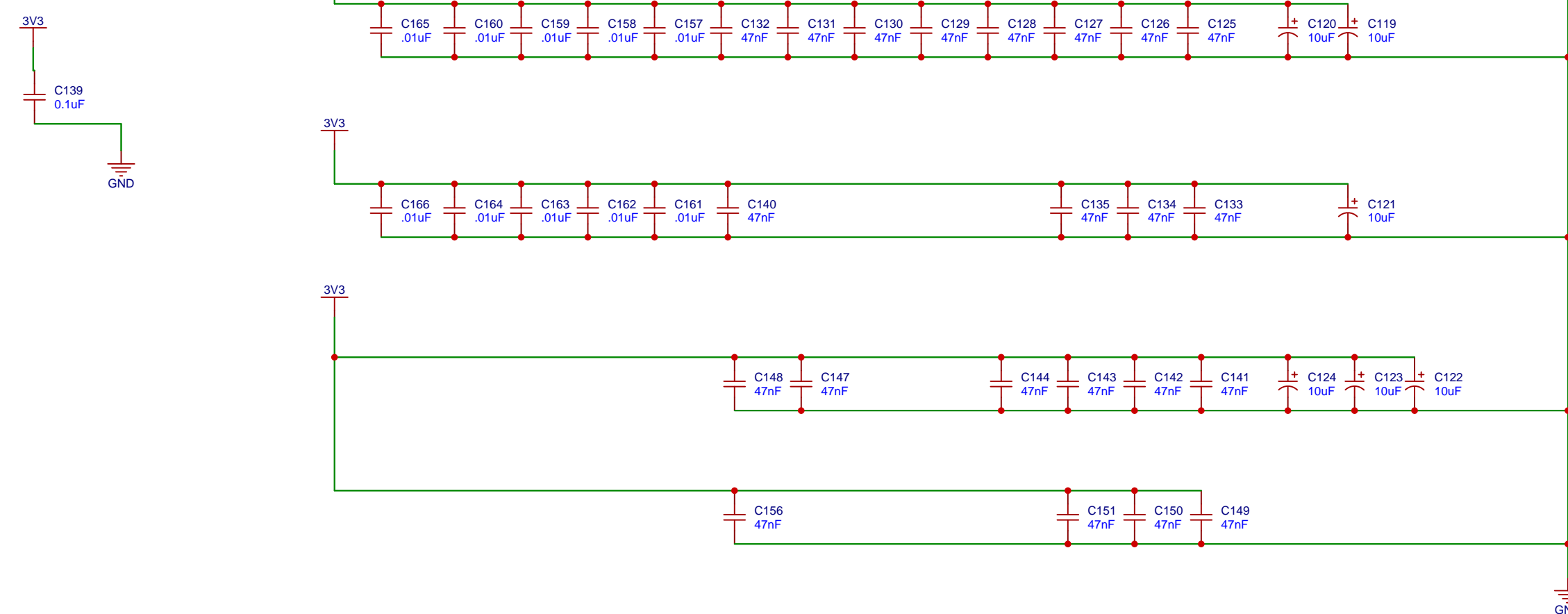
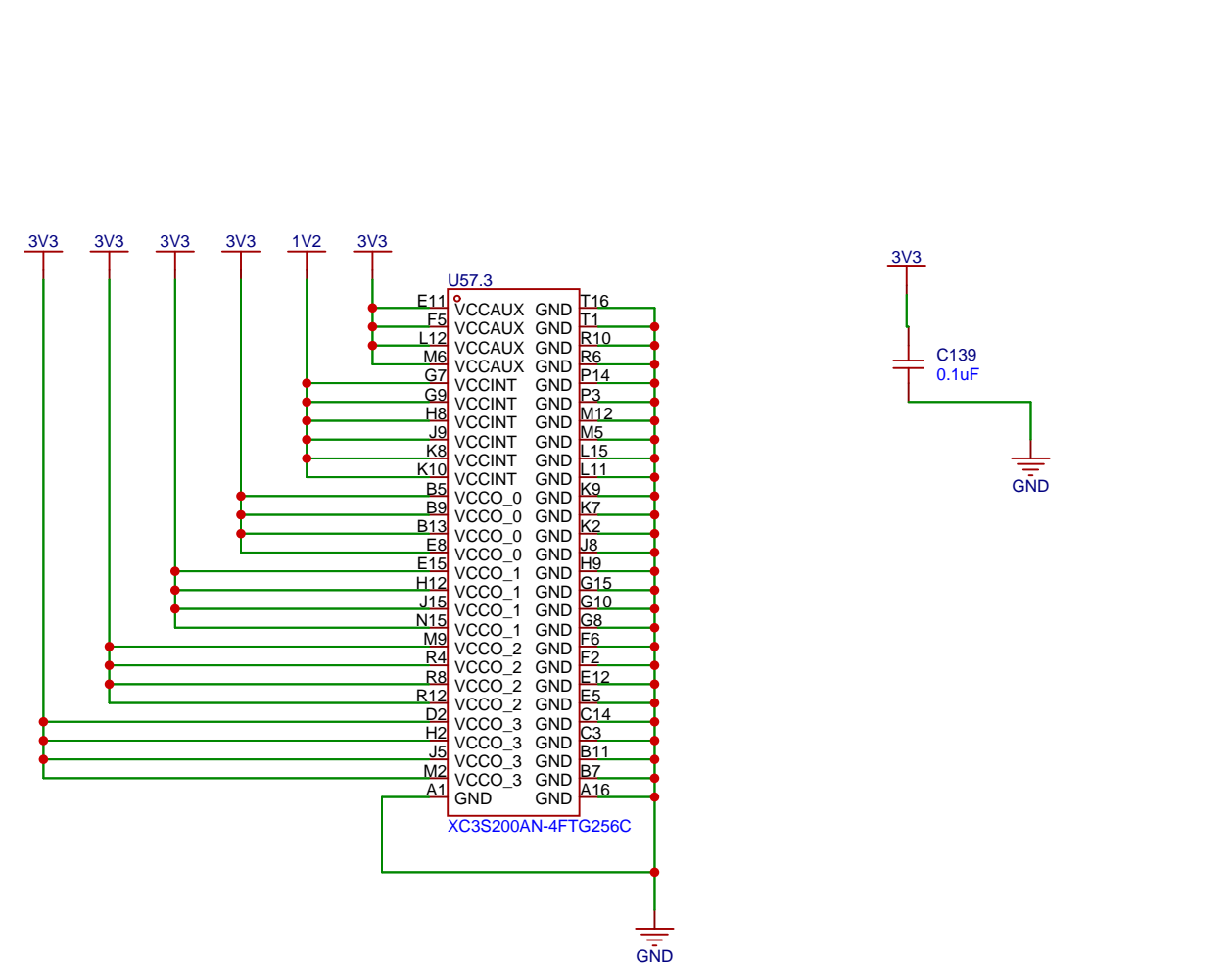
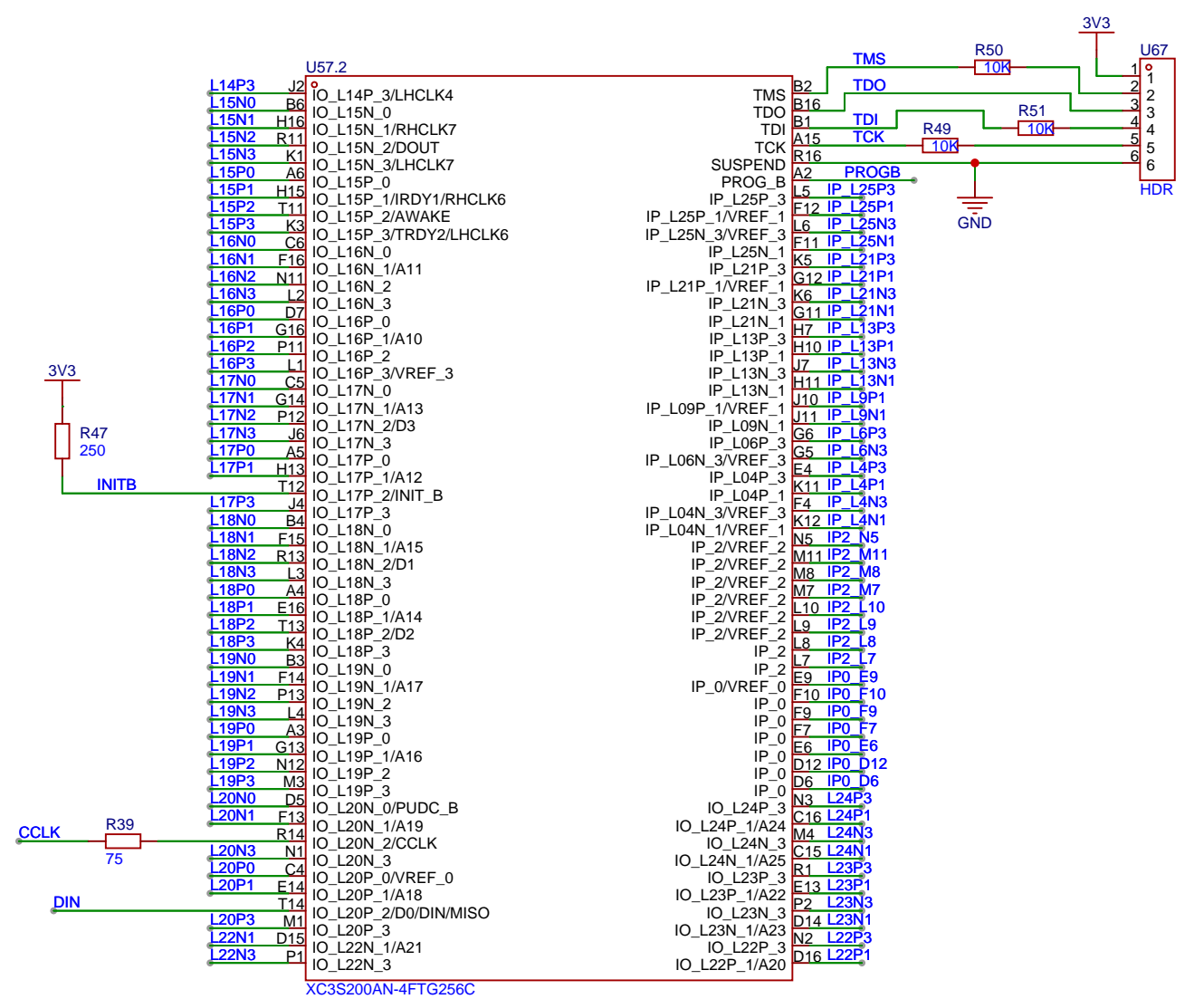
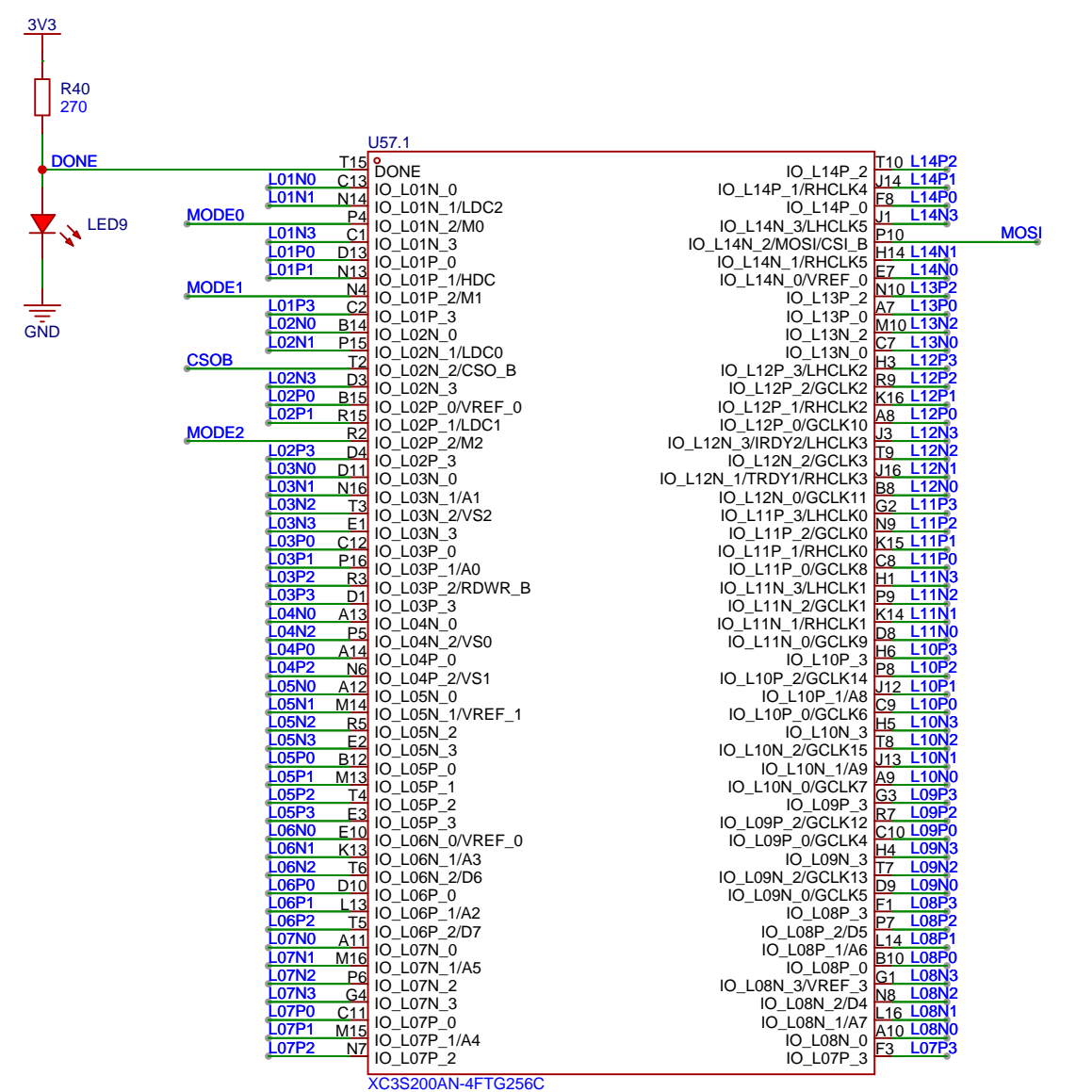
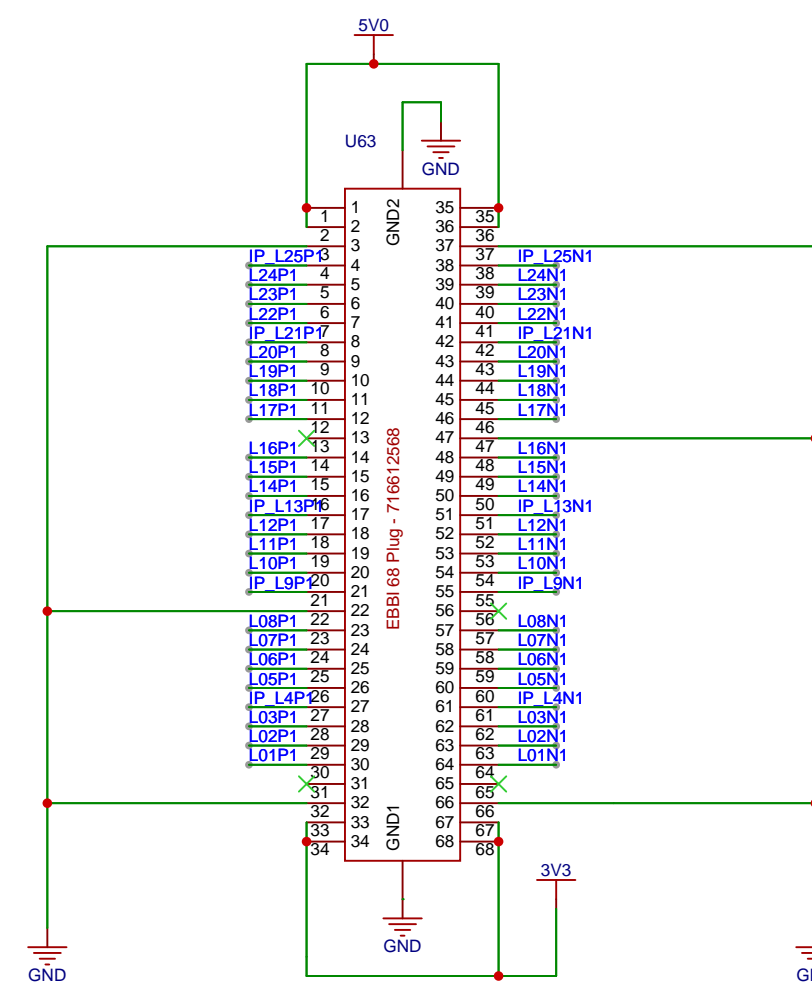
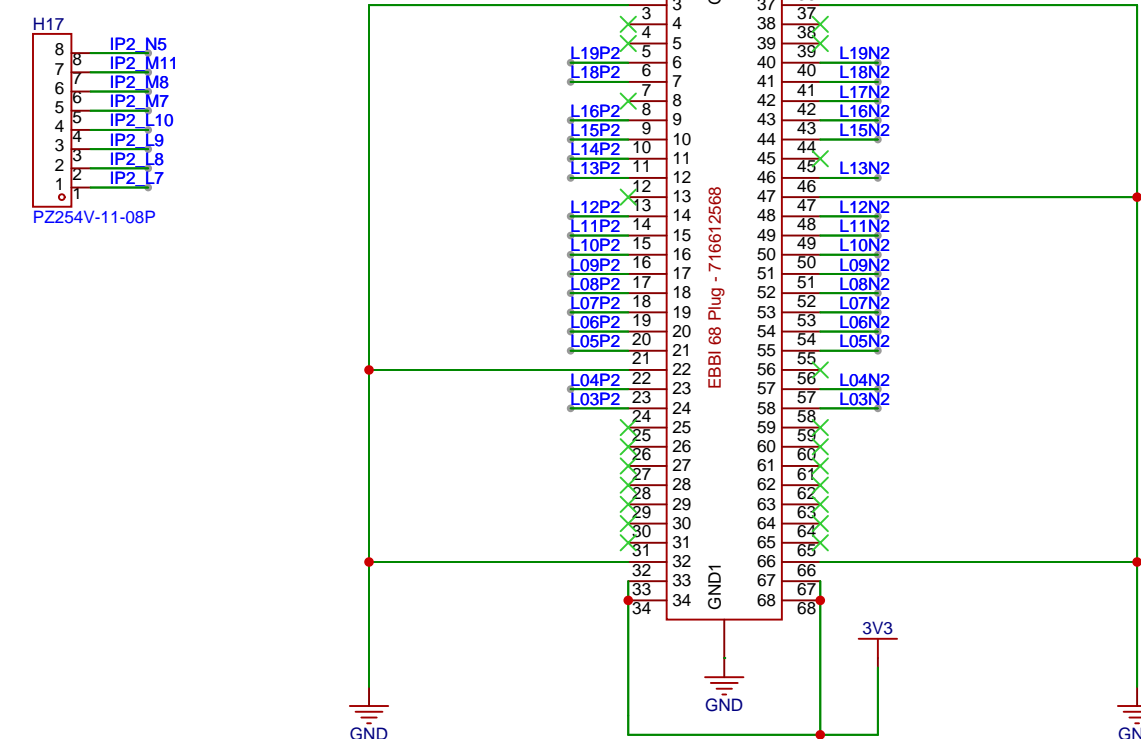
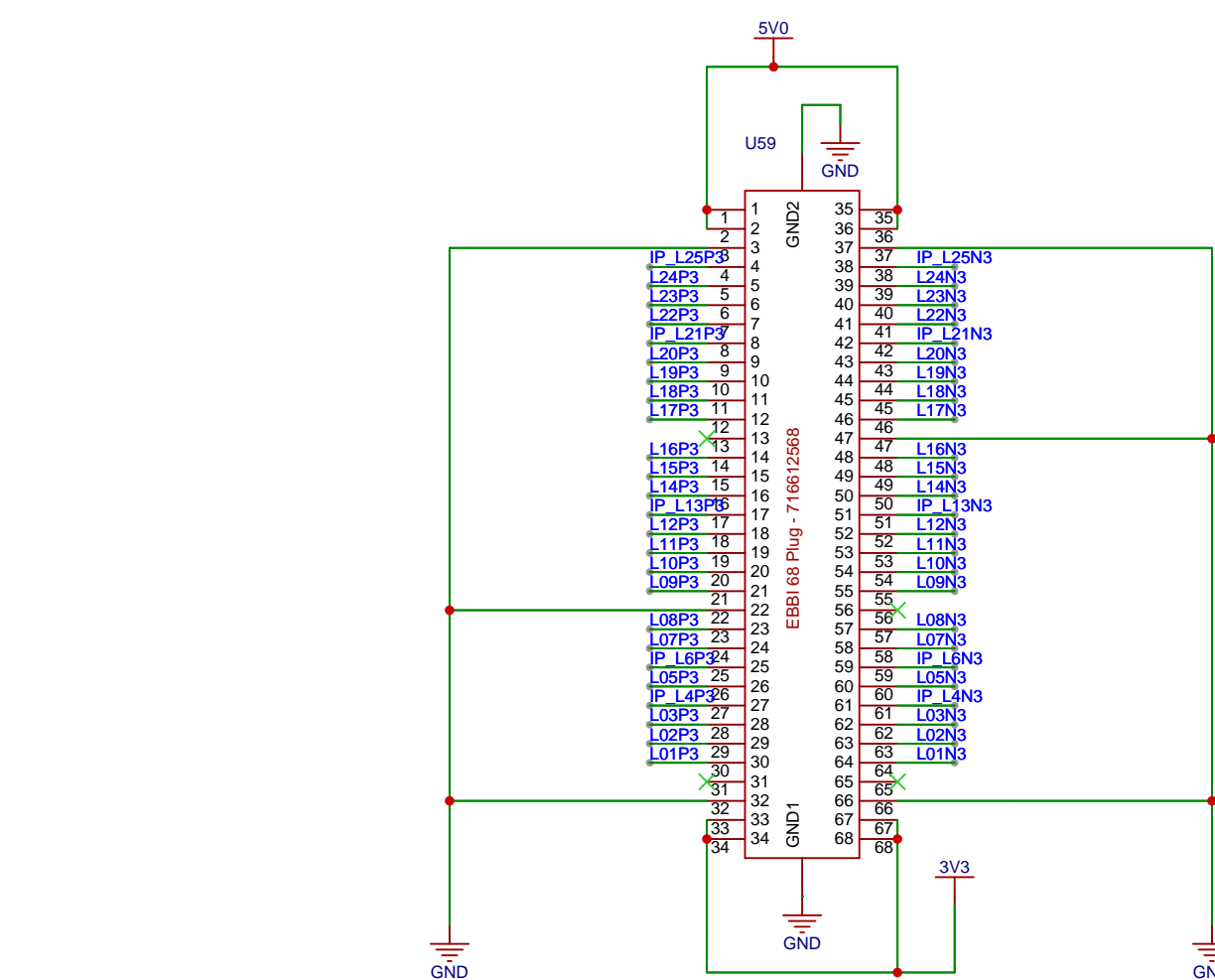
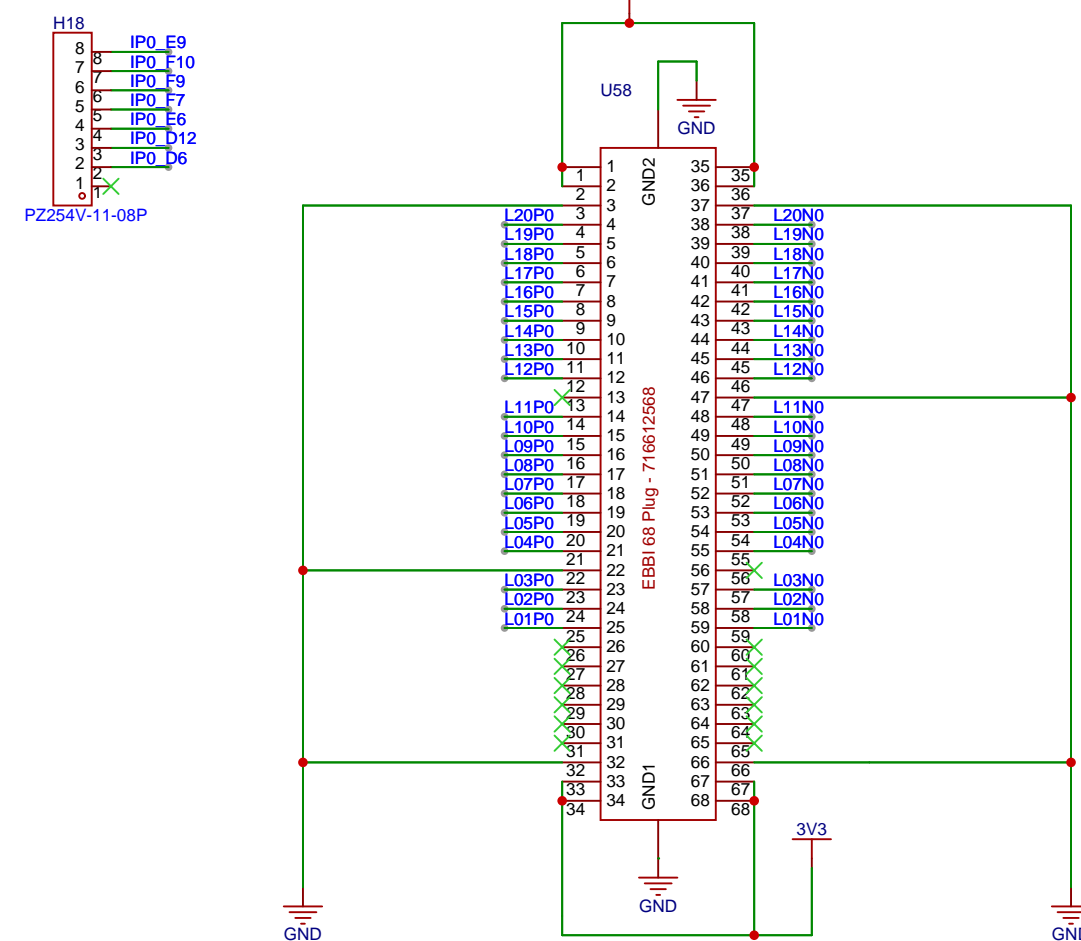



To do:

- Length matching
- Add CH340



UNTESTED!



Schematic	Schematic1			Update Date	2024-03-20
Page	P1			Create Date	2024-03-17
Drawn	rehsd	Part Number			JLPCPB-002
Reviewed	FPGA Dev Board - XILINIX XC35200AN-4FTG256C				
		VER	SIZE	PAGE	1 OF 1
 EasyEDA		V0.1	A4	EasyEDA.com	