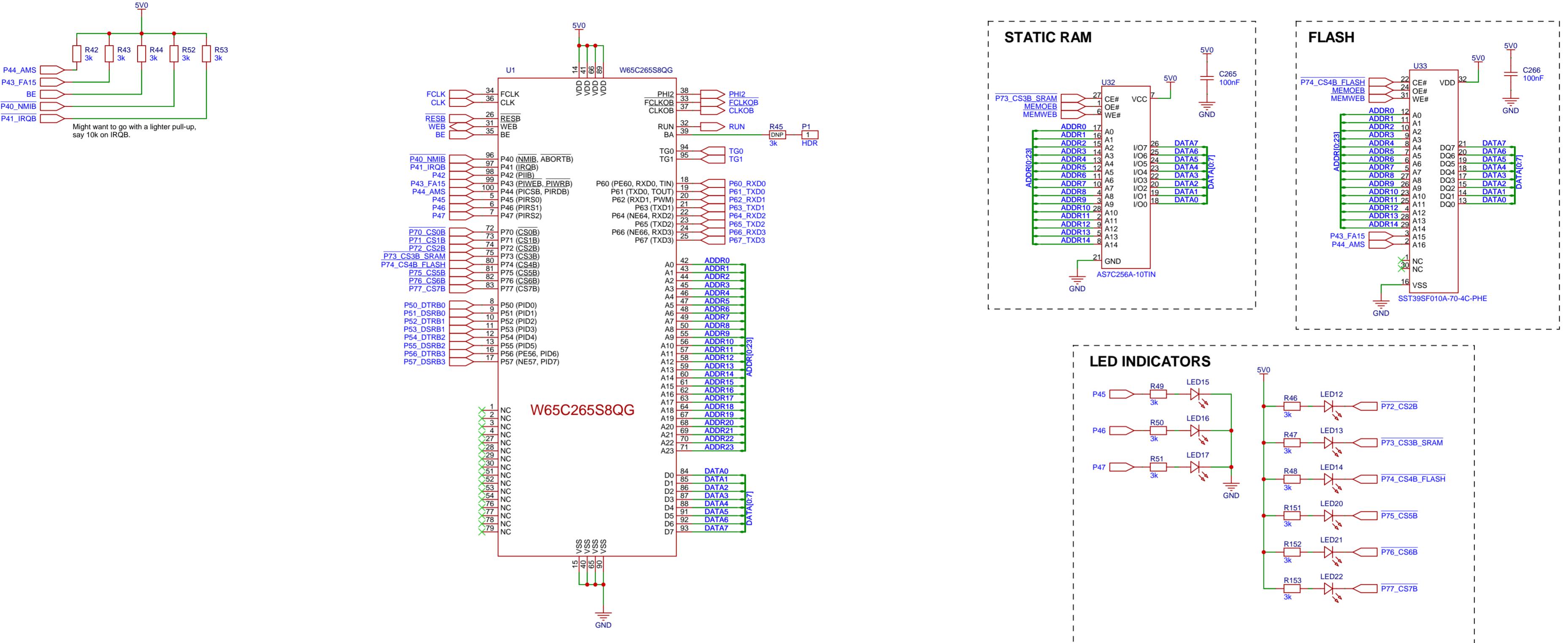


# MCU

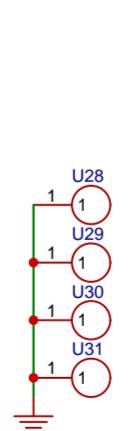


To do:

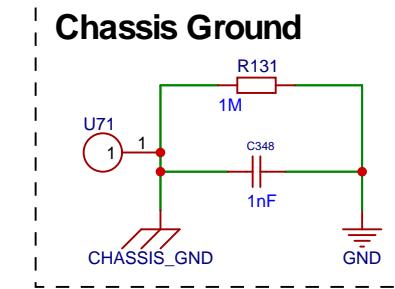
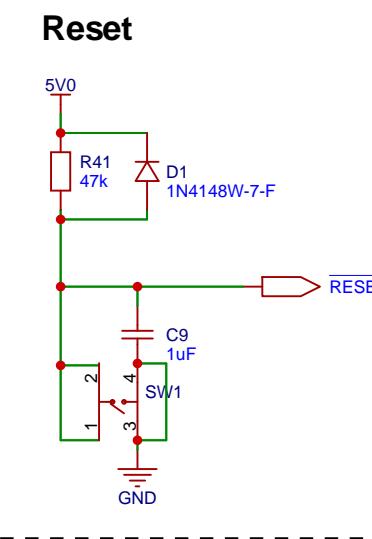
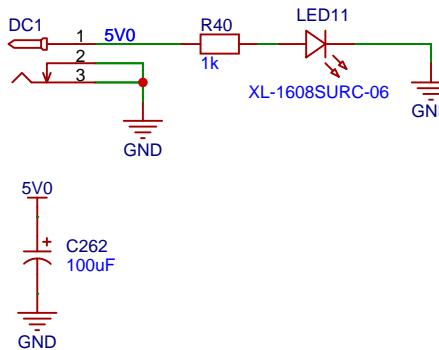
- Indicators for serial RX/TX
- Replace DIP ROM with SMD
- Onboard ROM programming
- power switch
- Reset delay circuit (board-wide)?
- LED indicators for all enable lines

Schematic	Schematic1		Create at	2025-12-04
Board	Board1		Update at	2025-12-19
Drawn			Page	MCU
W65C265S v0.04				
Reviewed			Version	Size
			Page 1 Total 8	
			V1.0	A4
			EasyEDA.com	

Place100nF cap near U1.41 and U1.89  
Place10nF cap near U1.14 and U1.66

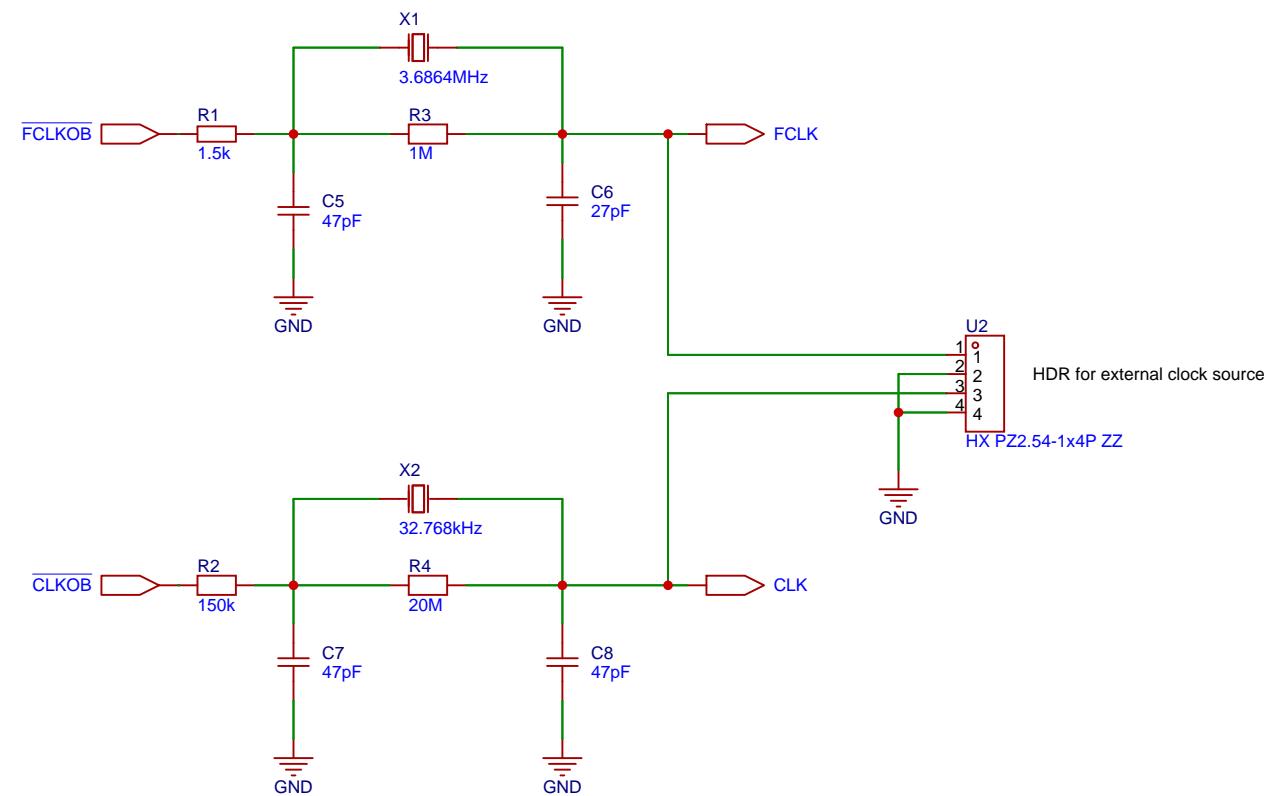


# POWER



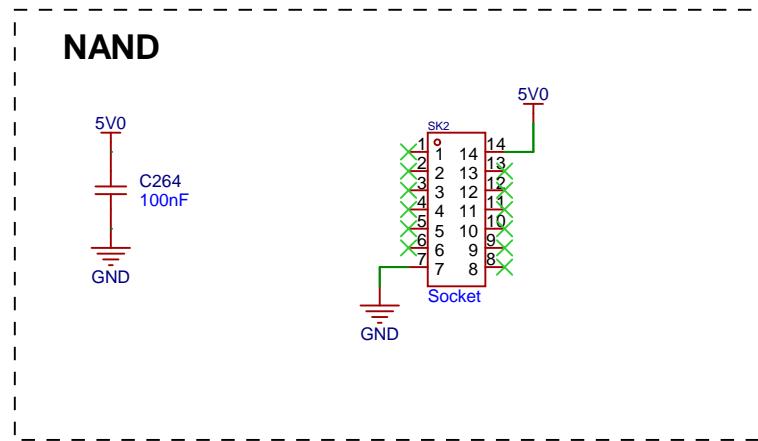
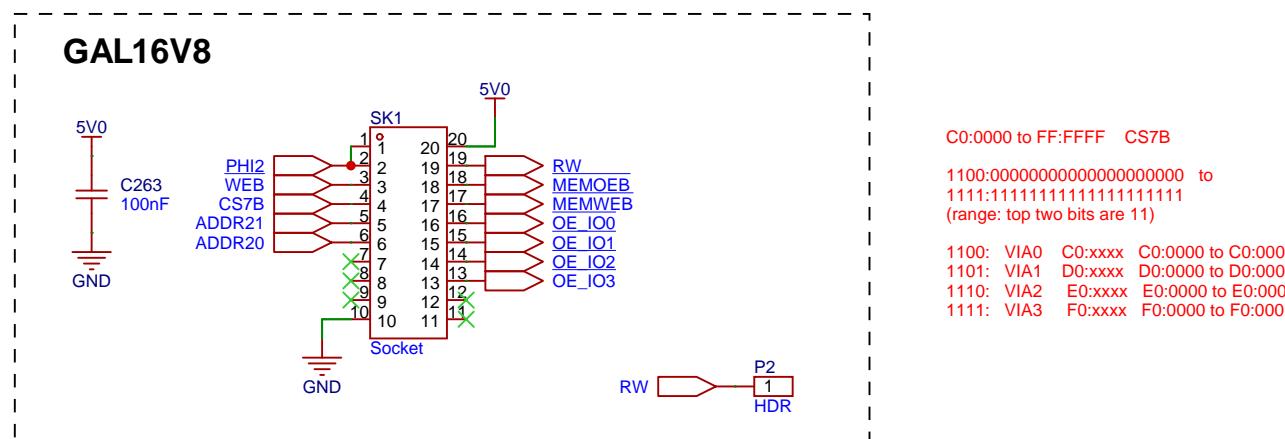
Schematic	Schematic1			Create at	2025-12-04
Board	Board1			Update at	2025-12-04
Drawn				Page	Power
Reviewed				W65C265S v0.04	
	Version	Size	Page 2 Total 8		
<b>EasyEDA</b>		V1.0	A4	EasyEDA.com	

# CLOCKS



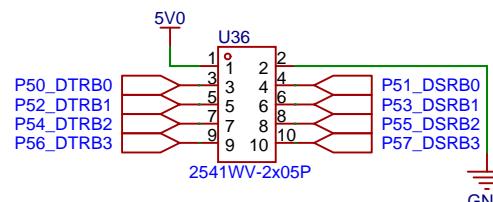
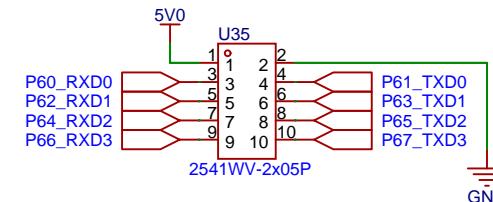
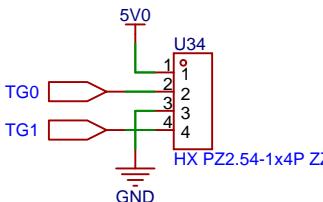
Schematic	Schematic1		Create at	2025-12-04
Board	Board1		Update at	2025-12-04
Drawn			Page	Clocks
Reviewed			W65C265S v0.04	
	Version	Size	Page 3 Total 8	
		V1.0	A4	EasyEDA.com

# DECODE

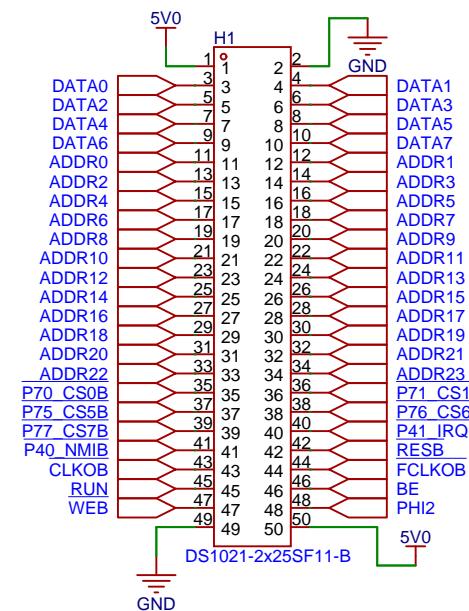
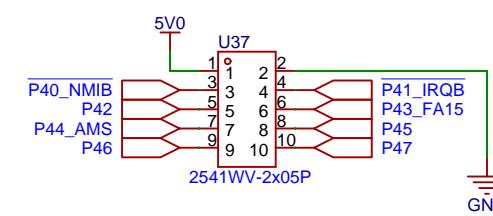


Schematic	Schematic1	Create at	2025-12-04
		Update at	2025-12-18
Board	Board1	Page	Decode
Drawn			
Reviewed		W65C265S v0.04	
		Version	Size
		Page 4 Total 8	
 EasyEDA		V1.0	A4
		EasyEDA.com	

# EXPANSION

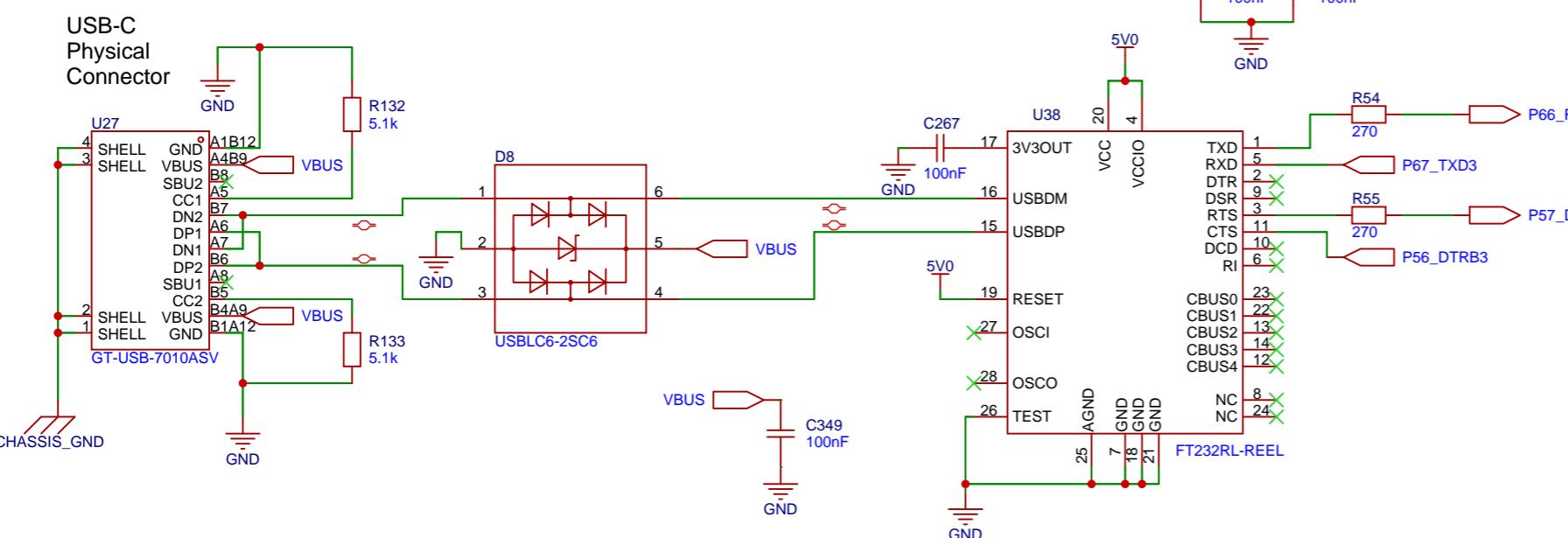


Used:

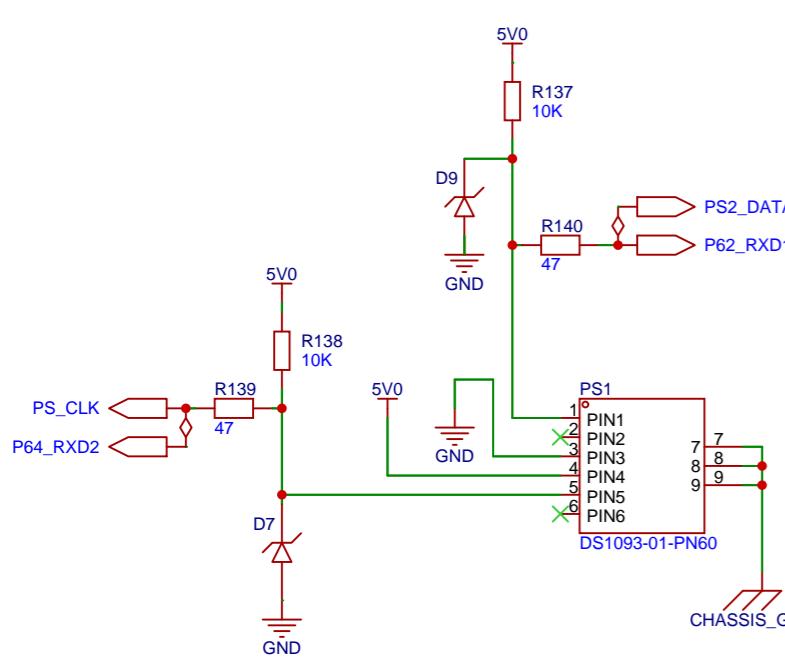
P50:56 - 1602 LCD  
P51 - AvailableP62 - PS2KBD DATA  
P64 - PS2KBD CLK  
P66 - RXD3  
P67 - TXD3

Schematic	Schematic1			Create at	2025-12-04
Board	Board1			Update at	2025-12-17
Drawn				Page	Expansion
Reviewed				W65C265S v0.04	
	Version	Size	Page 5 Total 8		
 EasyEDA		V1.0	A4	EasyEDA.com	

# USB SERIAL



# PS2 Keyboard

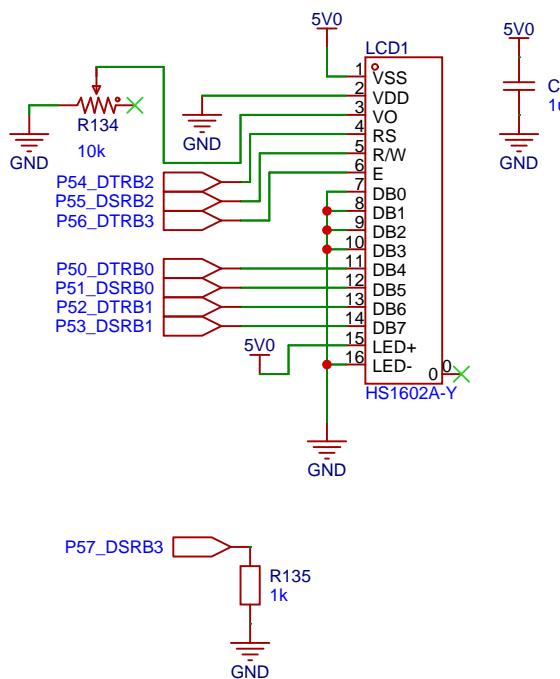


Auto-reset for keyboards that require credit to u/gfoot360 (George Foot)

Temporary: unconnected  
for inputs to be grounded

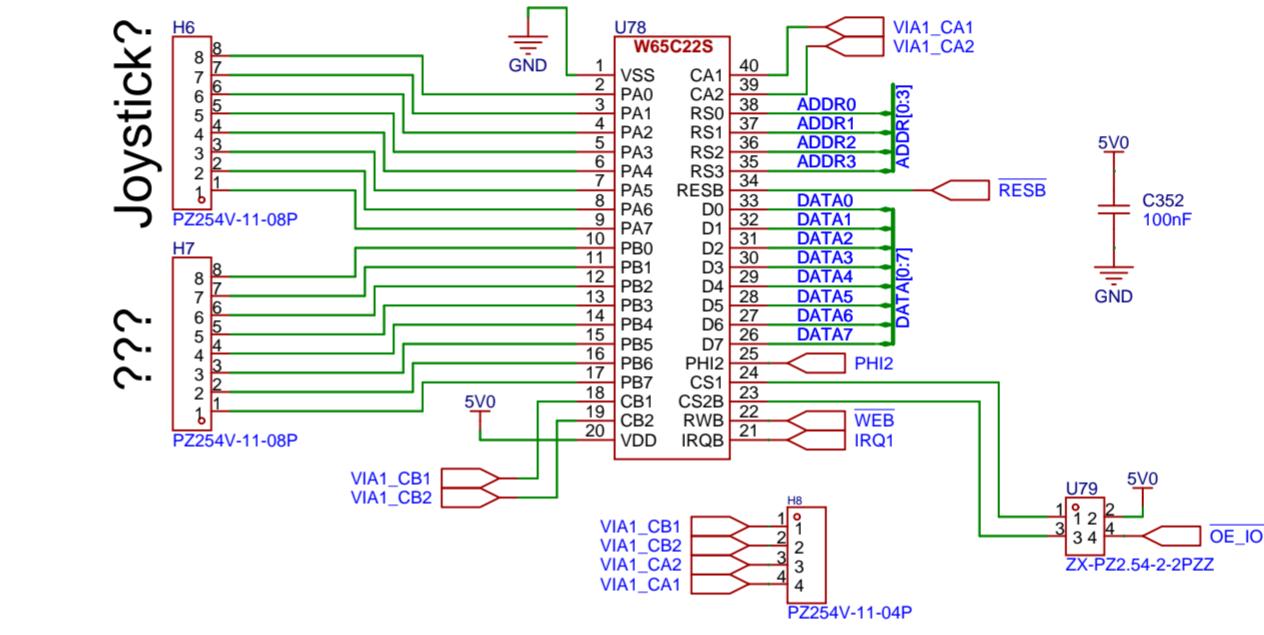
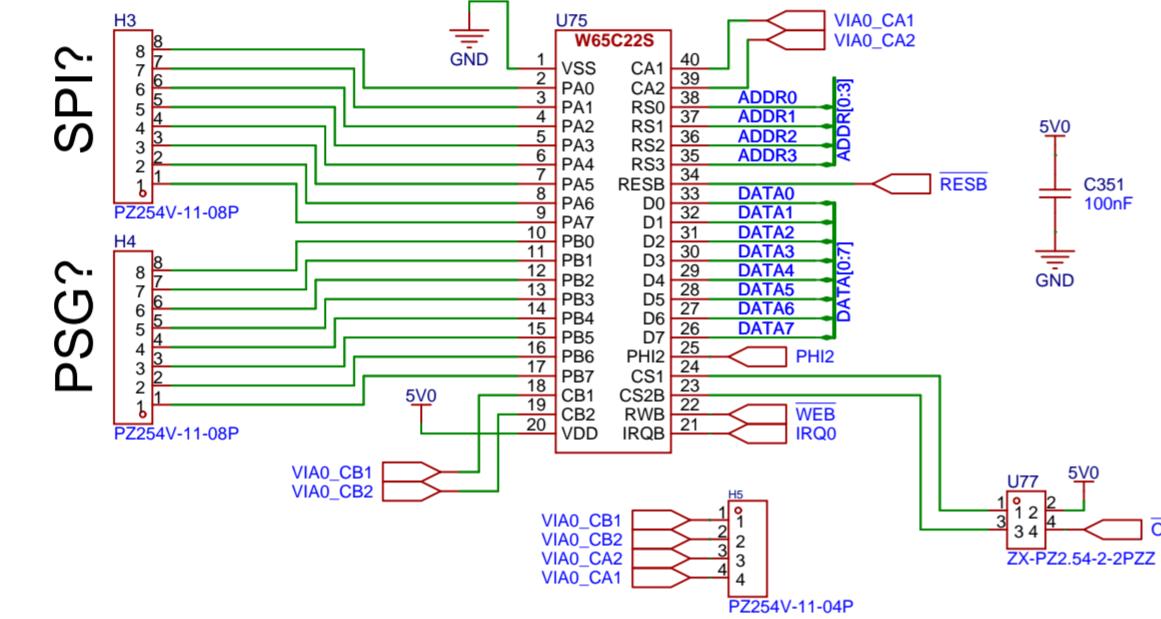
Schematic	Schematic1		Create at	2025-12-04
			Update at	2025-12-17
Board	Board1		Page	Serial & PS2
Drawn			W65C265S v0.04	
Reviewed				
			Page 6 Total 8	
 EasyEDA		V1.0	A4	EasyEDA.com

# LCD 1602



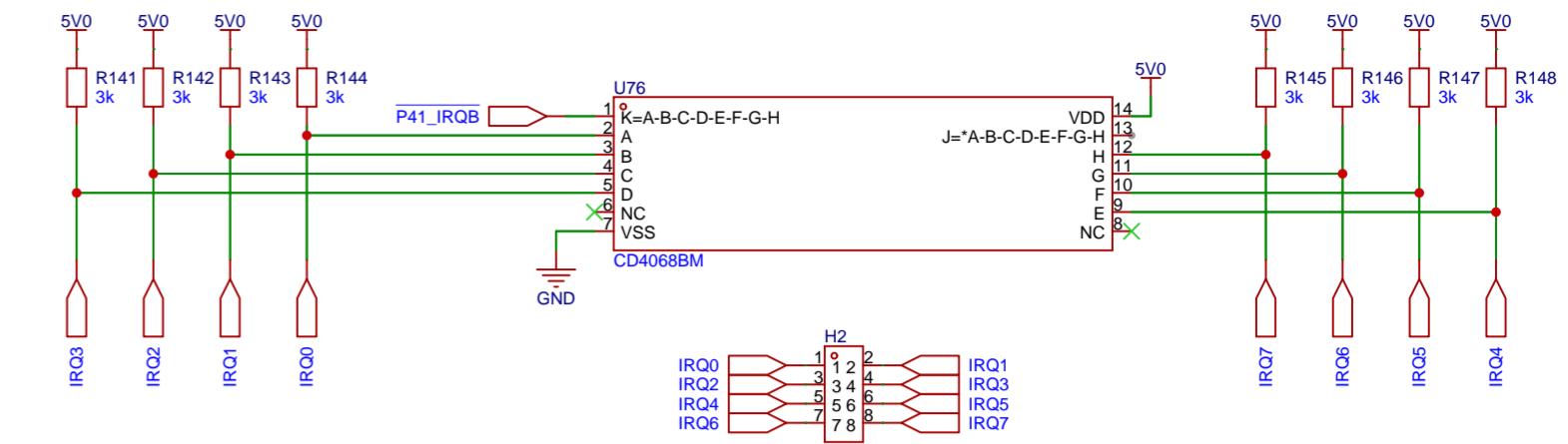
Schematic	Schematic1		Create at	2025-12-06
			Update at	2025-12-17
Board	Board1		Page	LCD_1602
Drawn				
Reviewed				
			Version	W65C265S v0.04
			Size	Page 7 Total 8
 EasyEDA		V1.0	A4	EasyEDA.com

# VIAx



C0:0000 to FF:FFFF CS7B  
1100:00000000000000000000000000000000 to  
1111:11111111111111111111111111111111  
(range: top two bits are 11)

1100: VIA0 C0:xxxx C0:0000 to C0:000F  
1101: VIA1 D0:xxxx D0:0000 to D0:000F  
1110: VIA2 E0:xxxx E0:0000 to E0:000F  
1111: VIA3 F0:xxxx F0:0000 to F0:000F



Schematic	Schematic1			Create at	2025-12-17	
Board	Board1			Update at	2025-12-19	
Drawn	Reviewed				Page	VIAx
W65C265S v0.04						
Version	Size	Page 8 Total 8				
V1.0	A4	EasyEDA.com				