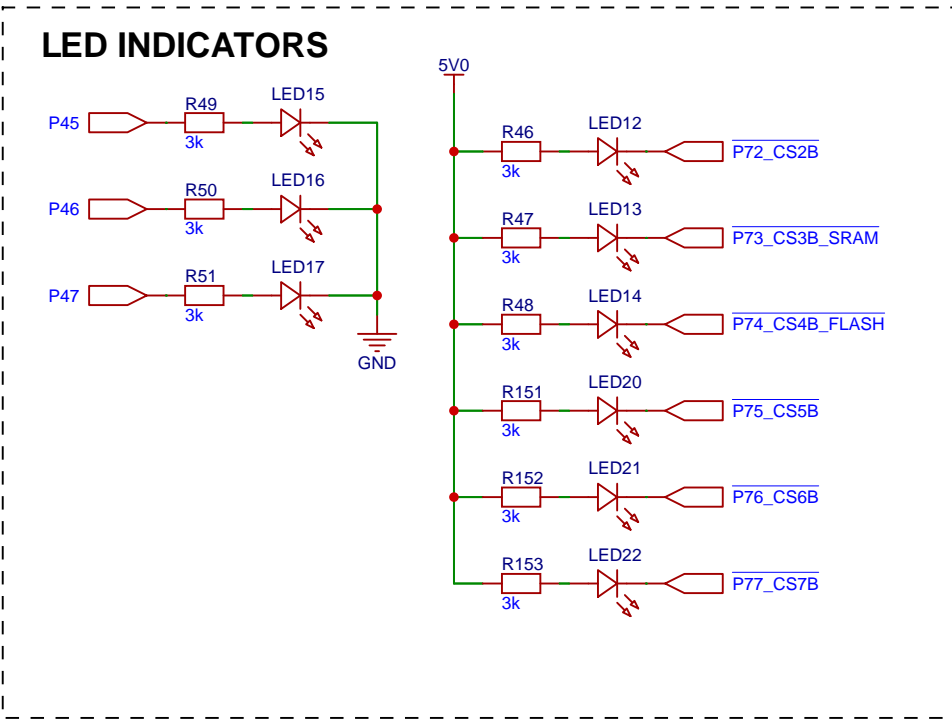
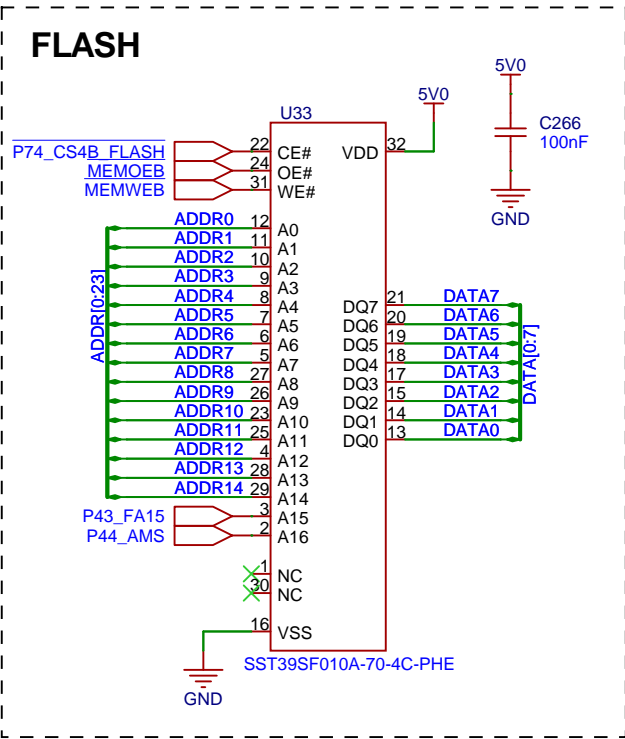
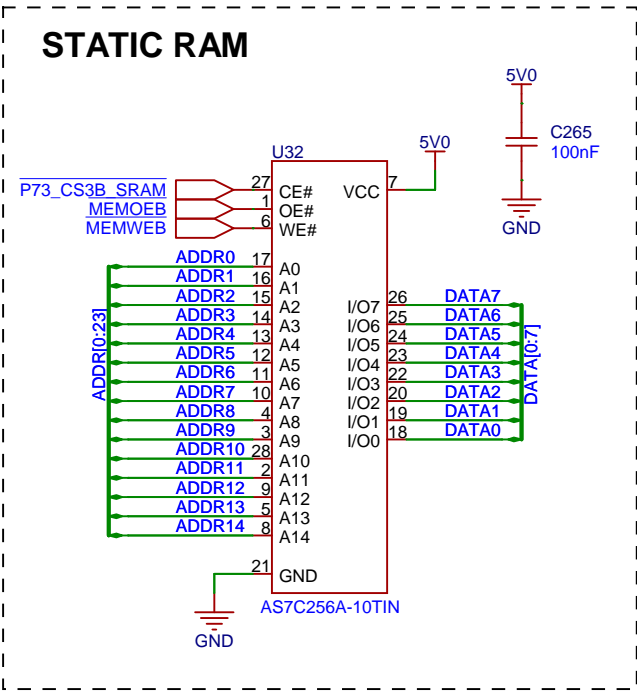
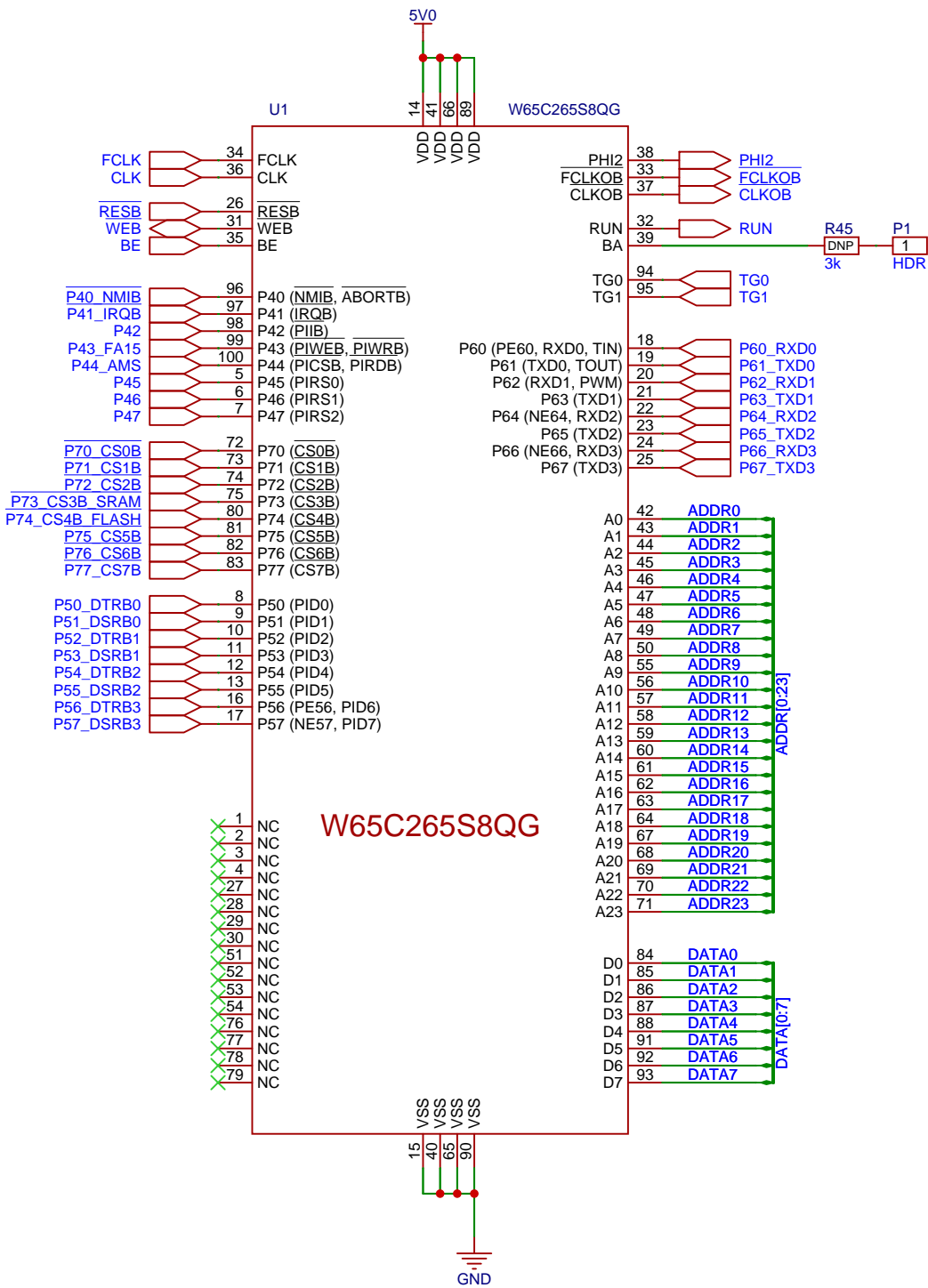
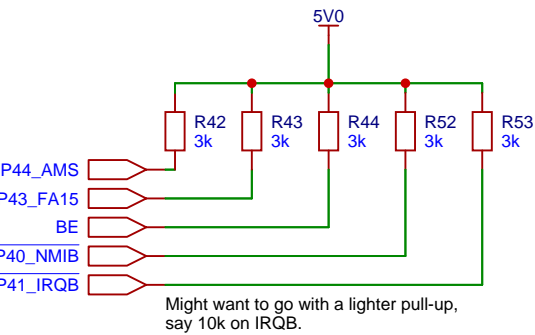
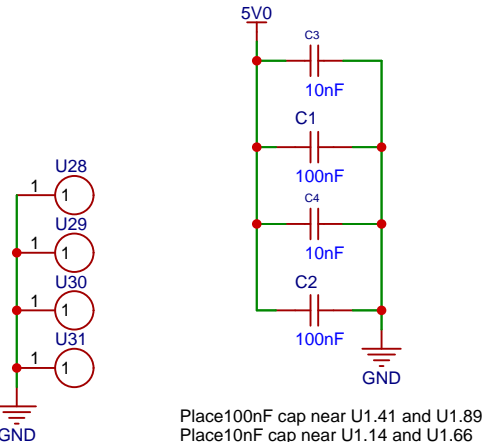


MCU

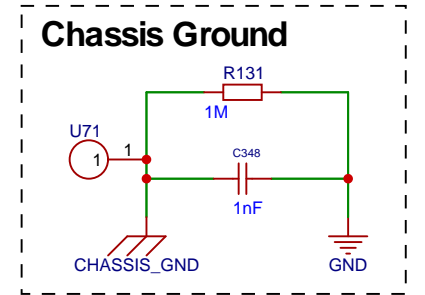
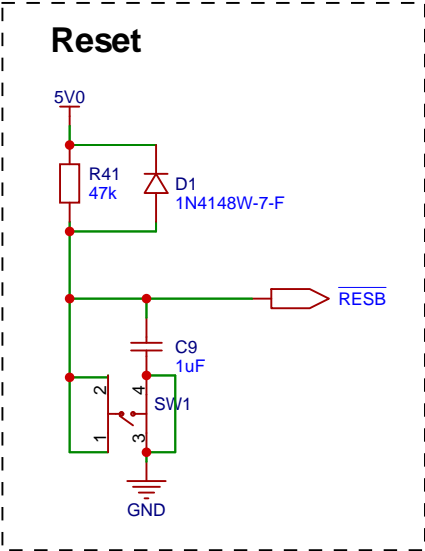
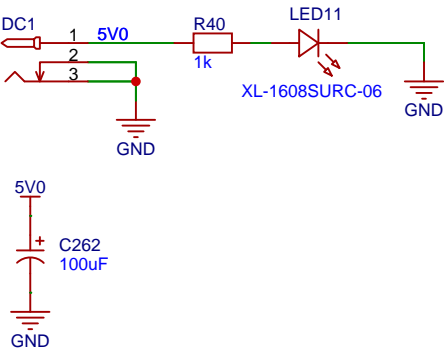


- To do:
- Indicators for serial RX/TX
 - Replace DIP ROM with SMD
 - Onboard ROM programming
 - power switch
 - Reset delay circuit (board-wide)?
 - LED indicators for all enable lines



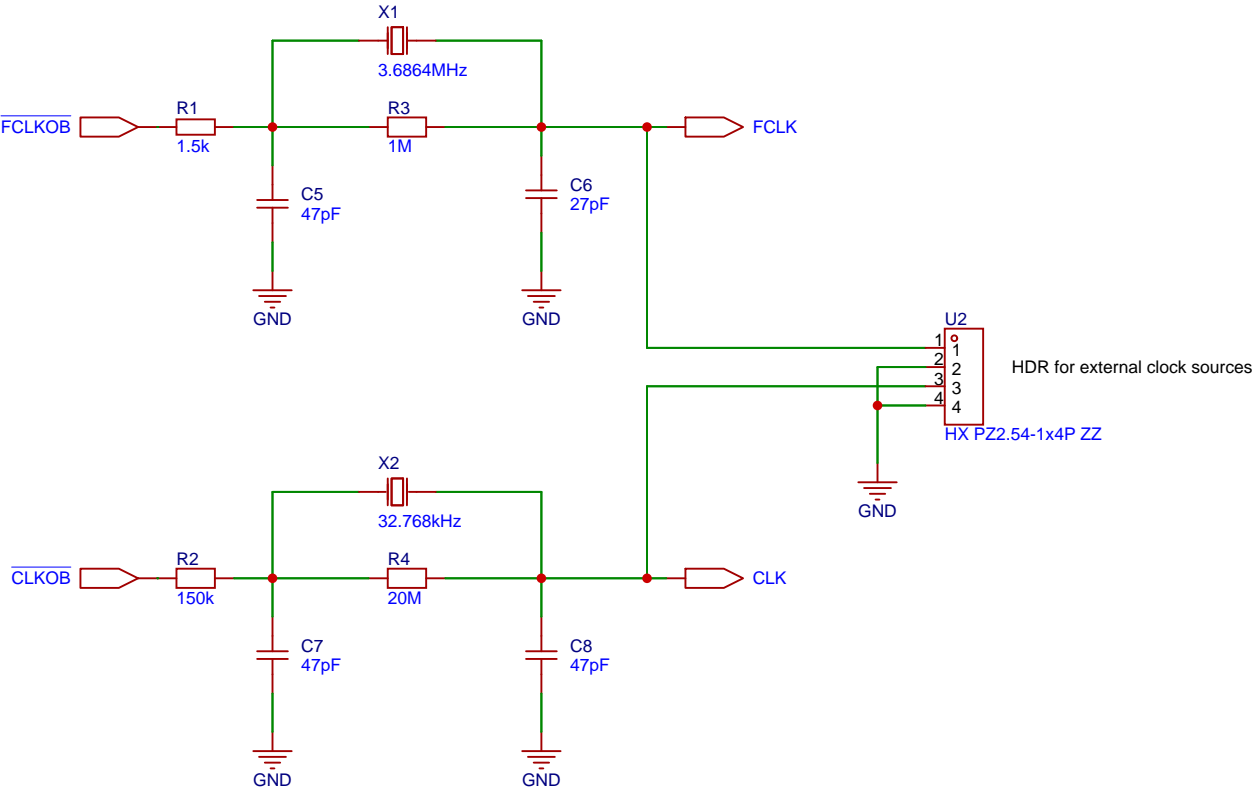
Schematic	Schematic1		Create at	2025-12-04
Board	Board1		Update at	2025-12-19
Drawn		W65C265S v0.04		
Reviewed				
		Version	Size	Page 1 Total 8
EasyEDA		V1.0	A4	EasyEDA.com

POWER



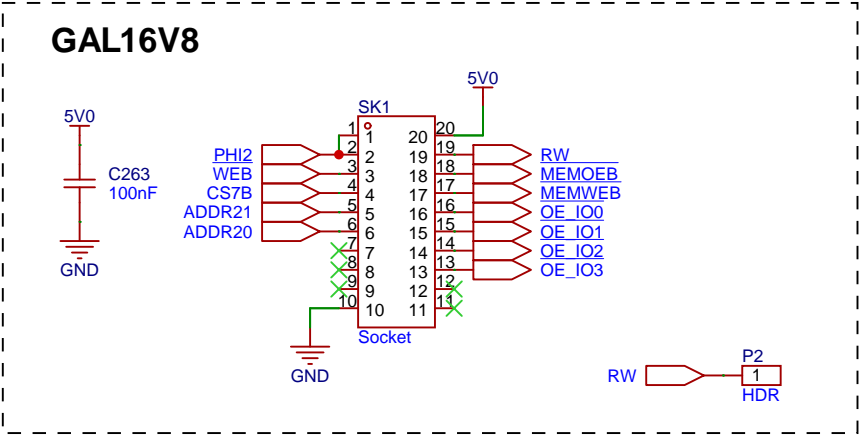
Schematic	Schematic1			Create at	2025-12-04
				Update at	2025-12-04
Board	Board1			Page	Power
Drawn	W65C265S v0.04				
Reviewed					
		Version	Size	Page 2 Total 8	
EasyEDA		V1.0	A4	EasyEDA.com	

CLOCKS



Schematic	Schematic1			Create at	2025-12-04
				Update at	2025-12-04
Board	Board1			Page	Clocks
Drawn	W65C265S v0.04				
Reviewed					
		Version	Size	Page 3 Total 8	
EasyEDA		V1.0	A4	EasyEDA.com	

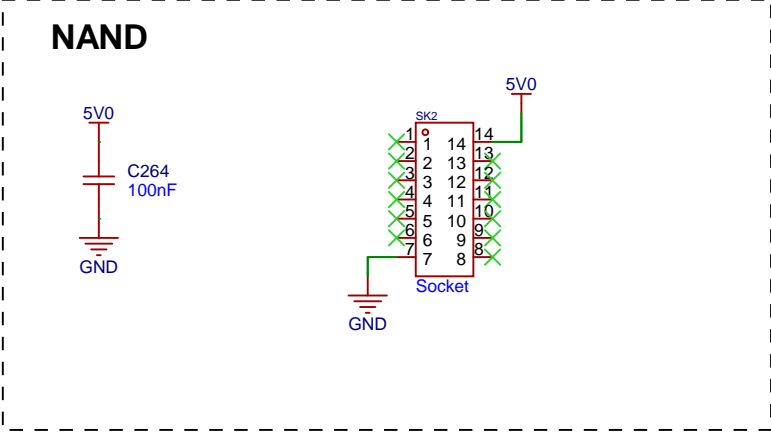
DECODE



C0:0000 to FF:FFFF CS7B

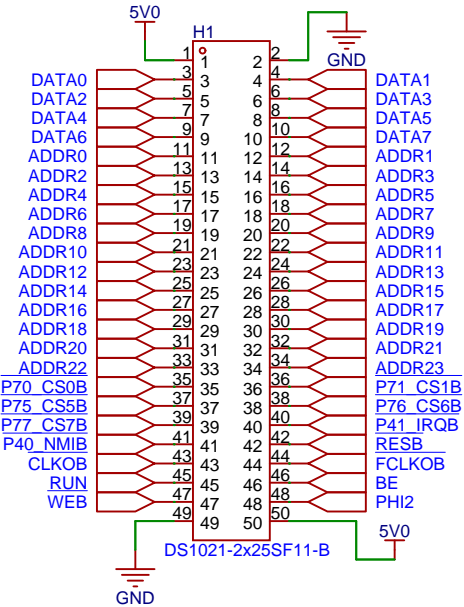
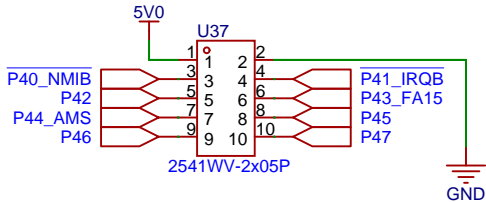
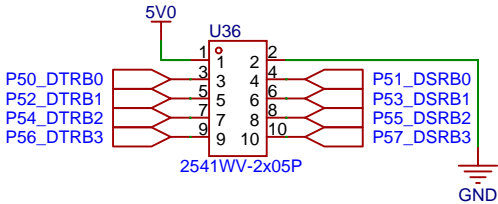
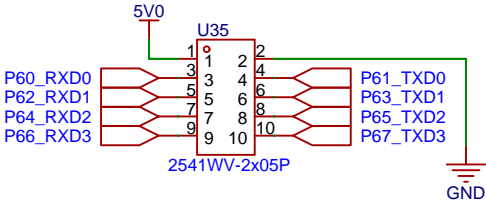
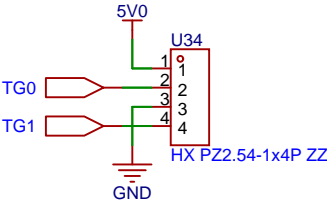
1100:00000000000000000000 to
1111:11111111111111111111
(range: top two bits are 11)

1100: VIA0 C0:xxxx C0:0000 to C0:000F
1101: VIA1 D0:xxxx D0:0000 to D0:000F
1110: VIA2 E0:xxxx E0:0000 to E0:000F
1111: VIA3 F0:xxxx F0:0000 to F0:000F



Schematic	Schematic1			Create at	2025-12-04
				Update at	2025-12-18
Board	Board1			Page	Decode
Drawn	W65C265S v0.04				
Reviewed					
		Version	Size	Page 4 Total 8	
		V1.0	A4	EasyEDA.com	

EXPANSION

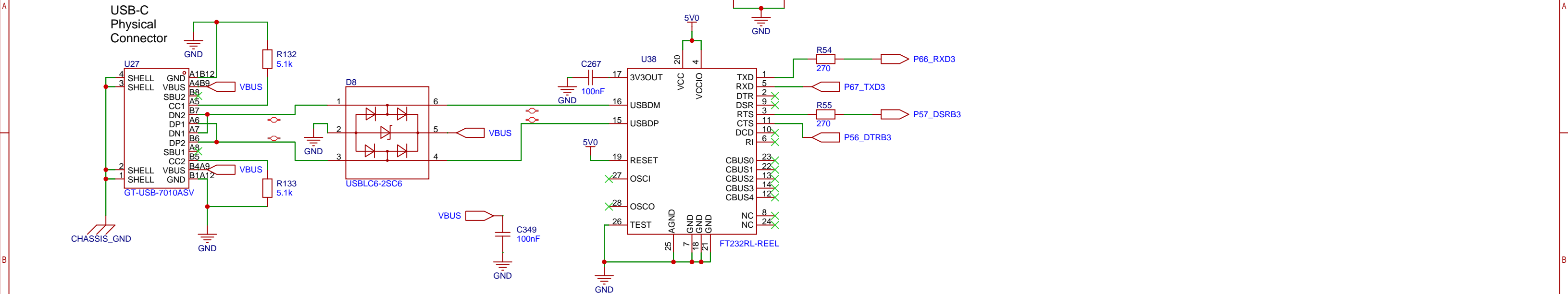


Used:

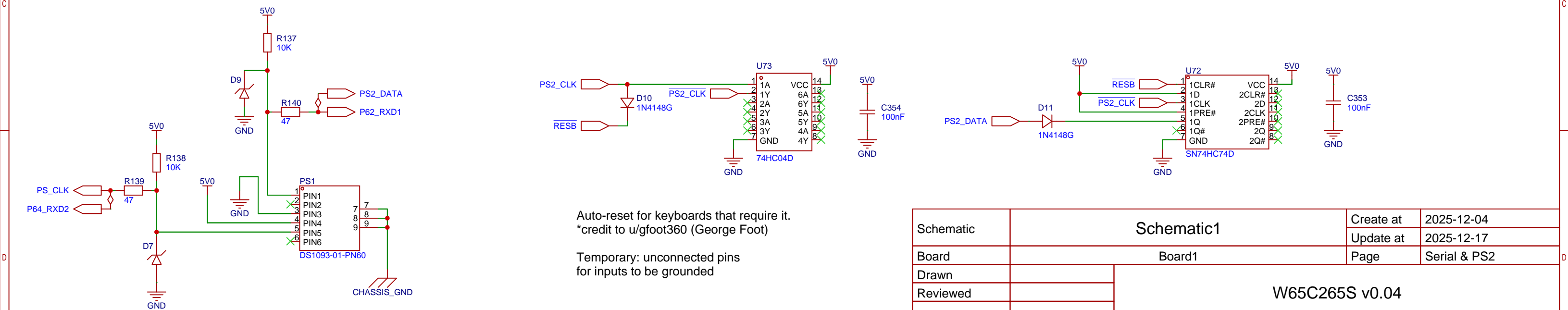
P50:56 - 1602 LCD
P51 - Available
P62 - PS2KBD DATA
P64 - PS2KBD CLK
P66 - RXD3
P67 - TXD3

Schematic	Schematic1			Create at	2025-12-04
Board	Board1			Update at	2025-12-17
Drawn	W65C265S v0.04			Page	Expansion
Reviewed					
		Version	Size	Page 5 Total 8	
EasyEDA		V1.0	A4	EasyEDA.com	

USB SERIAL



PS2 Keyboard

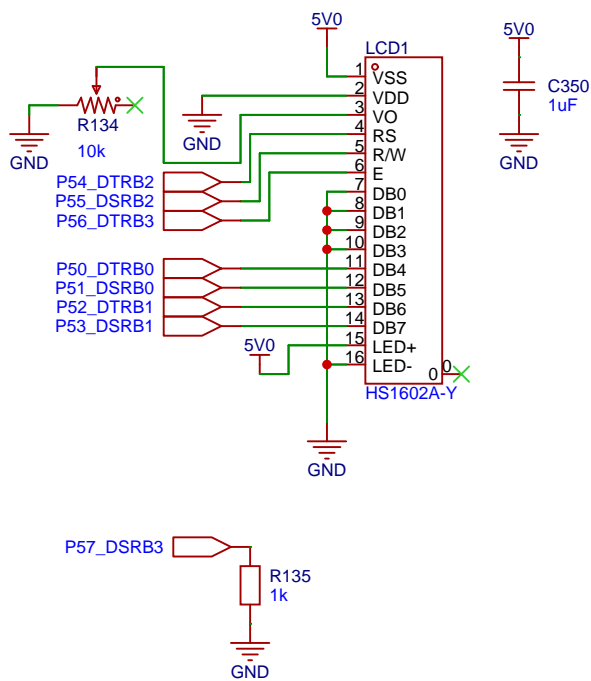


Auto-reset for keyboards that require it.
*credit to u/gfoot360 (George Foot)

Temporary: unconnected pins
for inputs to be grounded

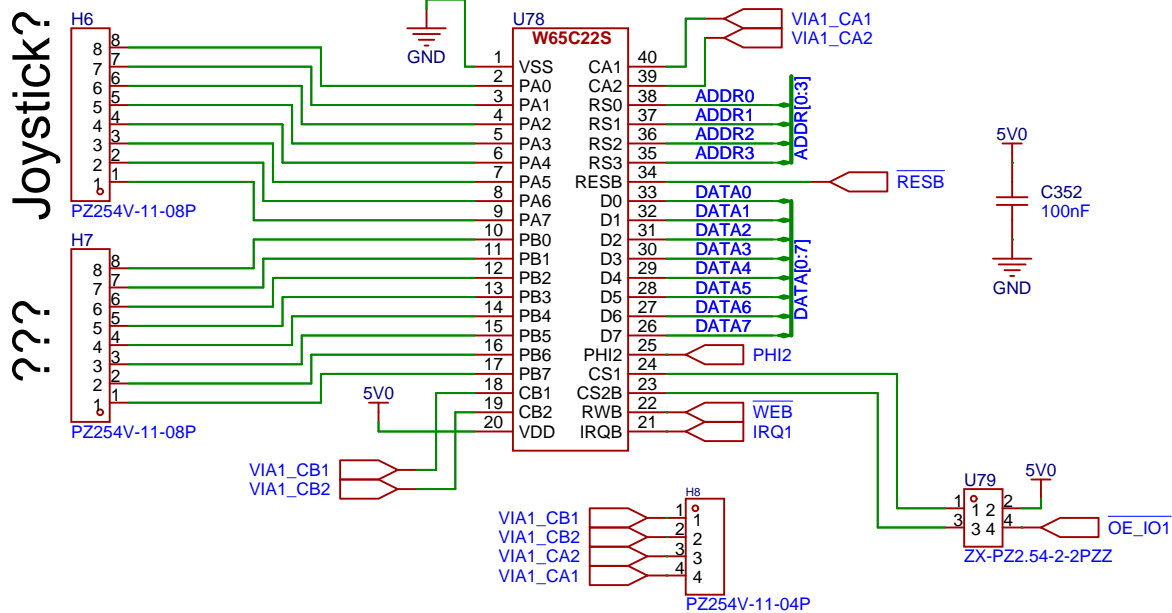
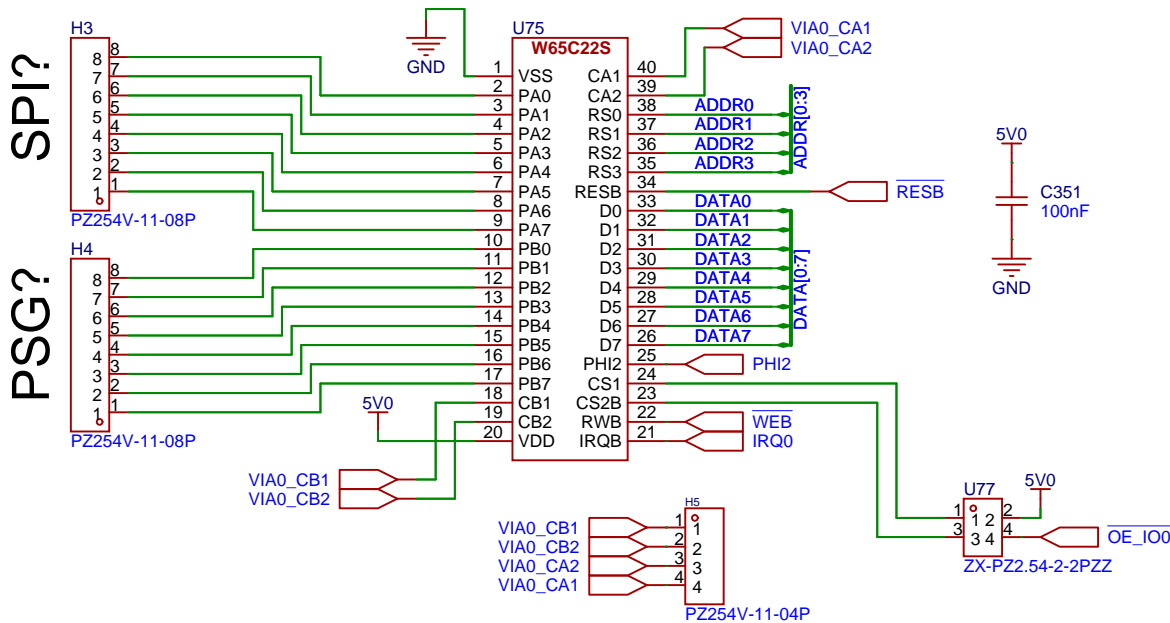
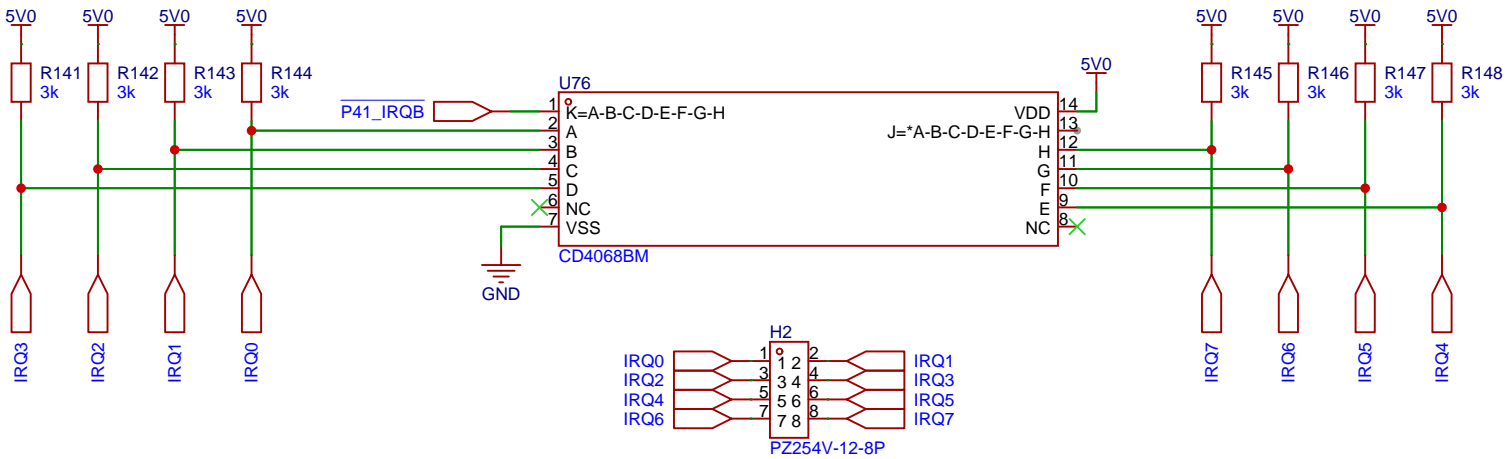
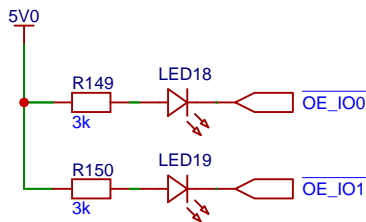
Schematic	Schematic1			Create at	2025-12-04
				Update at	2025-12-17
Board	Board1			Page	Serial & PS2
Drawn		W65C265S v0.04			
Reviewed					
		Version	Size	Page 6 Total 8	
EasyEDA		V1.0	A4	EasyEDA.com	

LCD 1602



Schematic	Schematic1		Create at	2025-12-06
			Update at	2025-12-17
Board	Board1		Page	LCD_1602
Drawn		W65C265S v0.04		
Reviewed				
		Version	Size	Page 7 Total 8
EasyEDA		V1.0	A4	EasyEDA.com

VIAs



C0:0000 to FF:FFFF CS7B

1100:00000000000000000000 to 1111:11111111111111111111 (range: top two bits are 11)

1100: VIA0 C0:xxxx C0:0000 to C0:000F
1101: VIA1 D0:xxxx D0:0000 to D0:000F
1110: VIA2 E0:xxxx E0:0000 to E0:000F
1111: VIA3 F0:xxxx F0:0000 to F0:000F

Schematic	Schematic1			Create at	2025-12-17
Board	Board1			Update at	2025-12-19
Drawn		W65C265S v0.04			
Reviewed					
		Version	Size	Page 8 Total 8	
		V1.0	A4	EasyEDA.com	