International Rectifier

IRLL014NPbF

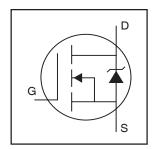
HEXFET® Power MOSFET

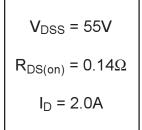
- Surface Mount
- Advanced Process Technology
- Ultra Low On-Resistance
- Dynamic dv/dt Rating
- Fast Switching
- Fully Avalanche Rated
- Lead-Free

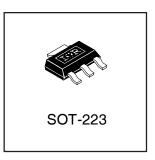
Description

Fifth Generation HEXFETs from International Rectifier utilize advanced processing techniques to achieve extremely low on-resistance per silicon area. This benefit, combined with the fast switching speed and ruggedized device design that HEXFET Power MOSFETs are well known for, provides the designer with an extremely efficient and reliable device for use in a wide variety of applications.

The SOT-223 package is designed for surface-mount using vapor phase, infra red, or wave soldering techniques. Its unique package design allows for easy automatic pick-and-place as with other SOT or SOIC packages but has the added advantage of improved thermal performance due to an enlarged tab for heatsinking. Power dissipation of 1.0W is possible in a typical surface mount application.







Absolute Maximum Ratings

	Parameter	Max.	Units	
I _D @ T _A = 25°C	Continuous Drain Current, V _{GS} @ 10V**	2.8		
I _D @ T _A = 25°C	Continuous Drain Current, V _{GS} @ 10V*	2.0	Α	
I _D @ T _A = 70°C	Continuous Drain Current, V _{GS} @ 10V*	uous Drain Current, V _{GS} @ 10V* 1.6		
I _{DM}	Pulsed Drain Current ①	16		
P _D @T _A = 25°C	Power Dissipation (PCB Mount)**	2.1	W	
P _D @T _A = 25°C	Power Dissipation (PCB Mount)*	1.0	W	
	Linear Derating Factor (PCB Mount)*	8.3	mW/°C	
V _{GS}	Gate-to-Source Voltage	± 16	V	
E _{AS}	Single Pulse Avalanche Energy®	32	mJ	
I _{AR}	Avalanche Current①	2.0	Α	
E _{AR}	Repetitive Avalanche Energy①*	0.1	mJ	
dv/dt	Peak Diode Recovery dv/dt ③	7.2	V/ns	
T _{J,} T _{STG}	Junction and Storage Temperature Range	-55 to + 150	°C	

Thermal Resistance

	Parameter	Тур.	Max.	Units	
$R_{\theta JA}$	Junction-to-Amb. (PCB Mount, steady state)*	90	120	°C/W	
$R_{\theta JA}$	Junction-to-Amb. (PCB Mount, steady state)**	50	60	_ C/VV	

^{*} When mounted on FR-4 board using minimum recommended footprint.

^{**} When mounted on 1 inch square copper board, for comparison with other SMD devices. www.irf.com

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Electrical Characteristics @ T_J = 25°C (unless otherwise specified)

	Parameter	Min.	Тур.	Max.	Units	Conditions
V _{(BR)DSS}	Drain-to-Source Breakdown Voltage	55			V	$V_{GS} = 0V, I_D = 250\mu A$
$\Delta V_{(BR)DSS}/\Delta T_J$	Breakdown Voltage Temp. Coefficient		0.015		V/°C	Reference to 25°C, I _D = 1mA
				0.14		V _{GS} = 10V, I _D = 2.0A ④
R _{DS(on)}	Static Drain-to-Source On-Resistance			0.20	Ω	V _{GS} = 5.0V, I _D = 1.2A ④
				0.28		V _{GS} = 4.0V, I _D = 1.0A ④
V _{GS(th)}	Gate Threshold Voltage	1.0		2.0	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$
g _{fs}	Forward Transconductance	2.3			S	$V_{DS} = 25V, I_D = 1.0A$
1	Drain to Source Leekage Current			25		V_{DS} = 55V, V_{GS} = 0V
I _{DSS}	Drain-to-Source Leakage Current			250	μA	V _{DS} = 44V, V _{GS} = 0V, T _J = 150°C
1	Gate-to-Source Forward Leakage			100	nA	V _{GS} = 16V
I _{GSS}	Gate-to-Source Reverse Leakage			-100	IIA	V _{GS} = -16V
Qg	Total Gate Charge		9.5	14		I _D = 2.0A
Q _{gs}	Gate-to-Source Charge		1.1	1.7	nC	$V_{DS} = 44V$
Q _{gd}	Gate-to-Drain ("Miller") Charge		3.0	4.4		V_{GS} = 10V, See Fig. 6 and 9 \oplus
t _{d(on)}	Turn-On Delay Time		5.1			V _{DD} = 28V
t _r	Rise Time		4.9		ns	I _D = 2.0A
t _{d(off)}	Turn-Off Delay Time		14		115	$R_G = 6.0\Omega$
t _f	Fall Time		2.9			R_D = 14 Ω , See Fig. 10 \oplus
C _{iss}	Input Capacitance		230			V _{GS} = 0V
Coss	Output Capacitance		66		pF	V _{DS} = 25V
C _{rss}	Reverse Transfer Capacitance		30			f = 1.0MHz, See Fig. 5

Source-Drain Ratings and Characteristics

	Parameter	Min.	Тур.	Max.	Units	Conditions
Is	Continuous Source Current			4.0		MOSFET symbol
	(Body Diode)		1.3		showing the	
I _{SM}	Pulsed Source Current			10	A	integral reverse G \(\bigcup \)
	(Body Diode) ①		16		p-n junction diode.	
V _{SD}	Diode Forward Voltage			1.0	V	T _J = 25°C, I _S = 2.0A, V _{GS} = 0V ④
t _{rr}	Reverse Recovery Time		41	61	ns	$T_J = 25$ °C, $I_F = 2.0A$
Q _{rr}	Reverse RecoveryCharge		73	110	nC	di/dt = 100A/µs ⊕
ton	Forward Turn-On Time	Intrinsic turn-on time is negligible (turn-on is dominated by L _S +L _D)				

Notes:

- ① Repetitive rating; pulse width limited by max. junction temperature. (See fig. 11)
- $\label{eq:loss} \begin{array}{l} \text{ } 3 \text{ } I_{SD} \leq 2.0A, \text{ } di/dt \leq 170A/\mu s, \text{ } V_{DD} \leq V_{(BR)DSS}, \\ T_{J} \leq 150 ^{\circ} C \end{array}$
- 4 Pulse width $\leq 300 \mu s$; duty cycle $\leq 2\%$.

International TOR Rectifier

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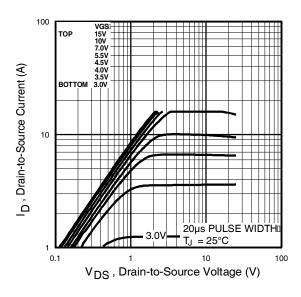


Fig 1. Typical Output Characteristics,

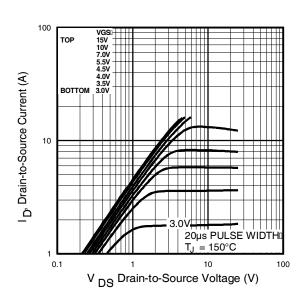


Fig 2. Typical Output Characteristics,

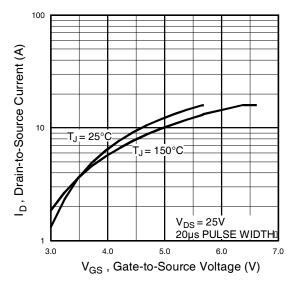


Fig 3. Typical Transfer Characteristics

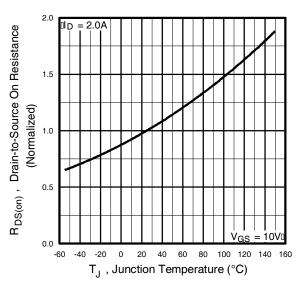


Fig 4. Normalized On-Resistance Vs. Temperature

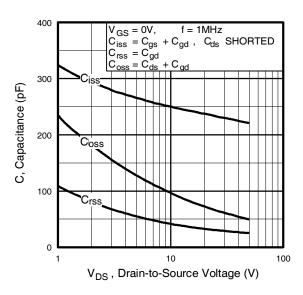


Fig 5. Typical Capacitance Vs. Drain-to-Source Voltage

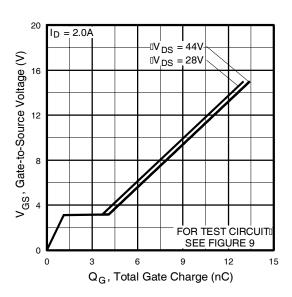


Fig 6. Typical Gate Charge Vs. Gate-to-Source Voltage

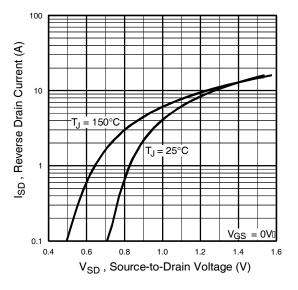


Fig 7. Typical Source-Drain Diode Forward Voltage

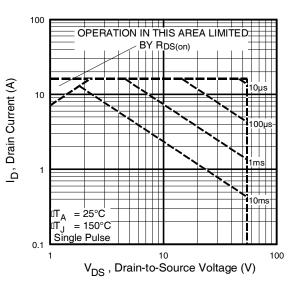


Fig 8. Maximum Safe Operating Area

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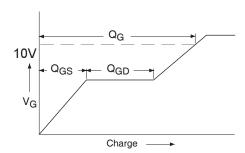


Fig 9a. Basic Gate Charge Waveform

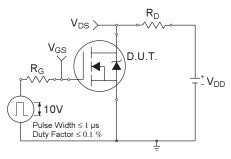


Fig 10a. Switching Time Test Circuit

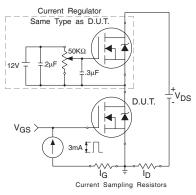


Fig 9b. Gate Charge Test Circuit

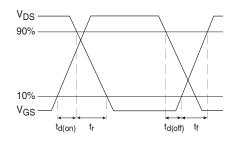


Fig 10b. Switching Time Waveforms

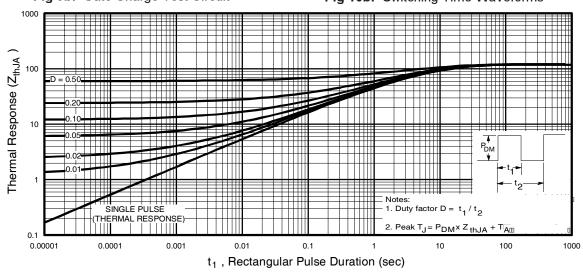


Fig 11. Maximum Effective Transient Thermal Impedance, Junction-to-Ambient

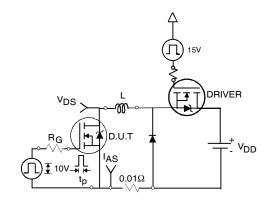


Fig 12a. Unclamped Inductive Test Circuit

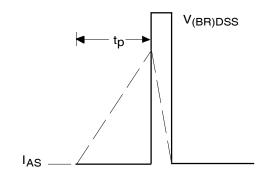


Fig 12b. Unclamped Inductive Waveforms

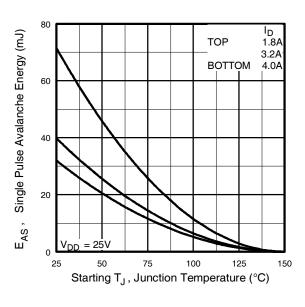
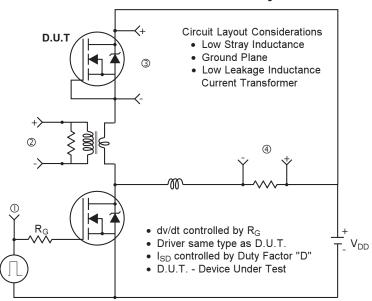
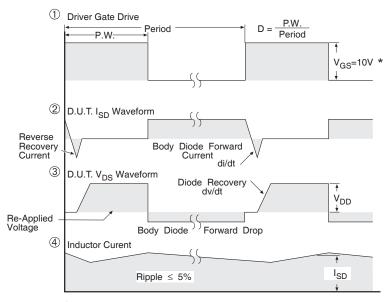


Fig 12c. Maximum Avalanche Energy Vs. Drain Current

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Peak Diode Recovery dv/dt Test Circuit



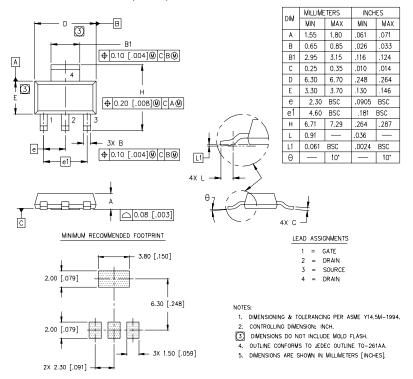


* V_{GS} = 5V for Logic Level Devices

Fig 13. For N-Channel HEXFETS

SOT-223 (TO-261AA) Package Outline

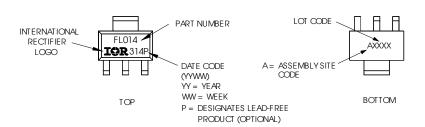
Dimensions are shown in milimeters (inches)



SOT-223 (TO-261AA) Part Marking Information

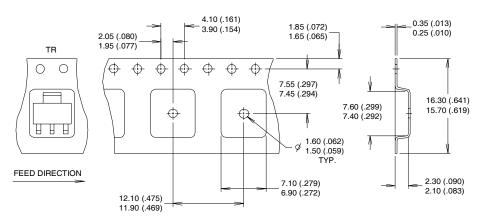
HEXFET PRODUCT MARKING

EXAMPLE: THIS IS AN IRFL014



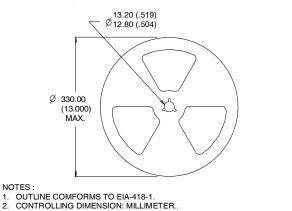
SOT-223 (TO-261AA) Tape & Reel Information

Dimensions are shown in milimeters (inches)

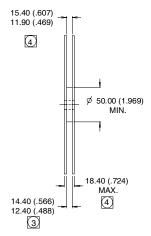


NOTES:

- 1. CONTROLLING DIMENSION: MILLIMETER.
- 2. OUTLINE CONFORMS TO EIA-481 & EIA-541.
- 3. EACH Ø330.00 (13.00) REEL CONTAINS 2,500 DEVICES.



DIMENSION MEASURED @ HUB. INCLUDES FLANGE DISTORTION @ OUTER EDGE.



Data and specifications subject to change without notice.



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