

PHOTOCOUPLER

PS2561-1,-2, PS2561L-1,-2

HIGH ISOLATION VOLTAGE SINGLE TRANSISTOR TYPE MULTI PHOTOCOUPLER SERIES

DESCRIPTION

The PS2561-1, -2 and PS2561L-1, -2 are optically coupled isolators containing a GaAs light emitting diode and an NPN silicon phototransistor.

PS2561-1, -2 are in a plastic DIP (Dual In-line Package) and PS2561L-1, -2 are lead bending type (Gull-wing) for surface mount.

FEATURES

- High isolation voltage BV = 5 000 Vr.m.s.: standard products
 - BV = 3 750 Vr.m.s.: VDE0884 approved products (Option)
- High collector to emitter voltage (VcEo = 80 V)
- High current transfer ratio (CTR = 200 % TYP.)
- High-speed switching ($t_r = 3 \mu s$ TYP., $t_f = 5 \mu s$ TYP.)
- UL approved (File No. E72422 (S))
- ★ CSA approved (No. CA 101391)
 - BSI approved (BS415, BS7002) No. 7112
 - SEMKO approved (SS4410165) No. 9317144
 - NEMKO approved (NEK-HD 195S6) No. A21409
 - DEMKO approved (Section 101, 137) No. 300535
- FIMKO approved (E69-89) No. 167265-08
 - VDE0884 approved (Option)

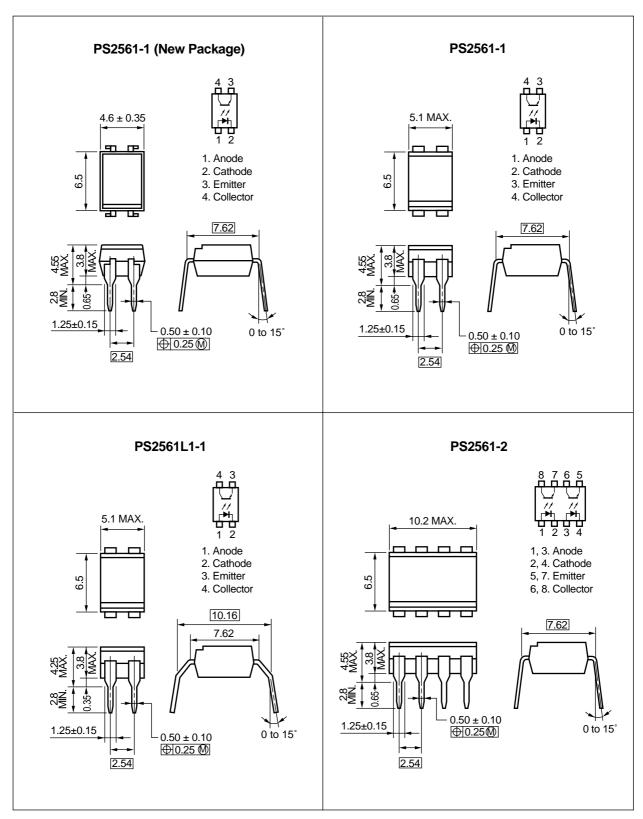
APPLICATIONS

- · Power supply
- · Telephone/FAX.
- FA/OA equipment
- · Programmable logic controller

The information in this document is subject to change without notice.

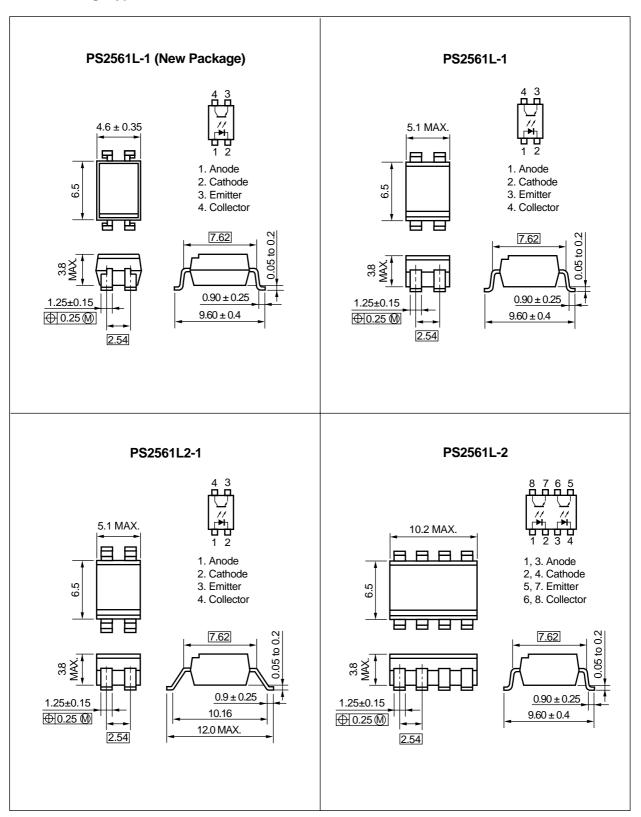
★ PACKAGE DIMENSIONS (in millimeters)

DIP Type



Caution New package 1ch only

Lead Bending Type



Caution New package 1ch only

* ORDERING INFORMATION

Part Number	Package	Safety Standard Approval	Application part number*1	
PS2561-1 PS2561L-1 PS2561L1-1 PS2561L2-1	4-pin DIP 4-pin DIP (lead bending surface mount) 4-pin DIP (for long distance) 4-pin DIP (for long distance surface mount)	Standard products • UL approved • BSI approved • DEMKO approved • FIMKO approved	PS2561-1	
PS2561-2 PS2561L-2	8-pin DIP 8-pin DIP (lead bending surface mount)		PS2561-2	
PS2561-1-V PS2561L-1-V PS2561L1-1-V PS2561L2-1-V	4-pin DIP 4-pin DIP (lead bending surface mount) 4-pin DIP (for long distance) 4-pin DIP (for long distance surface mount)	VDE0884 approved products (Option)	PS2561-1	
PS2561-2-V PS2561L-2-V	8-pin DIP 8-pin DIP (lead bending surface mount)		PS2561-2	

^{*1} As applying to Safety Standard, following part number should be used.

ABSOLUTE MAXIMUM RATINGS (TA = 25 °C, unless otherwise specified)

Parameter		Symbol	Ratings		Unit
			PS2561-1, PS2561L-1	PS2561-2, PS2561L-2	
Diode	Reverse Voltage	VR	6	٧	
	Forward Current (DC)	lF	80		mA
	Power Dissipation Derating	∆P₀/°C	1.5	1.2	mW/°C
	Power Dissipation	PD	150	120	mW/ch
	Peak Forward Current*1	IFP	1		Α
Transistor	Collector to Emitter Voltage	VCEO	80		٧
	Emitter to Collector Voltage	VECO	7		٧
	Collector Current	lc	50		mA/ch
	Power Dissipation Derating	∆Pc/°C	1.5	1.2	mW/°C
	Power Dissipation	Pc	150	120	mW/ch
Isolation Voltage ^{*2}		BV	5 000 3 750 ^{*3}		Vr.m.s.
Operating Ambient Temperature		TA	-55 to +100		°C
Storage Temperature		T _{stg}	-55 to +150		°C

^{*1} PW = 100 μ s, Duty Cycle = 1 %

^{*2} AC voltage for 1 minute at T_A = 25 °C, RH = 60 % between input and output

^{*3} VDE0884 approved products (Option)

ELECTRICAL CHARACTERISTICS (TA = 25 °C)

Parameter		Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Diode	Forward Voltage	VF	IF = 10 mA		1.17	1.4	V
	Reverse Current	lR	V _R = 5 V			5	μΑ
	Terminal Capacitance	Ct	V = 0 V, f = 1.0 MHz		50		pF
Transistor	Collector to Emitter Dark Current	Iceo	VcE = 80 V, IF = 0 mA			100	nA
Coupled	Current Transfer Ratio*1	CTR	IF = 5 mA, VcE = 5 V	80	200	400	%
	Collector Saturation Voltage	VCE (sat)	I _F = 10 mA, I _C = 2 mA			0.3	V
	Isolation Resistance	R _{I-O}	Vi-o = 1.0 kV	10¹¹			Ω
	Isolation Capacitance	C _{I-O}	V = 0 V, f = 1.0 MHz		0.5		pF
	Rise Time *2	t r	$Vcc = 10 \text{ V}, \text{ Ic} = 2 \text{ mA}, \text{ RL} = 100 \Omega$		3		μs
	Fall Time *2	t _f			5		

*1 CTR rank (only PS2561-1, PS2561L-1)

L : 200 to 400 (%)

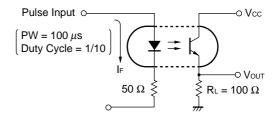
M : 80 to 240 (%)

D : 100 to 300 (%)

H : 80 to 160 (%)

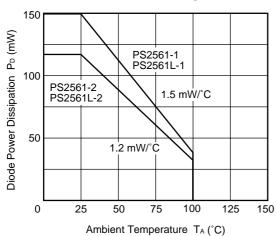
W : 130 to 260 (%)

*2 Test circuit for switching time

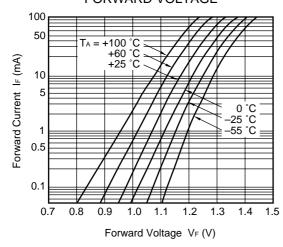


TYPICAL CHARACTERISTICS (TA = 25 °C, unless otherwise specified)

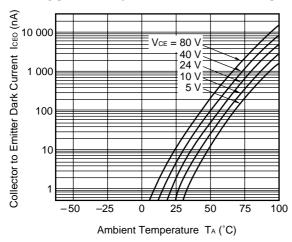




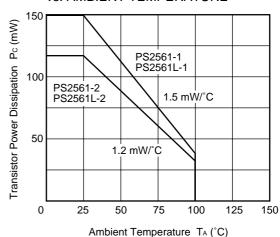
FORWARD CURRENT vs. FORWARD VOLTAGE



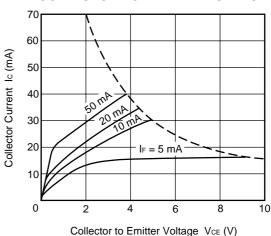
COLLECTOR TO EMITTER DARK CURRENT vs. AMBIENT TEMPERATURE



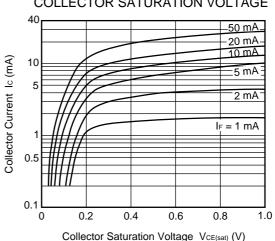
TRANSISTOR POWER DISSIPATION vs. AMBIENT TEMPERATURE



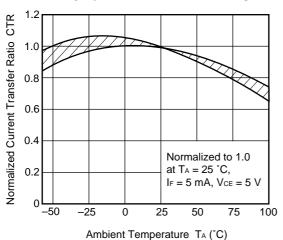
COLLECTOR CURRENT vs. COLLECTOR TO EMITTER VOLTAGE



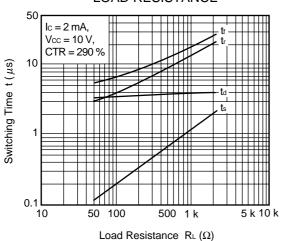
COLLECTOR CURRENT vs. **COLLECTOR SATURATION VOLTAGE**



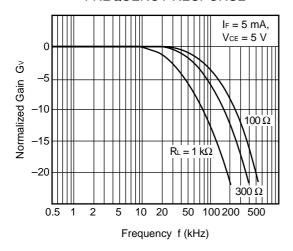
NORMALIZED CURRENT TRANSFER RATIO vs. AMBIENT TEMPERATURE



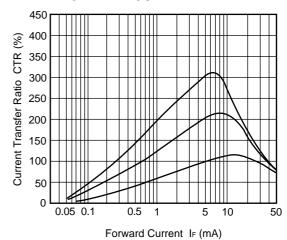
SWITCHING TIME vs. LOAD RESISTANCE



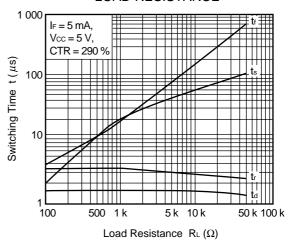
FREQUENCY RESPONSE



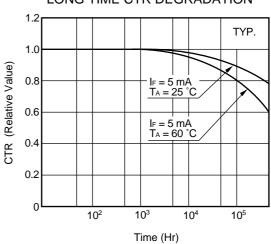
CURRENT TRANSFER RATIO vs. FORWARD CURRENT



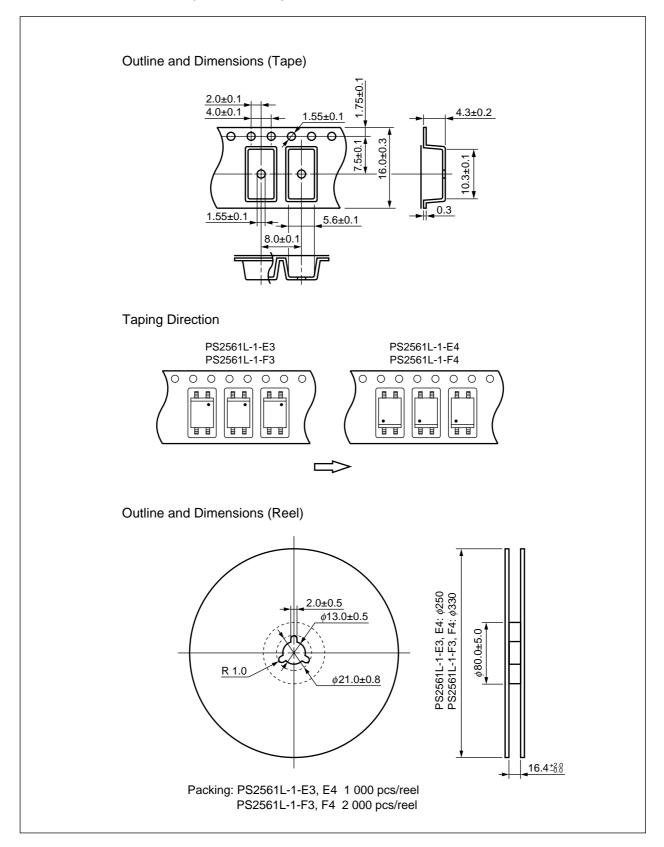
SWITCHING TIME vs. LOAD RESISTANCE

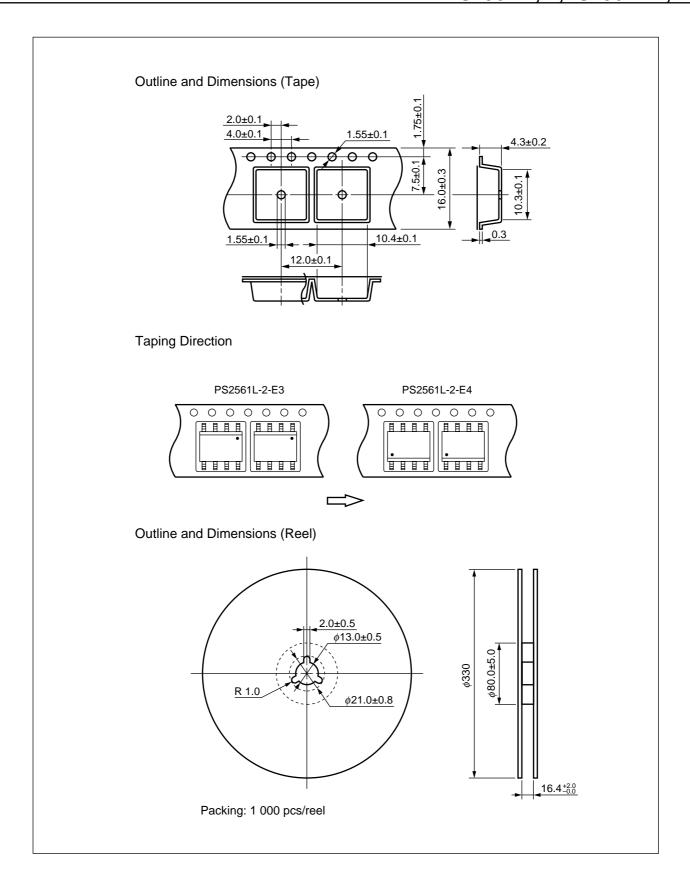


LONG TIME CTR DEGRADATION



* TAPING SPECIFICATIONS (in millimeters)





* RECOMMENDED SOLDERING CONDITIONS

(1) Infrared reflow soldering

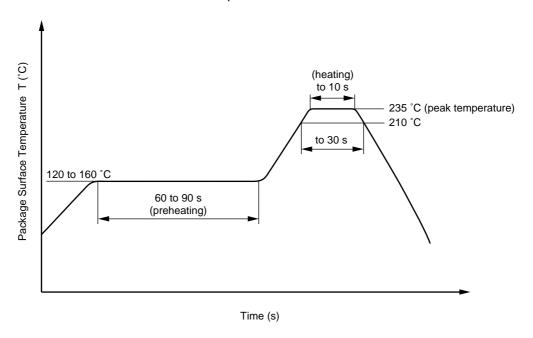
• Peak reflow temperature 235 °C (package surface temperature)

• Time of temperature higher than 210 °C 30 seconds or less

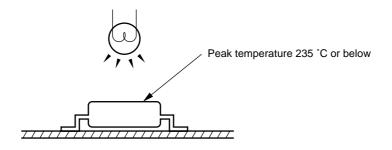
• Number of reflows Three

• Flux Rosin flux containing small amount of chlorine (The flux with a maximum chlorine content of 0.2 Wt % is recommended.)

Recommended Temperature Profile of Infrared Reflow



Caution Please avoid to removed the residual flux by water after the first reflow processes.



(2) Dip soldering

• Temperature 260 °C or below (molten solder temperature)

• Time 10 seconds or less

• Number of times One

• Flux Rosin flux containing small amount of chlorine (The flux with a maximum chlorine content of

0.2 Wt % is recommended.)



SPECIFICATION OF VDE MARKS LICENSE DOCUMENT (VDE0884)

Parameter	Symbol	Speck	Unit
Application classification (DIN VDE 0109) for rated line voltages ≤ 300 Vr.m.s. for rated line voltages ≤ 600 Vr.m.s.		IV III	
Climatic test class (DIN IEC 68 Teil 1/09.80)		55/100/21	
Dielectric strength maximum operating isolation voltage Test voltage (partial discharge test procedure a for type test and random test) $U_{pr} = 1.2 \times U_{IORM}$, $P_d < 5 \ pC$	UIORM Upr	890 1 068	V _{peak} V _{peak}
Test voltage (partial discharge test procedure b for random test) $U_{pr} = 1.6 \times U_{IORM}, P_d < 5 pC$	Upr	1 424	V _{peak}
Highest permissible overvoltage	Utr	6 000	V _{peak}
Degree of pollution (DIN VDE 0109)		2	
Clearance distance		> 7.0	mm
Creepage distance		> 7.0	mm
Comparative tracking index (DIN IEC 112/VDE 0303 part 1)	CTI	175	
Material group (DIN VDE 0109)		III a	
Storage temperature range	T _{stg}	-55 to +150	°C
Operating temperature range	TA	-55 to +100	°C
Isolation resistance, minimum value VIO = 500 V dc at TA = 25 °C VIO = 500 V dc at TA MAX. at least 100 °C	Ris MIN. Ris MIN.	10 ¹² 10 ¹¹	Ω Ω
Safety maximum ratings (maximum permissible in case of fault, see thermal derating curve) Package temperature Current (input current I _F , Psi = 0)	Tsi Isi	175 400	°C mA
Power (output or total power dissipation) Isolation resistance Vio = 500 V dc at T _A = 175 °C (Tsi)	Psi Ris MIN.	700 10°	mW Ω

CAUTION

Within this device there exists GaAs (Gallium Arsenide) material which is a harmful substance if ingested. Please do not under any circumstances break the hermetic seal.

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Special: Transportation equipment (automobiles, trains, ships, etc.), traffic control systems, anti-disaster systems, anti-crime systems, safety equipment and medical equipment (not specifically designed for life support)

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Anti-radioactive design is not implemented in this product.