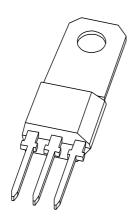
DISCRETE SEMICONDUCTORS

DATA SHEET



BF869; BF871 NPN high-voltage transistors

Product specification Supersedes data of 1996 Dec 09 1999 Apr 12





NPN high-voltage transistors

BF869; BF871

FEATURES

• Low feedback capacitance.

APPLICATIONS

• For use in class-B video output stages in colour television receivers.

DESCRIPTION

NPN transistors in a TO-202 plastic package. PNP complement: BF872.

PINNING

PIN	DESCRIPTION	
1	emitter	
2	collector, connected to mounting base	
3	base	

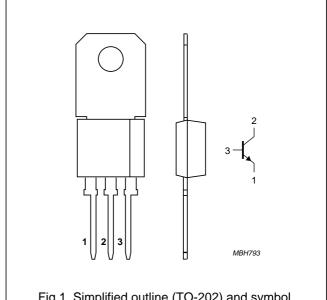


Fig.1 Simplified outline (TO-202) and symbol.

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter			
	BF869		_	250	V
	BF871		_	300	V
V _{CEO}	collector-emitter voltage	open base			
	BF869		_	250	V
	BF871		_	300	V
V _{EBO}	emitter-base voltage	open collector	_	5	V
I _C	collector current (DC)		_	50	mA
I _{CM}	peak collector current	peak value	_	100	mA
I _{BM}	peak base current		_	50	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	_	1.6	W
		T _{mb} ≤ 25 °C	_	5	W
T _{stg}	storage temperature		-65	+150	°C
Tj	junction temperature		_	150	°C
T _{amb}	operating ambient temperature		-65	+150	°C

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NPN high-voltage transistors

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THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	VALUE	UNIT
R _{th j-a}	thermal resistance from junction to ambient		K/W
R _{th j-mb}	R _{th j-mb} thermal resistance from junction to mounting base		K/W

CHARACTERISTICS

 $T_j = 25$ °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
I _{CBO}	collector cut-off current	I _E = 0; V _{CB} = 200 V	_	10	nA
		I _E = 0; V _{CB} = 200 V; T _j = 150 °C		10	μΑ
I _{EBO}	emitter cut-off current	I _C = 0; V _{EB} = 5 V	_	50	nA
h _{FE}	DC current gain	I _C = 25 mA; V _{CE} = 20 V	50	_	
V _{CEsat}	collector-emitter saturation voltage	$I_C = 30 \text{ mA}; I_B = 5 \text{ mA}$	_	600	mV
C _{re}	feedback capacitance	$I_C = i_c = 0$; $V_{CE} = 30 \text{ V}$; $f = 1 \text{ MHz}$	_	2	pF
f _T	transition frequency	I _C = 10 mA; V _{CE} = 10 V; f = 100 MHz	60	_	MHz

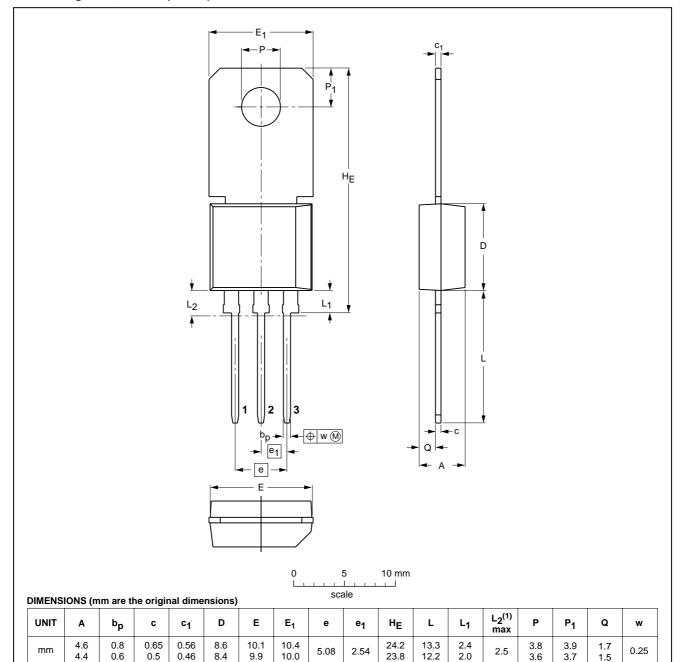
NPN high-voltage transistors

BF869; BF871

PACKAGE OUTLINE

Plastic single-ended leaded (through hole) package; with cooling fin, mountable to heatsink, 1 mounting hole; 3 leads (in-line)

SOT128B



Note

1. Plastic flash allowed within this zone

0.6

0.5

0.46

8.4

9.9

10.0

OUTLINE	REFERENCES		EUROPEAN	ISSUE DATE		
VERSION	IEC	JEDEC	EIAJ		PROJECTION 1550E	ISSUE DATE
SOT128B		TO-202				97-02-28

23.8

12.2

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NPN high-voltage transistors

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DEFINITIONS

Data sheet status	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
Limiting values	

Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation

of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

Application information

Where application information is given, it is advisory and does not form part of the specification.

LIFE SUPPORT APPLICATIONS

These products are not designed for use in life support appliances, devices, or systems where malfunction of these products can reasonably be expected to result in personal injury. Philips customers using or selling these products for use in such applications do so at their own risk and agree to fully indemnify Philips for any damages resulting from such improper use or sale.

NPN high-voltage transistors

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NOTES

NPN high-voltage transistors

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NOTES

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