

RL78/F1x RS-CAN Lite operation

CS+/CG & RS-CAN Configurator

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- Integrated RS-CAN API into CS+ project
- Setup debug enviroment

RS-CAN Configurator API List

RS-CAN Configurator API List - Initial

Function / Description
Can_RtnType R_CAN_Init(void) Initialize CAN controller after reset
Can_RtnType R_CAN_Globalstart(void) Start global operation
Can_RtnType R_CAN_chstart_CH0(void) Start channel operation (Channel 0)
Can_RtnType R_CAN_Readchstatus(void) Read channel status (C0STS1)

RS-CAN Configurator API List - Transmit

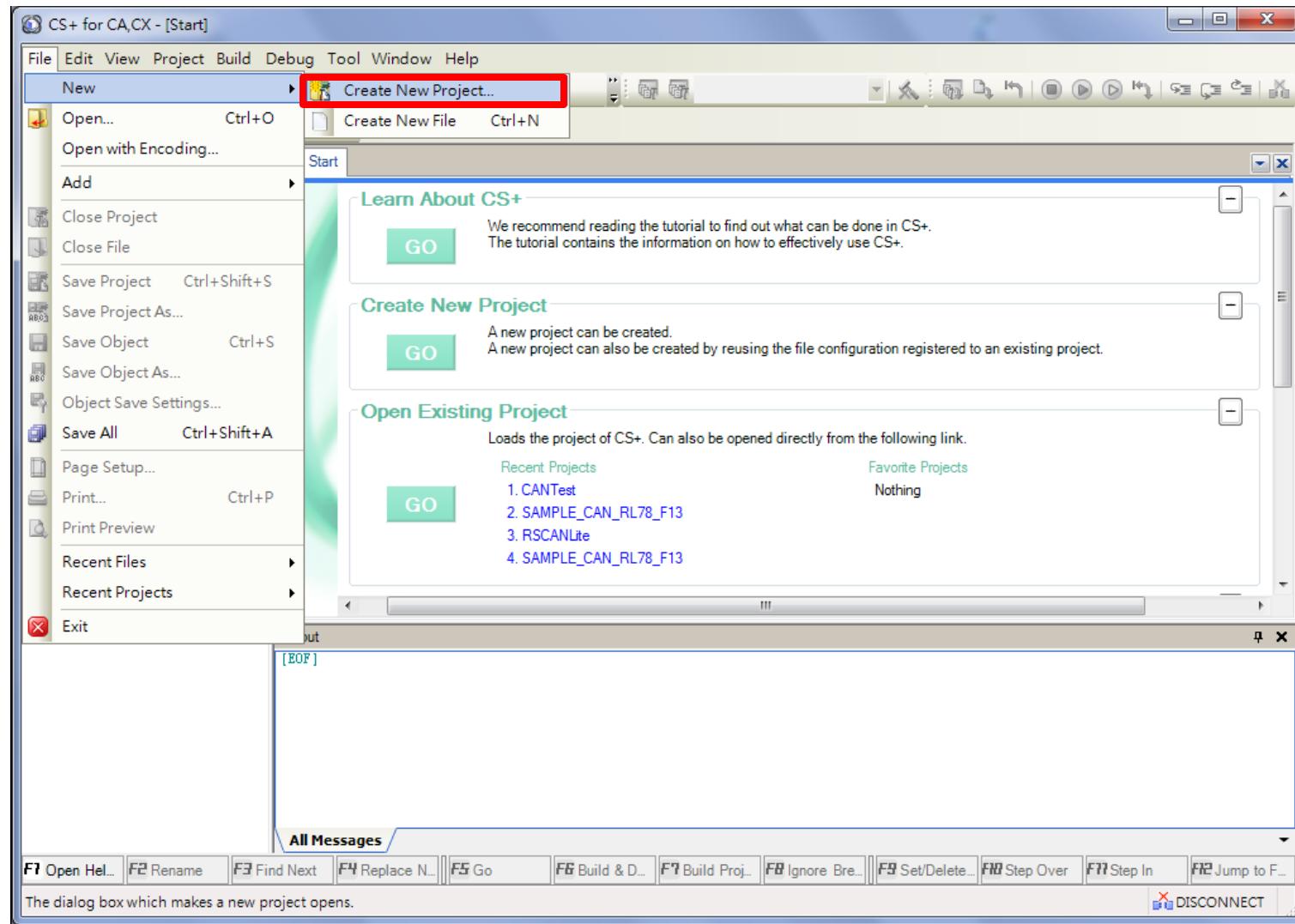
Function / Description
Can_RtnType R_CAN_TrmByTxBuf_CH0 (can_txbuf_t txbuf_idx, const can_frame_t * pFrame)
Transmit a frame by Tx buffer (Channel 0)
Can_RtnType R_CAN_AbortTrm_CH0 (can_txbuf_t txbuf_idx)
Abort a CAN transmission (Channel 0)
Can_RtnType R_CAN_CheckTxBufResult_CH0 (can_txbuf_t txbuf_idx)
Read the result of transmission from Tx buffer (Channel 0)
Can_RtnType R_CAN_TrmByTRFIFO0_CH0 (const can_frame_t * pFrame)
Read channel status (C0STSL)

RS-CAN Configurator API List - Receive

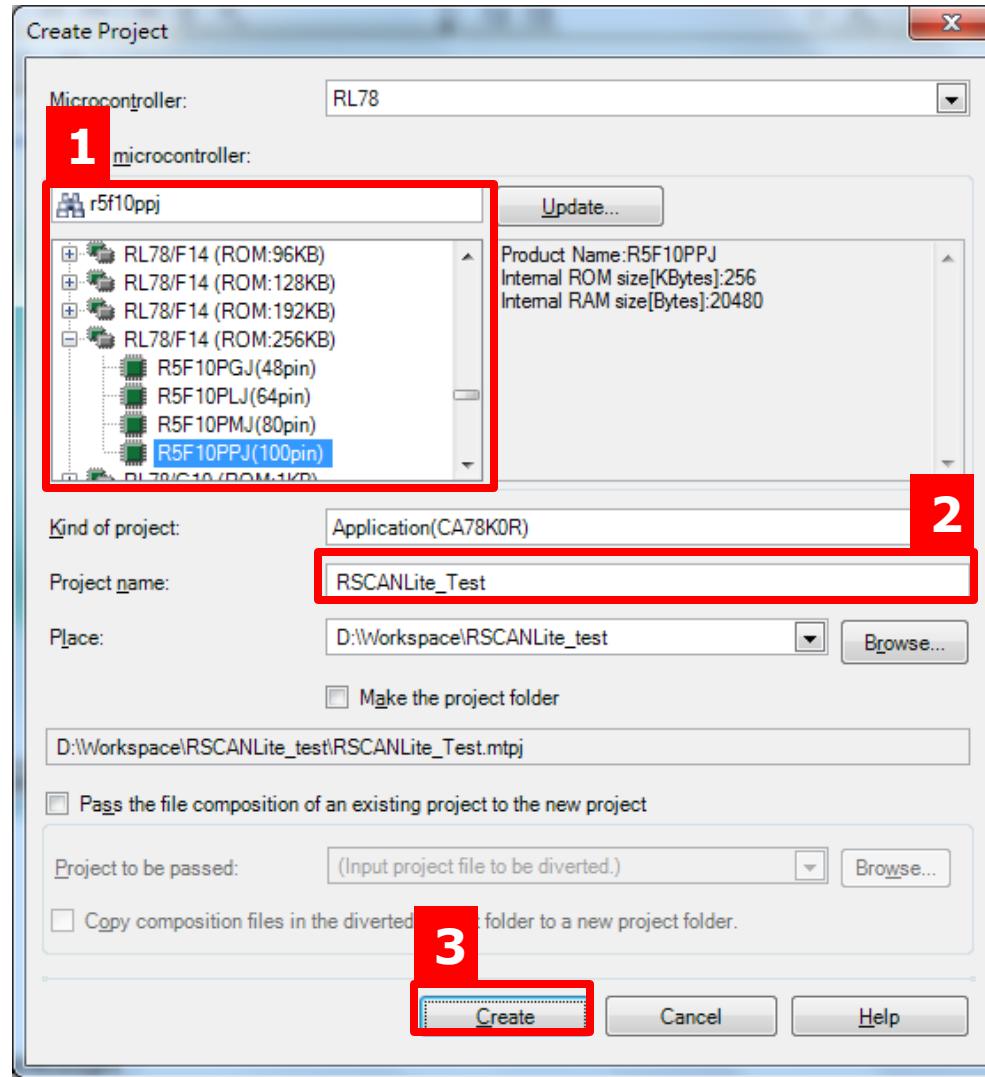
Function / Description
Can_RtnType R_CAN_ReadRxBuffer (uint8_t * p_rxbuf_idx, can_frame_t * pFrame) Read message from Rx buffer
Can_RtnType R_CAN_ReadRxFIFO (can_rxfifo_t rxfifo_idx, can_frame_t * pFrame) Read message from Rx FIFO
Can_RtnType R_CAN_ReadTRFIFO0_CH0 (can_frame_t * pFrame) Read message from common (Tx/Rx) FIFO

Create a new project by using CS+/CG

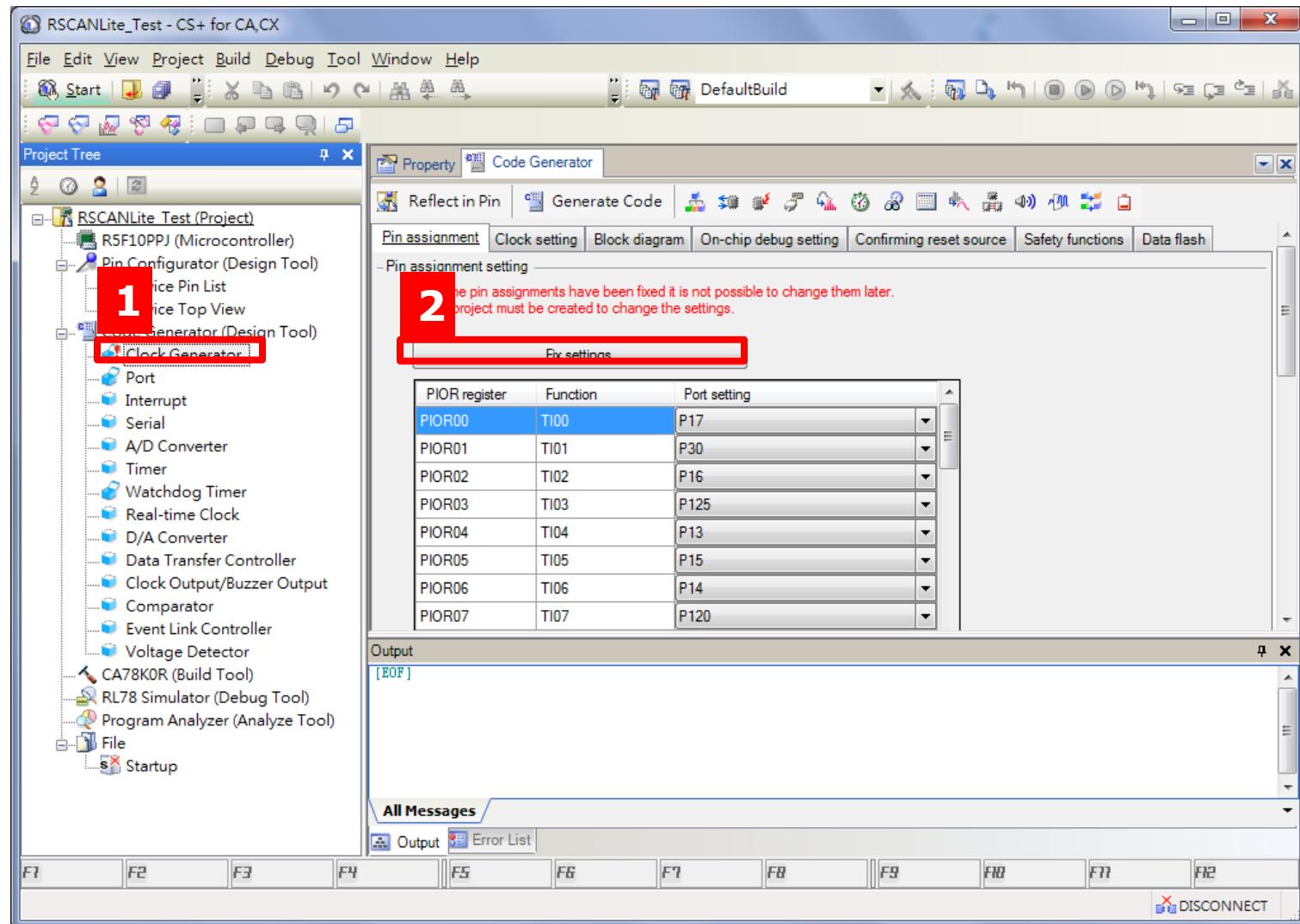
Create a new project by using CS+/CG



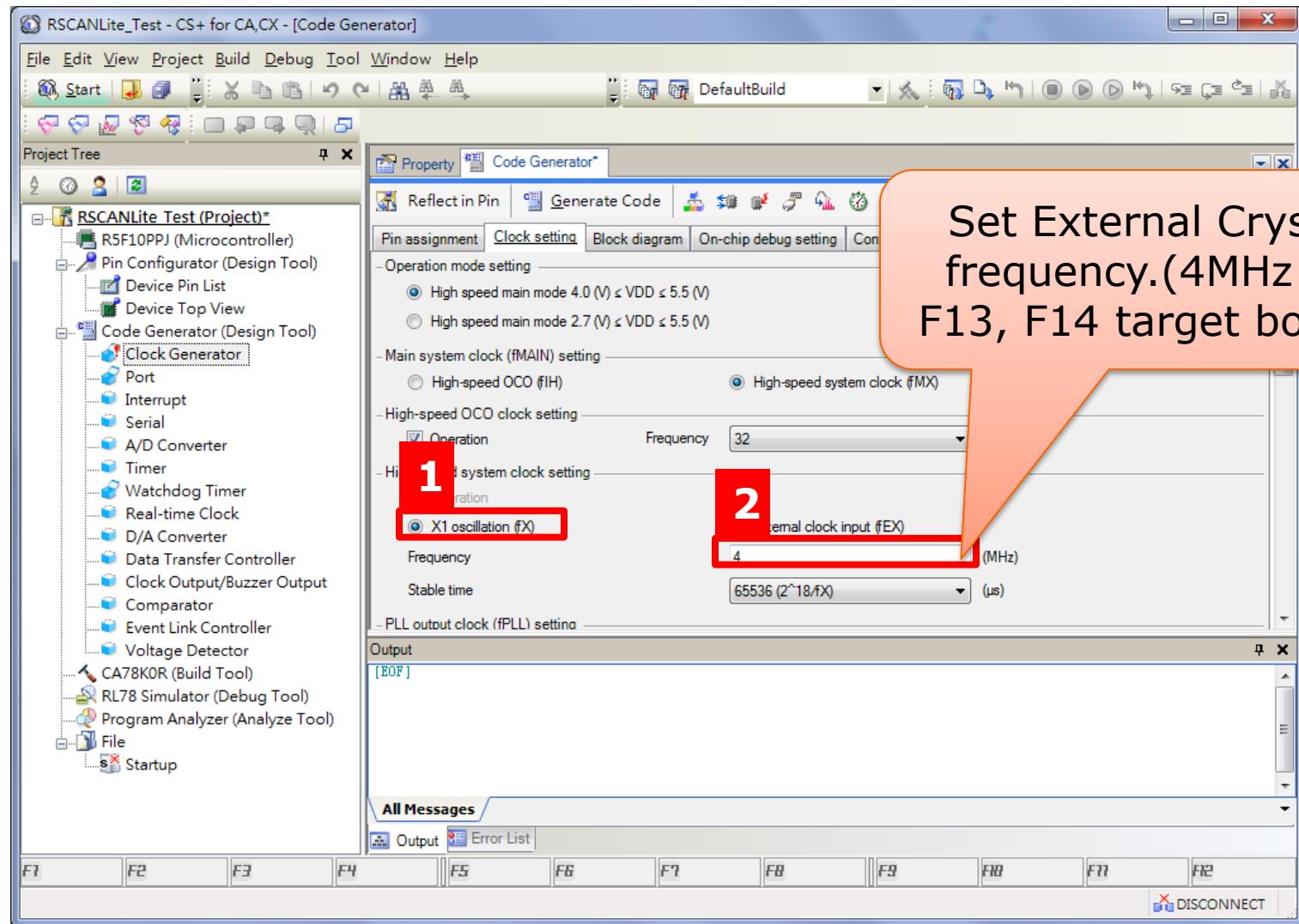
Create a new project by using CS+/CG



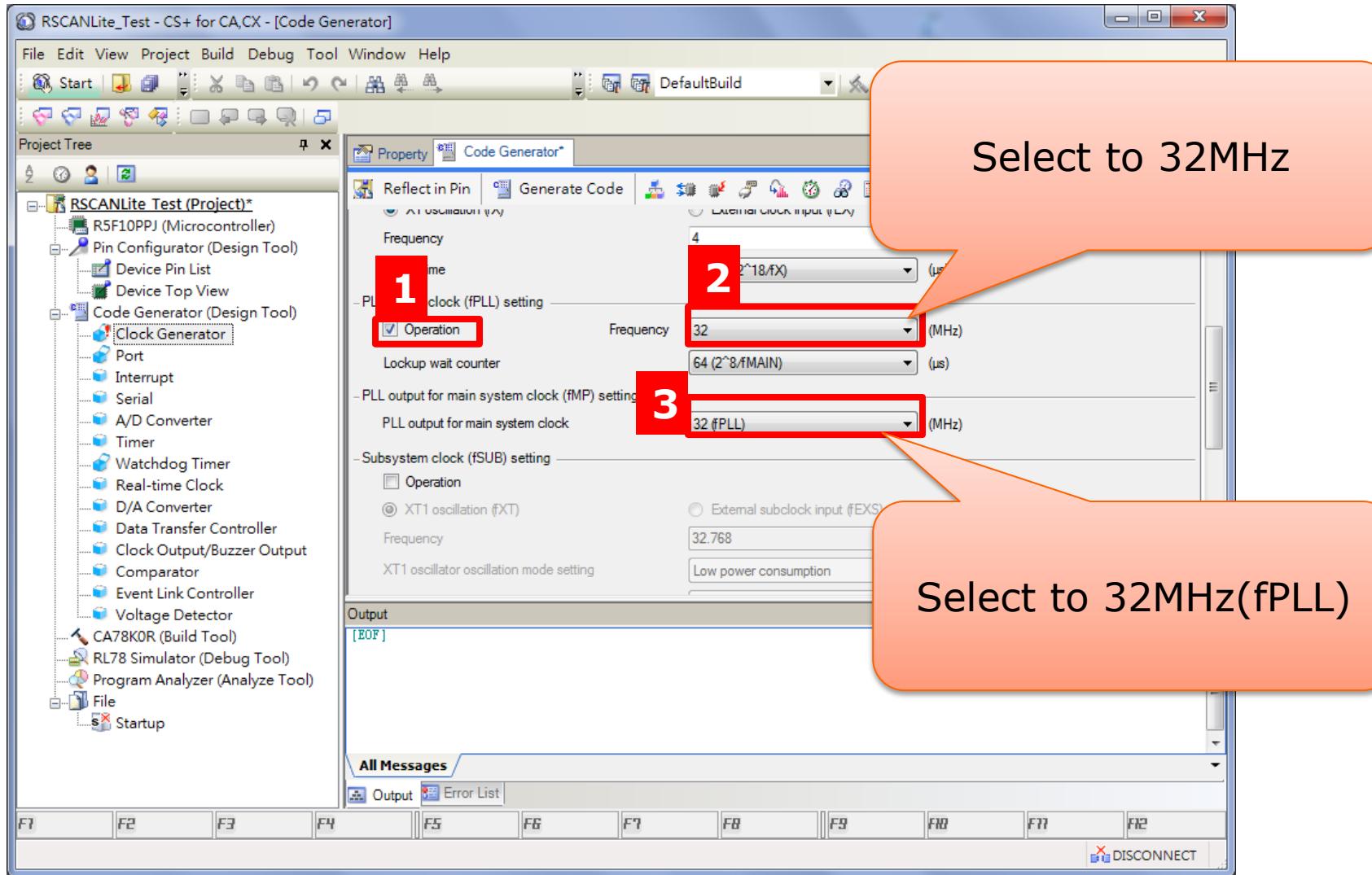
Create a new project by using CS+/CG



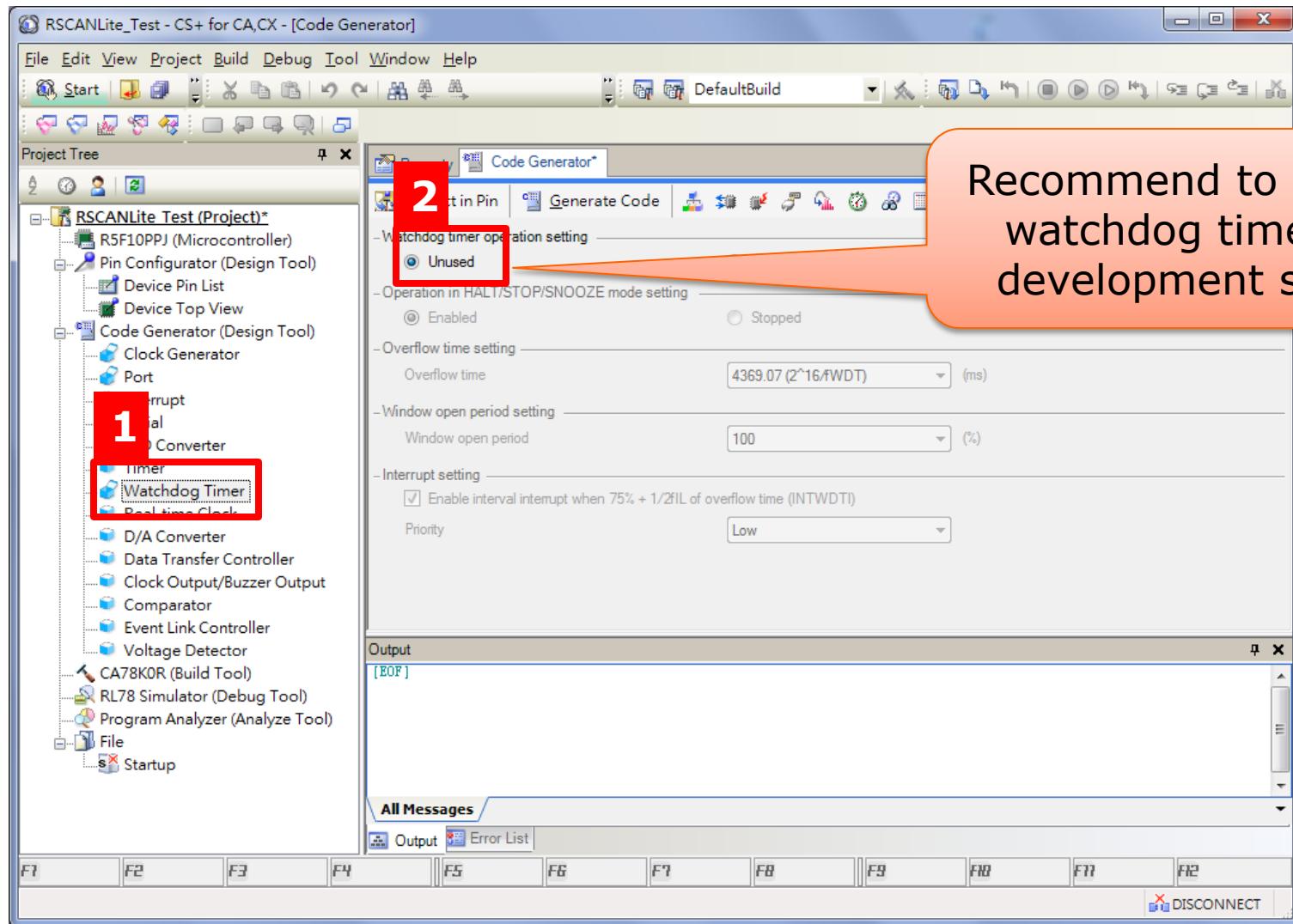
Create a new project by using CS+/CG



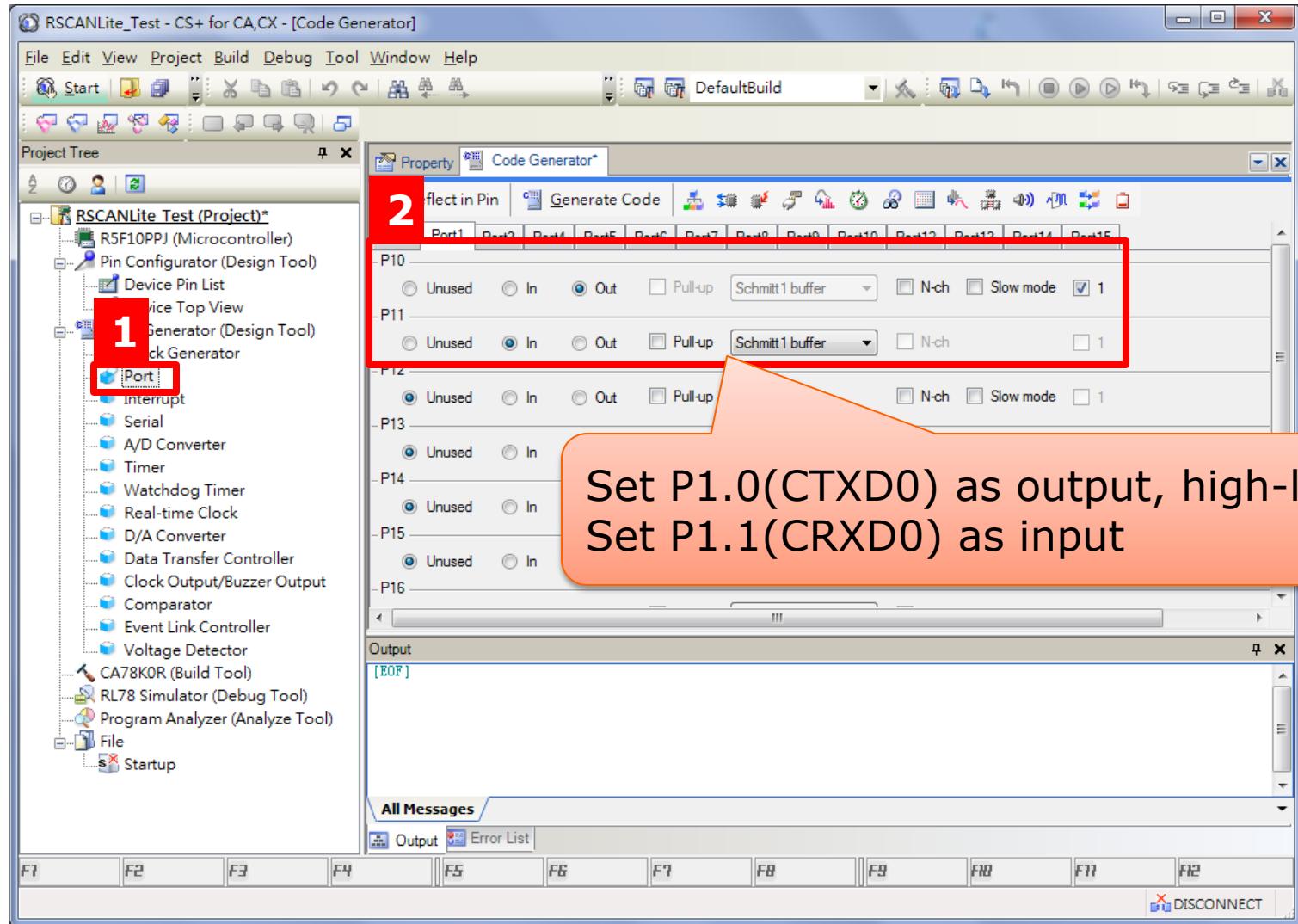
Create a new project by using CS+/CG



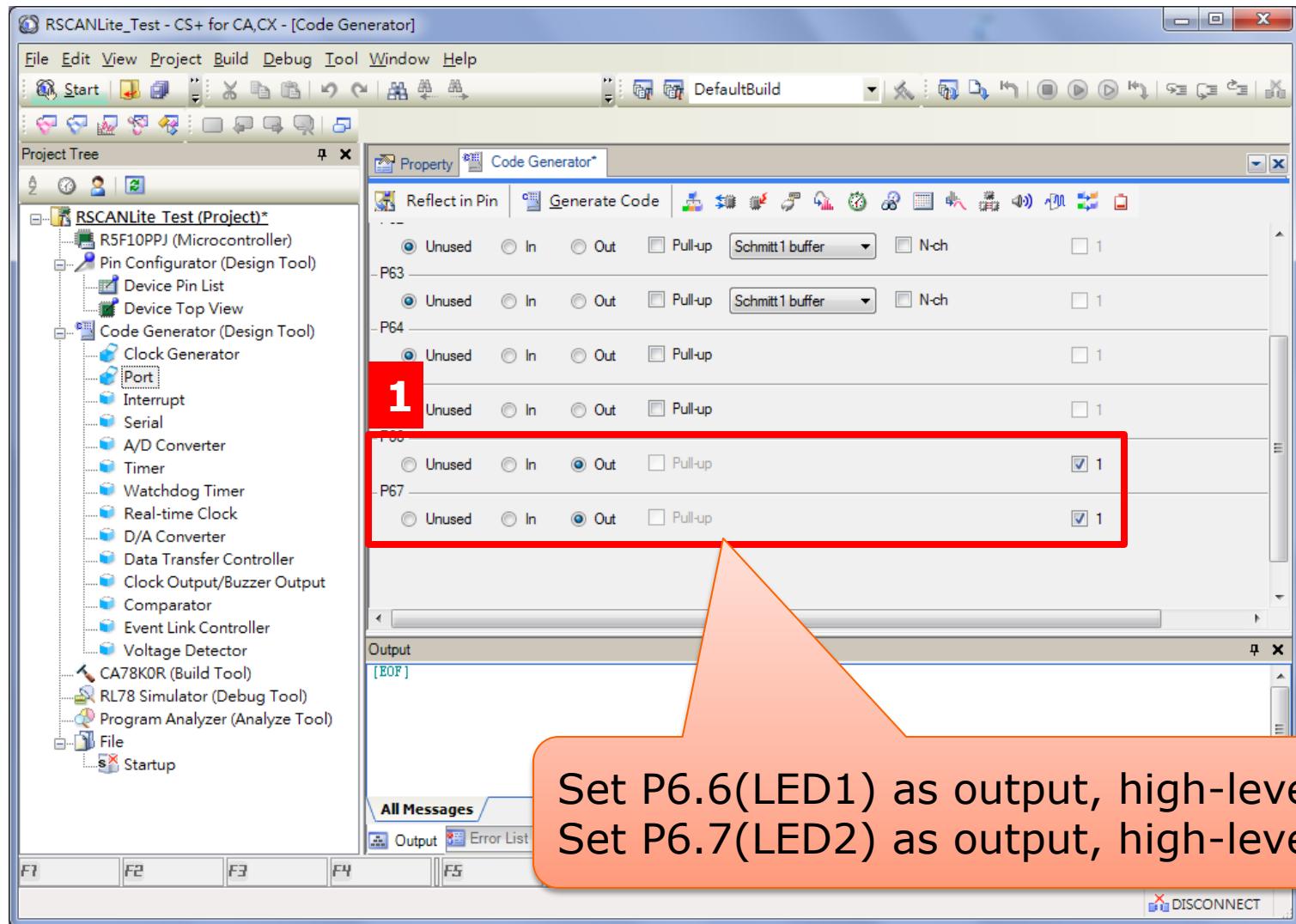
Create a new project by using CS+/CG



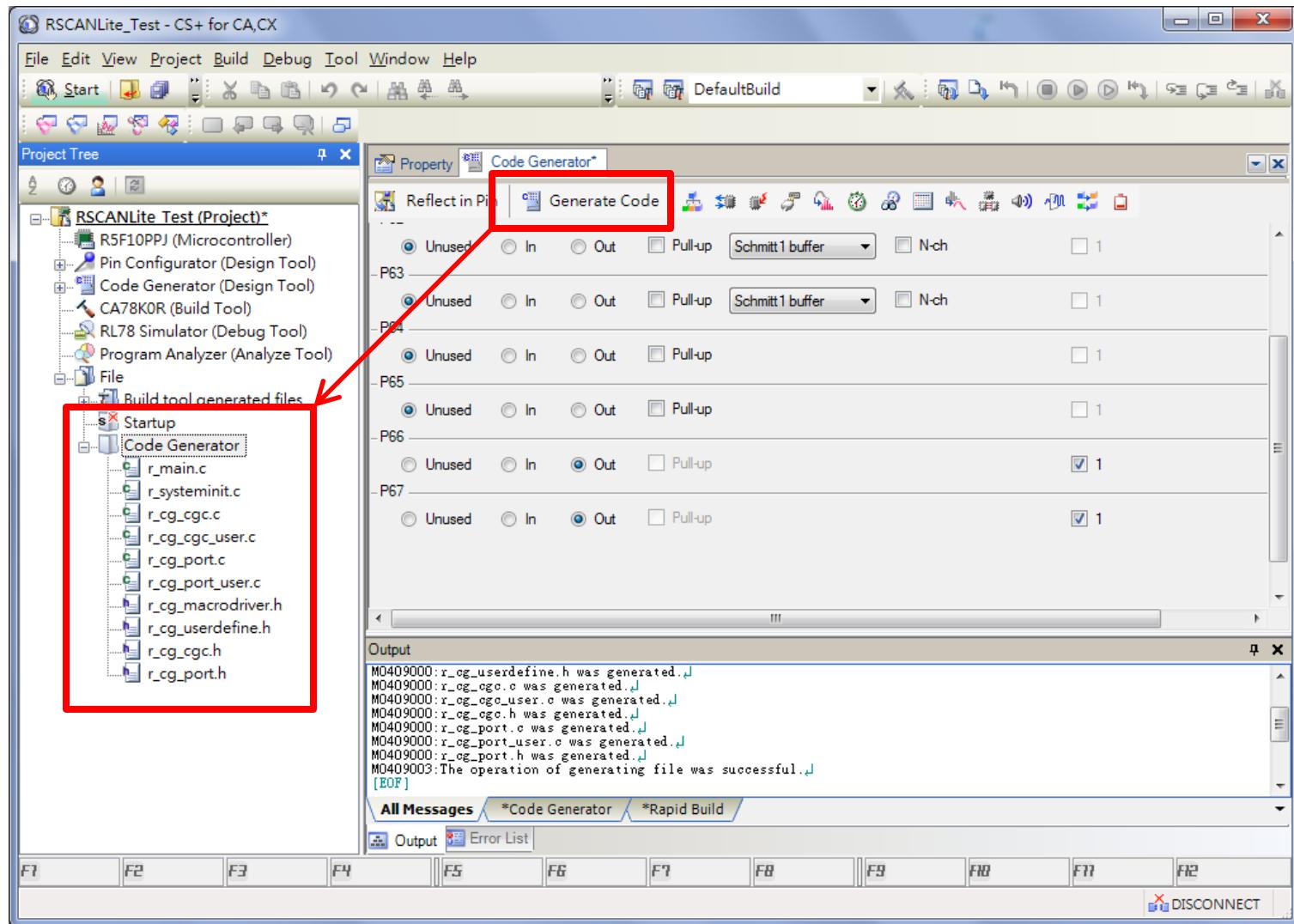
Create a new project by using CS+/CG



Create a new project by using CS+/CG

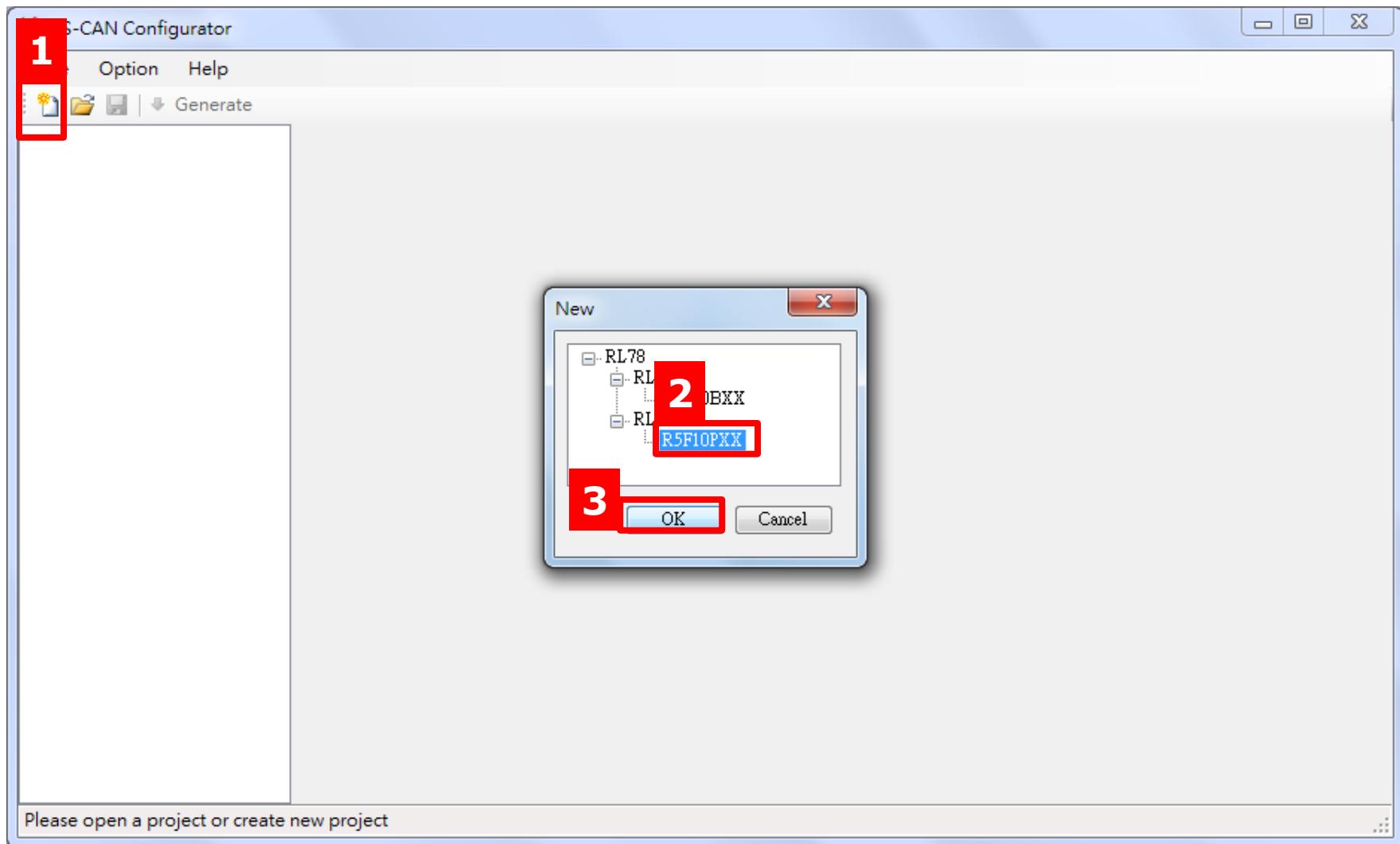


Create a new project by using CS+/CG

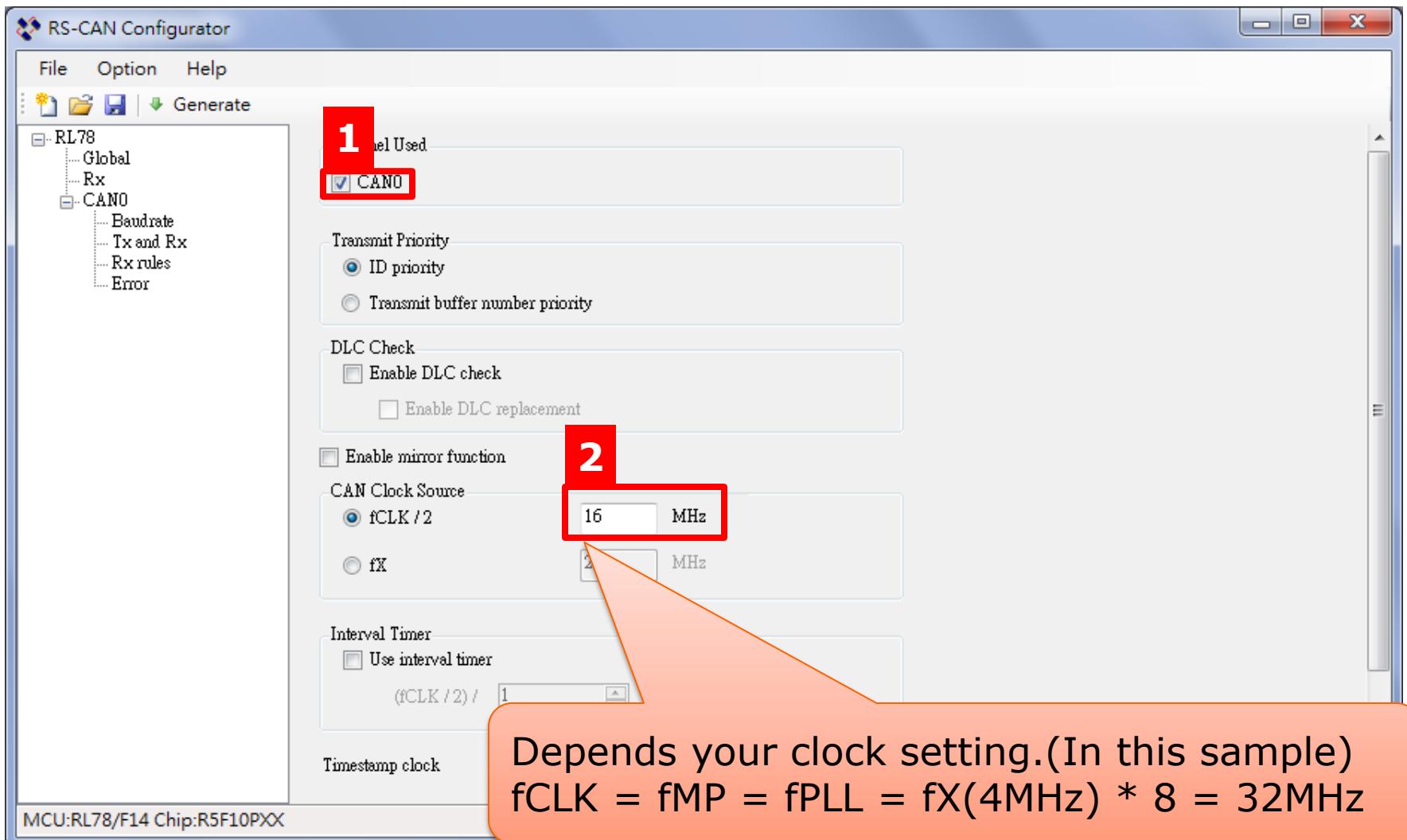


Create a RS-CAN API by RS-CAN Configurator

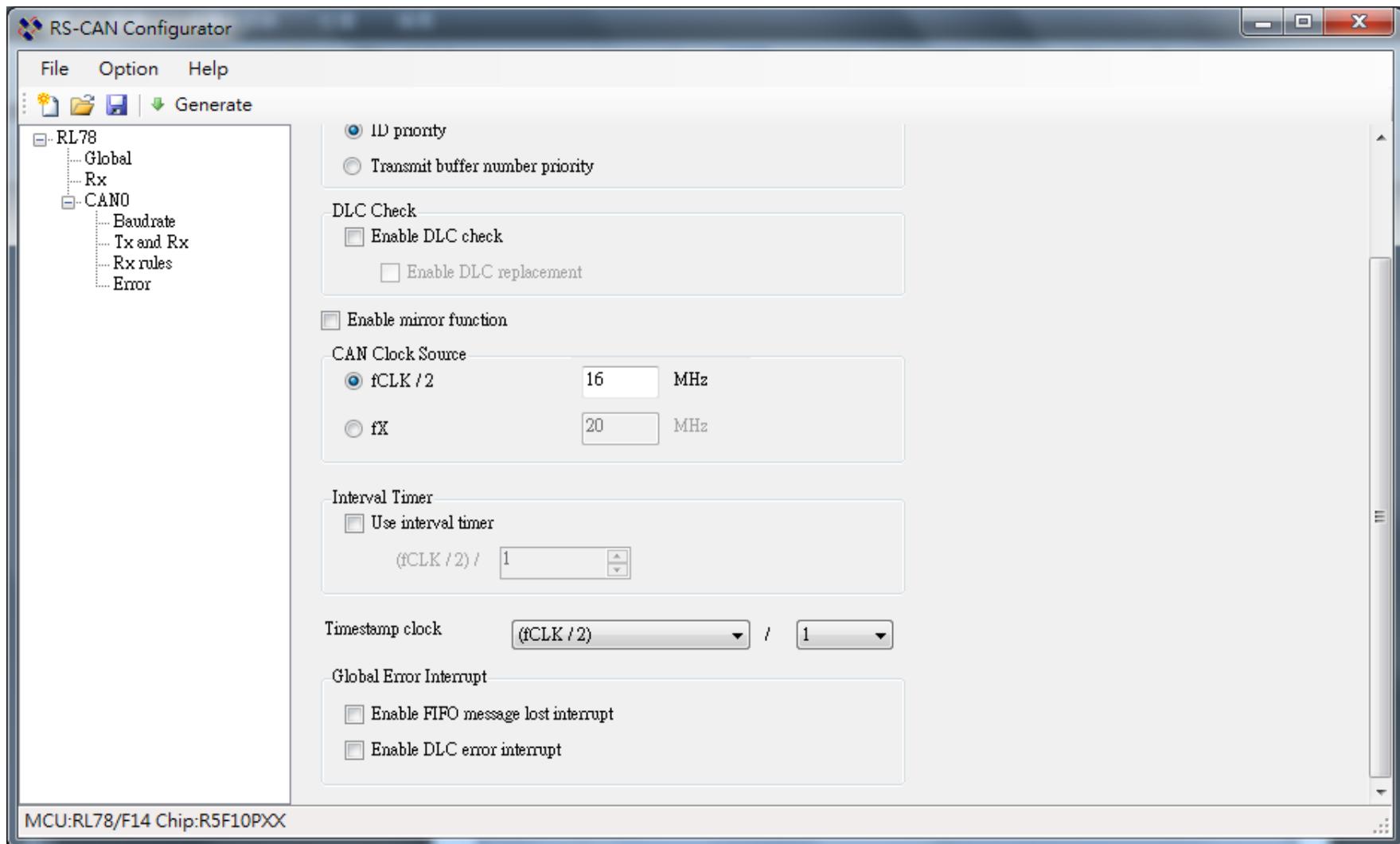
Create a RS-CAN API by RS-CAN Configurator



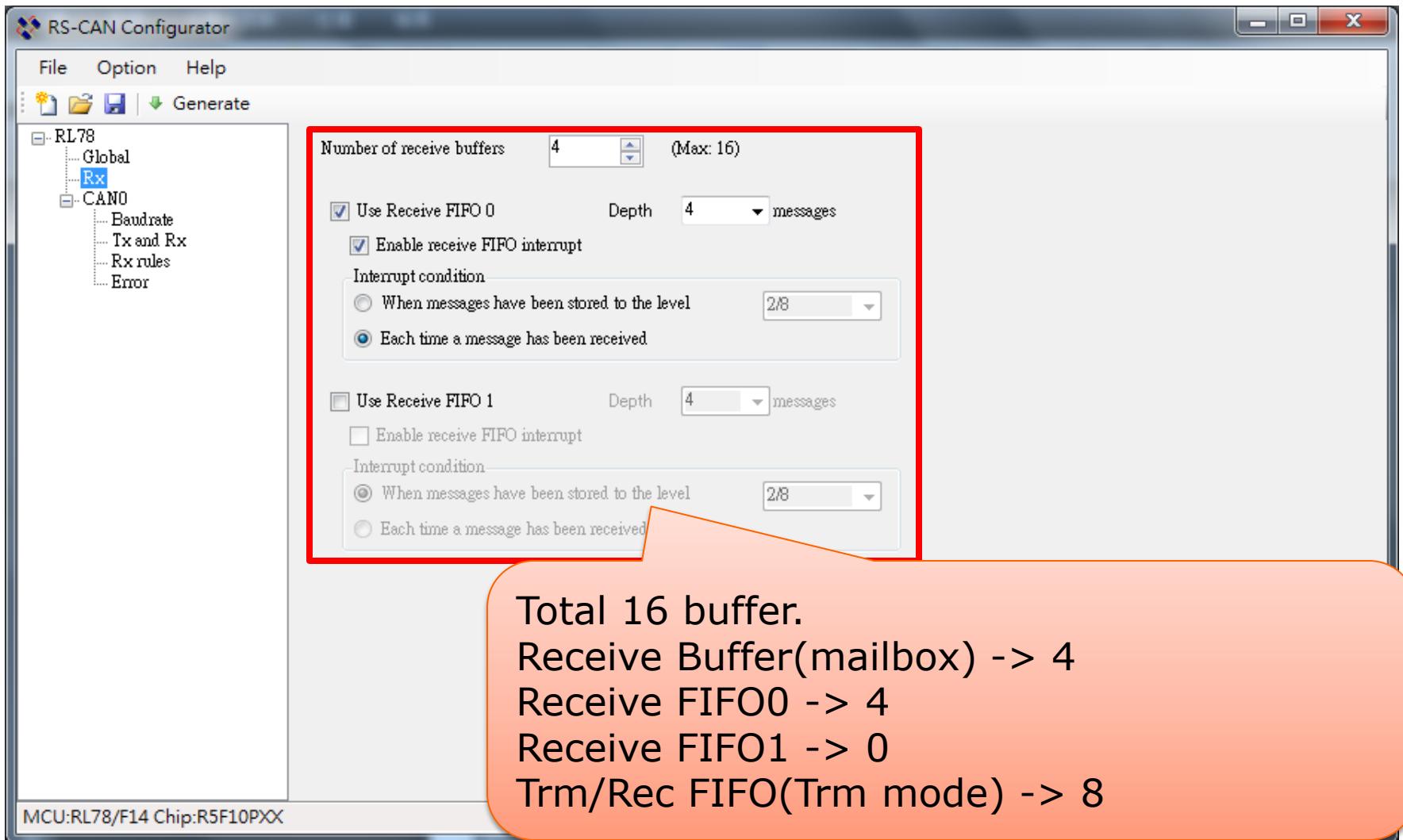
Create a RS-CAN API by RS-CAN Configurator



Create a RS-CAN API by RS-CAN Configurator



Create a RS-CAN API by RS-CAN Configurator



Create a RS-CAN API by RS-CAN Configurator

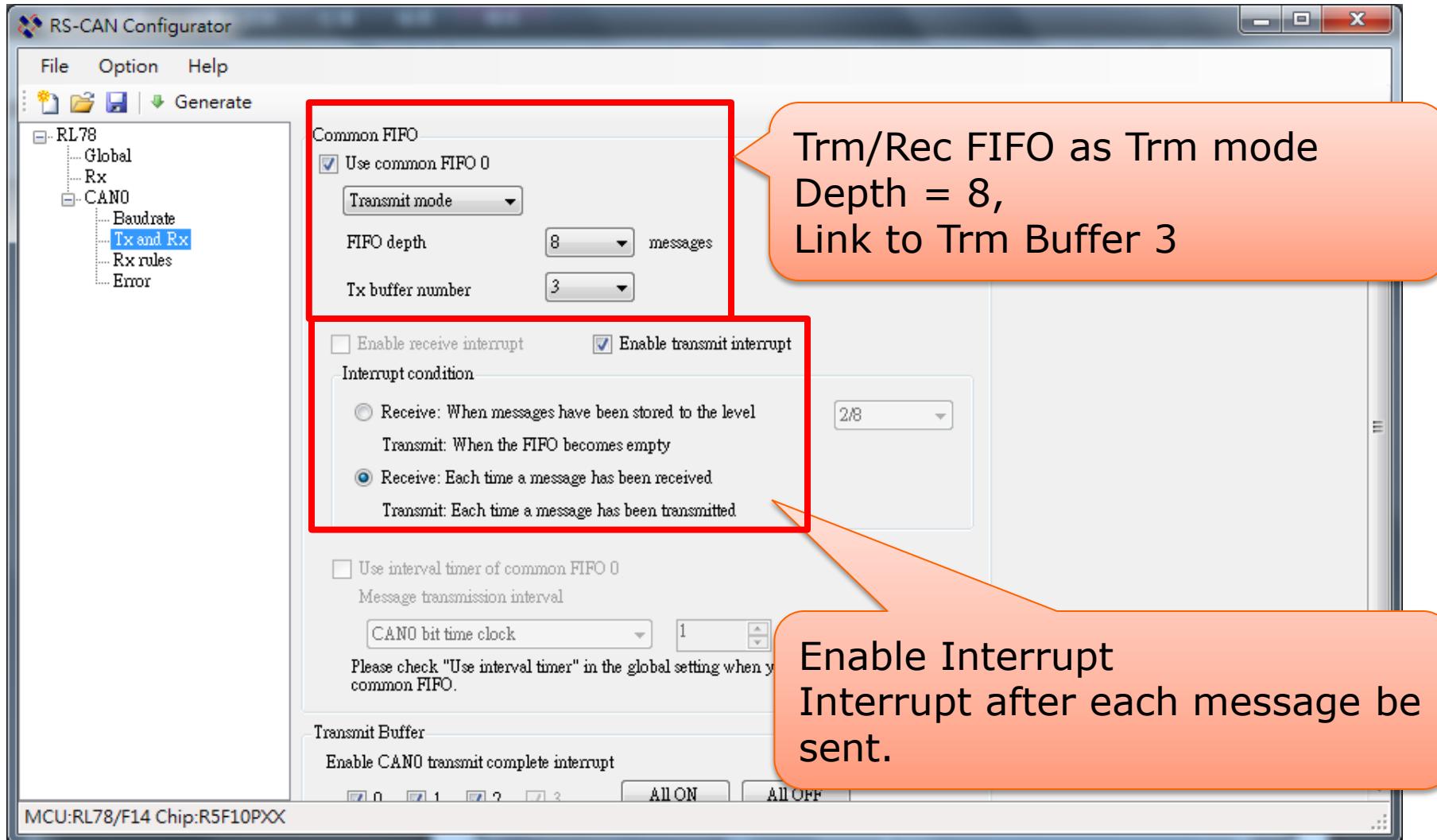
The screenshot shows the RS-CAN Configurator interface for an RL78/F14 chip. The left sidebar shows a tree view with nodes: Global, Rx, CAN0 (selected), Baudrate, Tx and Rx, Rx rules, and Error. The main area has two dropdown menus: 'CAN clock' set to 16 MHz and 'Baud rate' set to 250 bps. A red box highlights the 'Baud rate' dropdown. Below these are two tables. The first table lists Prescaler values (4, 8, 16, 32) with corresponding DBT, SS, TSEG1, TSEG2, SP(%), and SJW values. The second table lists the same parameters for different combinations of Prescaler and DBT values. An orange callout bubble points to the bottom row of the second table, which corresponds to a Prescaler of 16, DBT of 1, SS of 11, TSEG1 of 4, TSEG2 of 4, SP(%) of 75.0, and SJW of 4. This row is highlighted with a blue background and a red border.

Prescaler	DBT	SS	TSEG1	TSEG2	SP(%)	SJW
4	16	1	8	7	56.3	1
4	16	1	8	7	56.3	2
4	16	1	8	7	56.3	3
4	16	1	8	7	56.3	4
4	16	1	9	6	62.5	1
4	16	1	9	6	62.5	2
4	16	1	9	6	62.5	3
4	16	1	9	6	62.5	4
4	16	1	10	5	68.8	1
4	16	1	10	5	68.8	2
4	16	1	10	5	68.8	3
4	16	1	10	5	68.8	4
4	16	1	11	4	75.0	1
4	16	1	11	4	75.0	2
4	16	1	11	4	75.0	3
16	16	1	11	4	75.0	4
4	16	1	12	3	81.3	1
4	16	1	12	3	81.3	2
4	16	1	12	3	81.3	3
4	16	1	13	2	87.5	1
4	16	1	13	2	87.5	2
8	8	1	4	3	62.5	1
0	0	1	4	3	62.5	0

MCU:RL78/F14 Chip:R5F10PXX

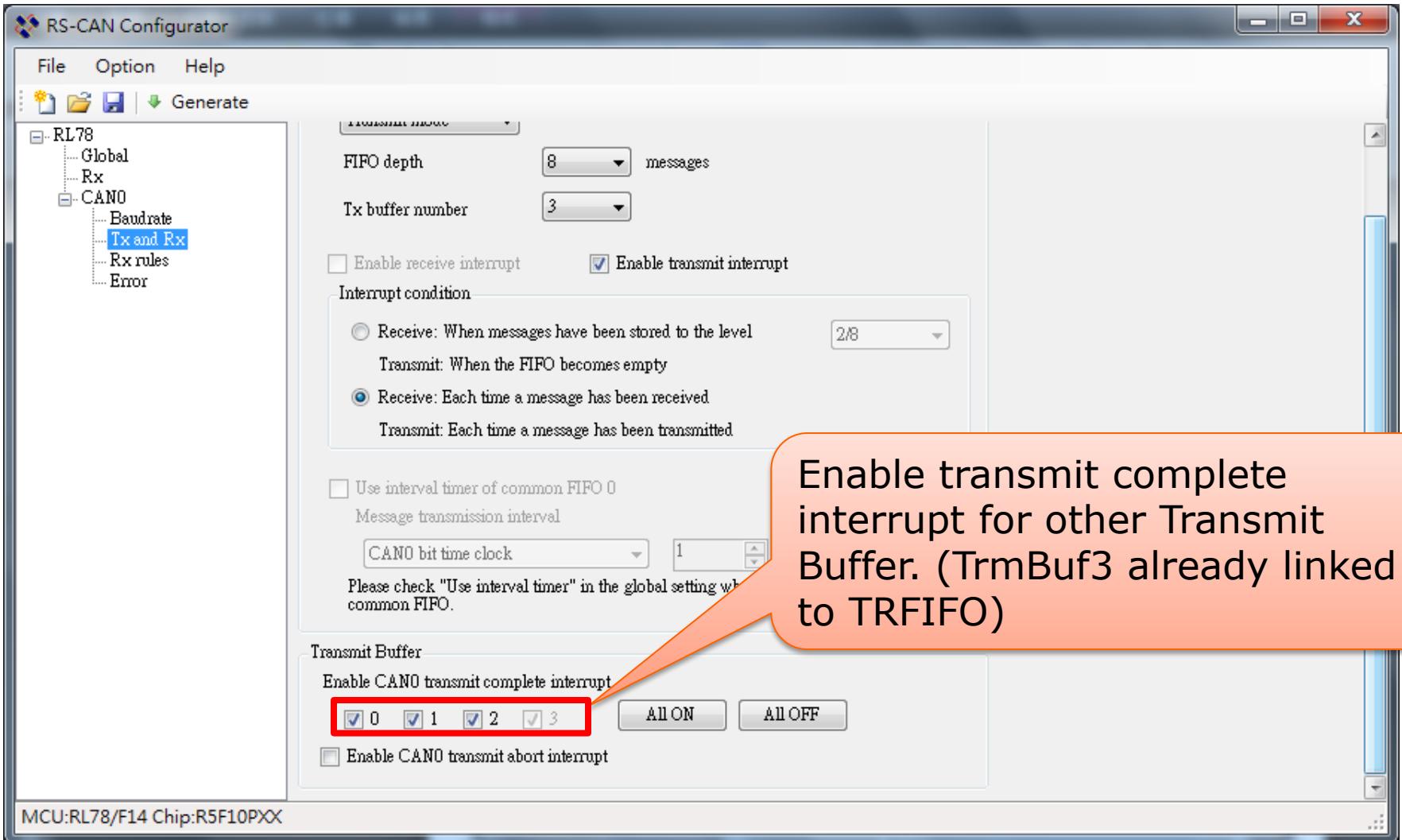
250kBPS
75%

Create a RS-CAN API by RS-CAN Configurator



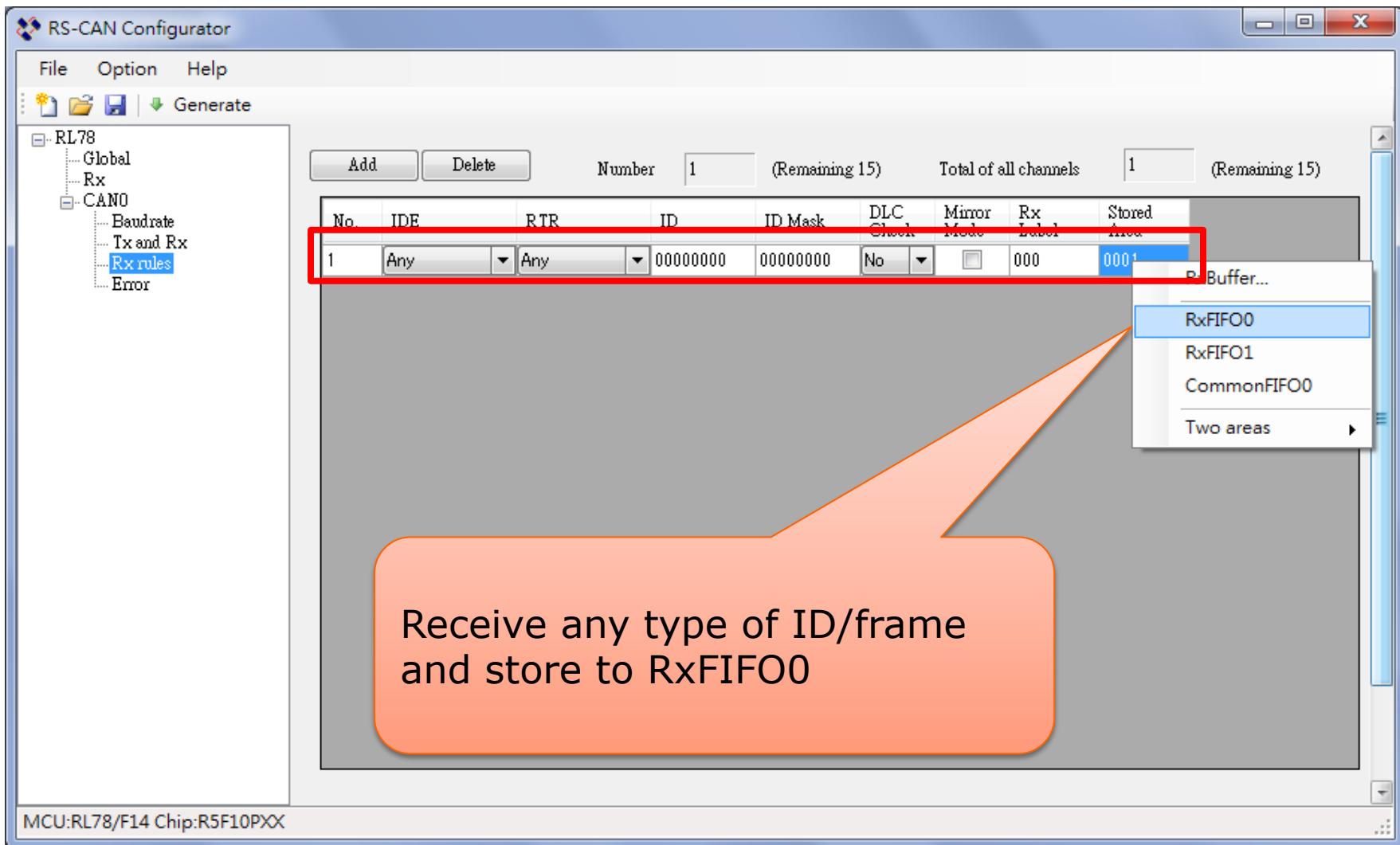
MCU:RL78/F14 Chip:R5F10PXX

Create a RS-CAN API by RS-CAN Configurator



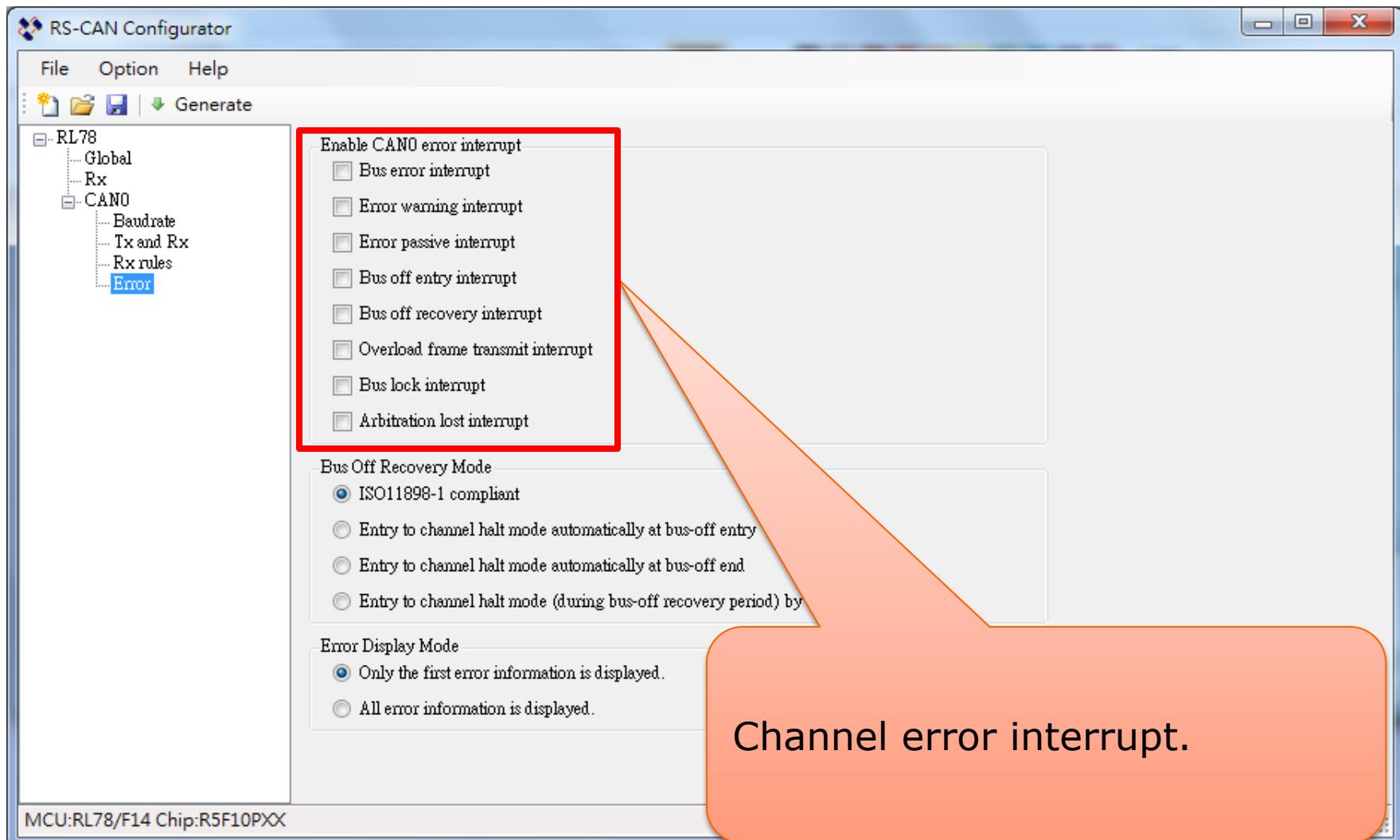
Enable transmit complete
interrupt for other Transmit
Buffer. (TrmBuf3 already linked
to TRFIFO)

Create a RS-CAN API by RS-CAN Configurator

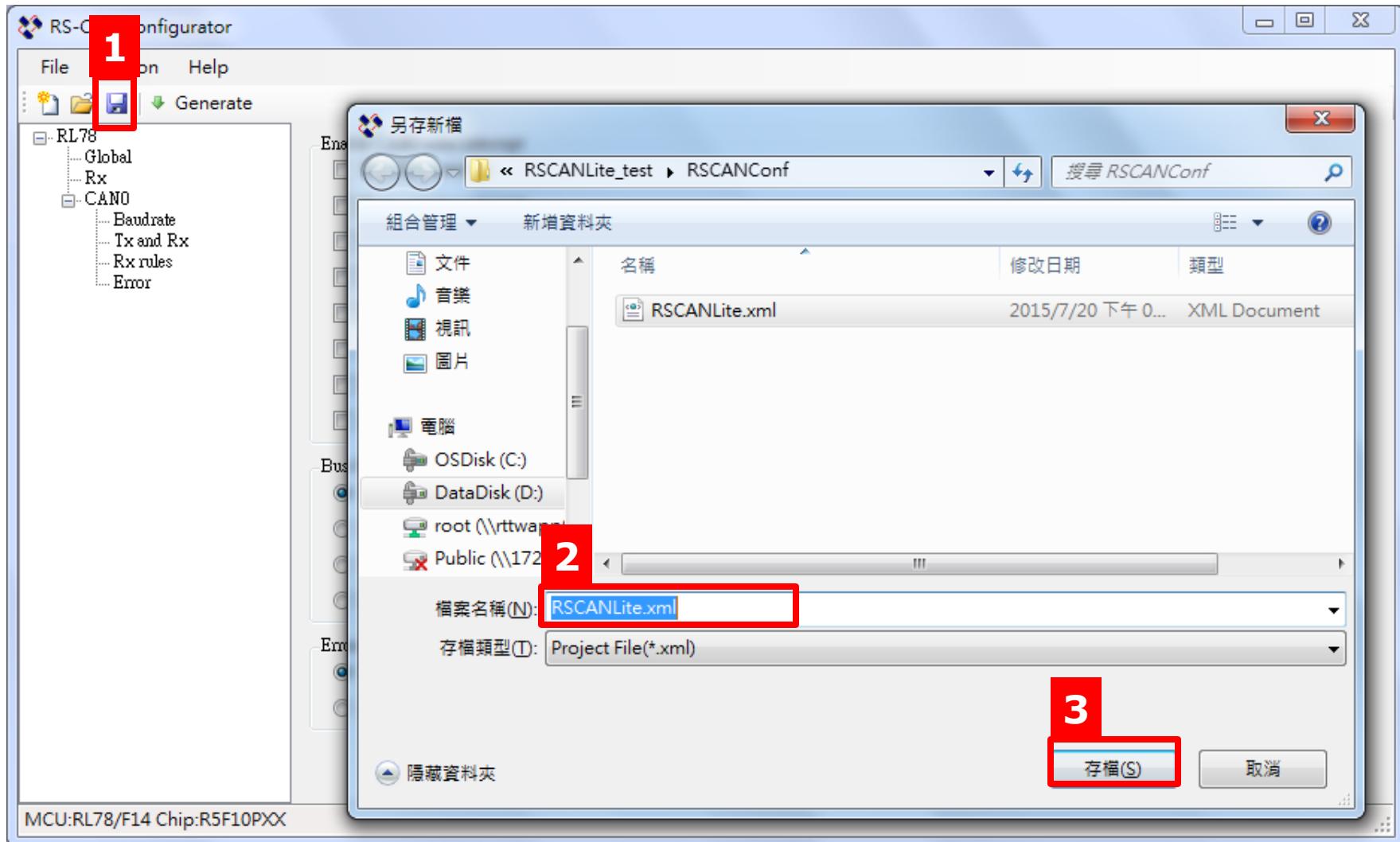


MCU:RL78/F14 Chip:R5F10PXX

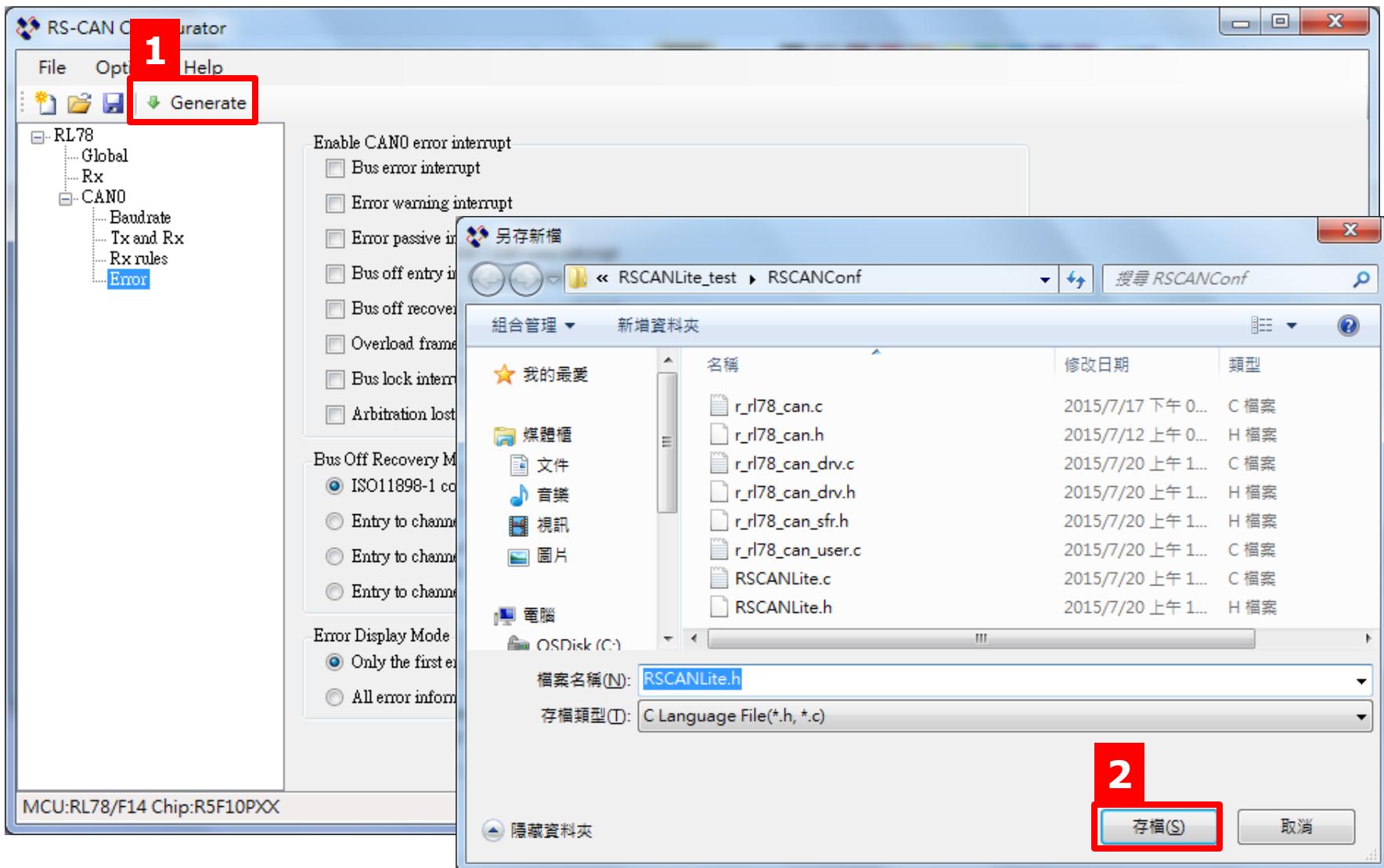
Create a RS-CAN API by RS-CAN Configurator



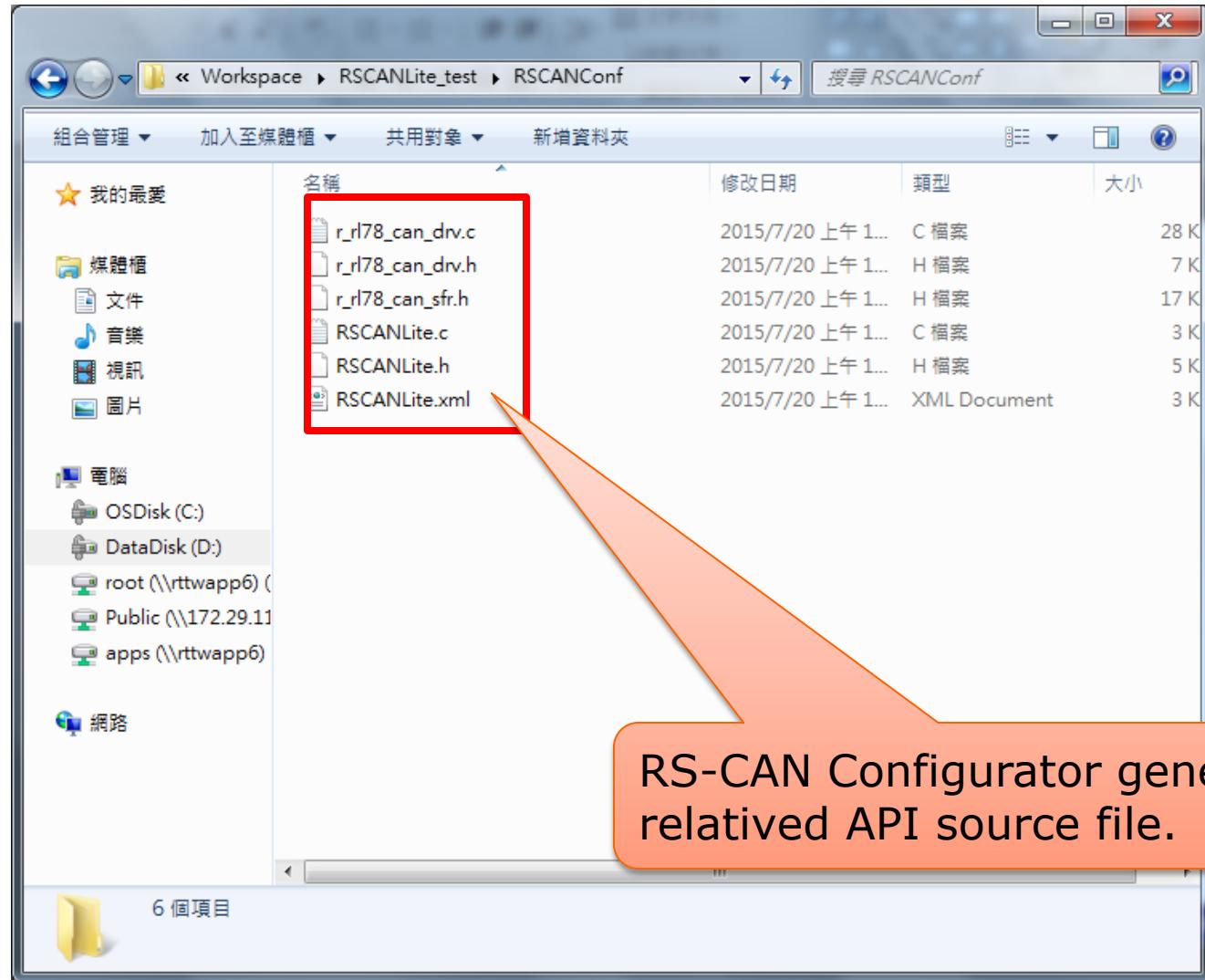
Create a RS-CAN API by RS-CAN Configurator



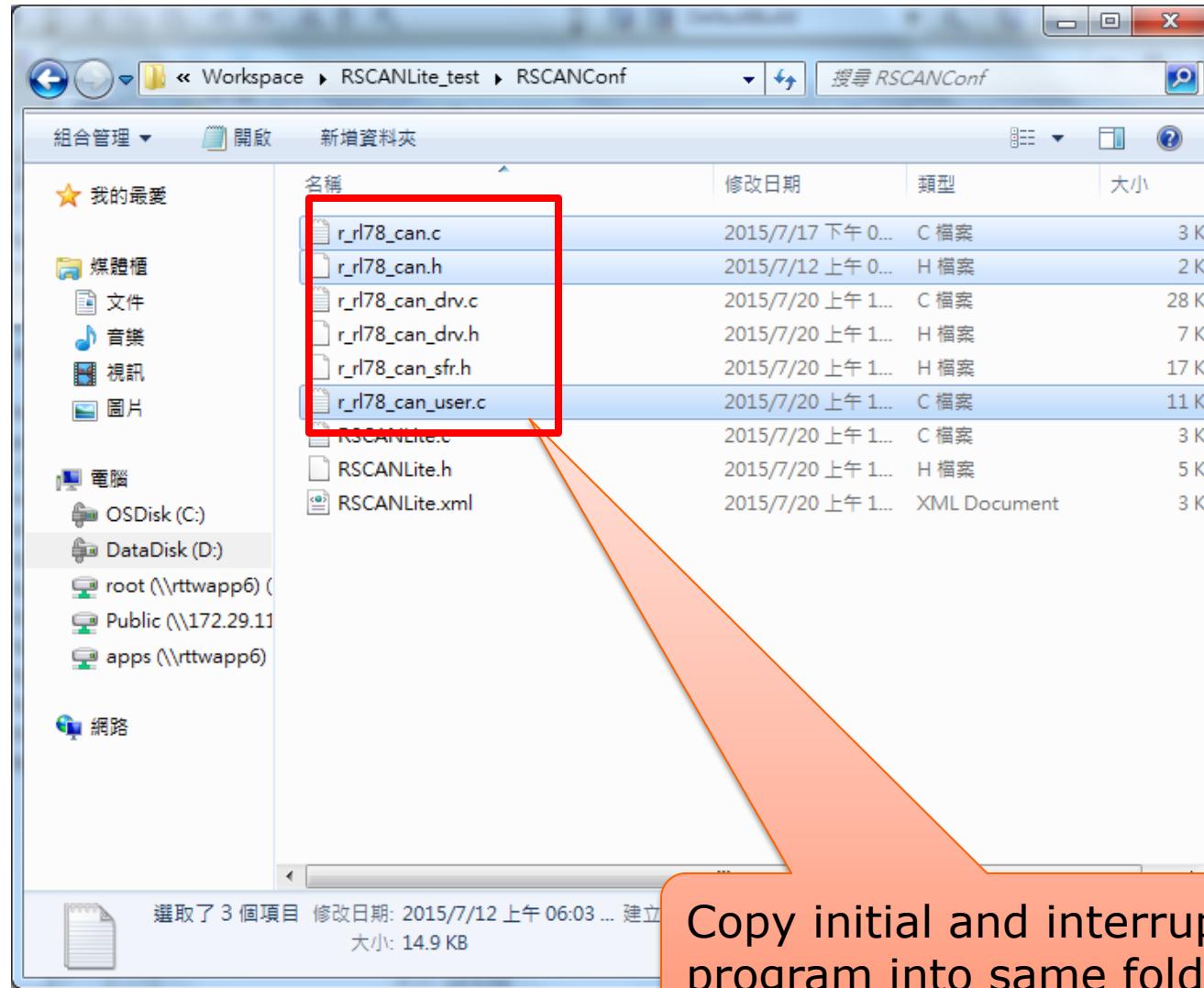
Create a RS-CAN API by RS-CAN Configurator



Create a RS-CAN API by RS-CAN Configurator

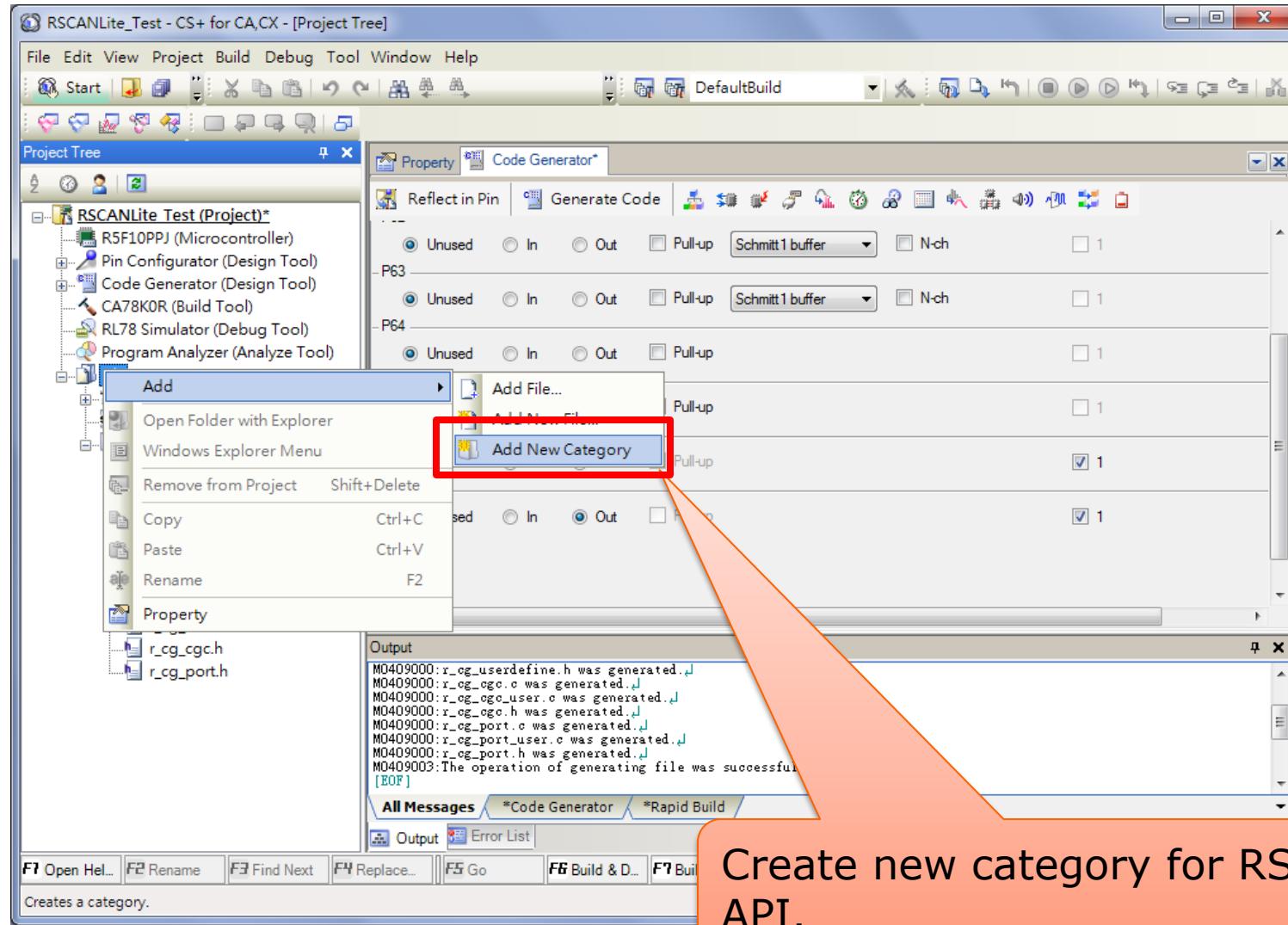


Create a RS-CAN API by RS-CAN Configurator



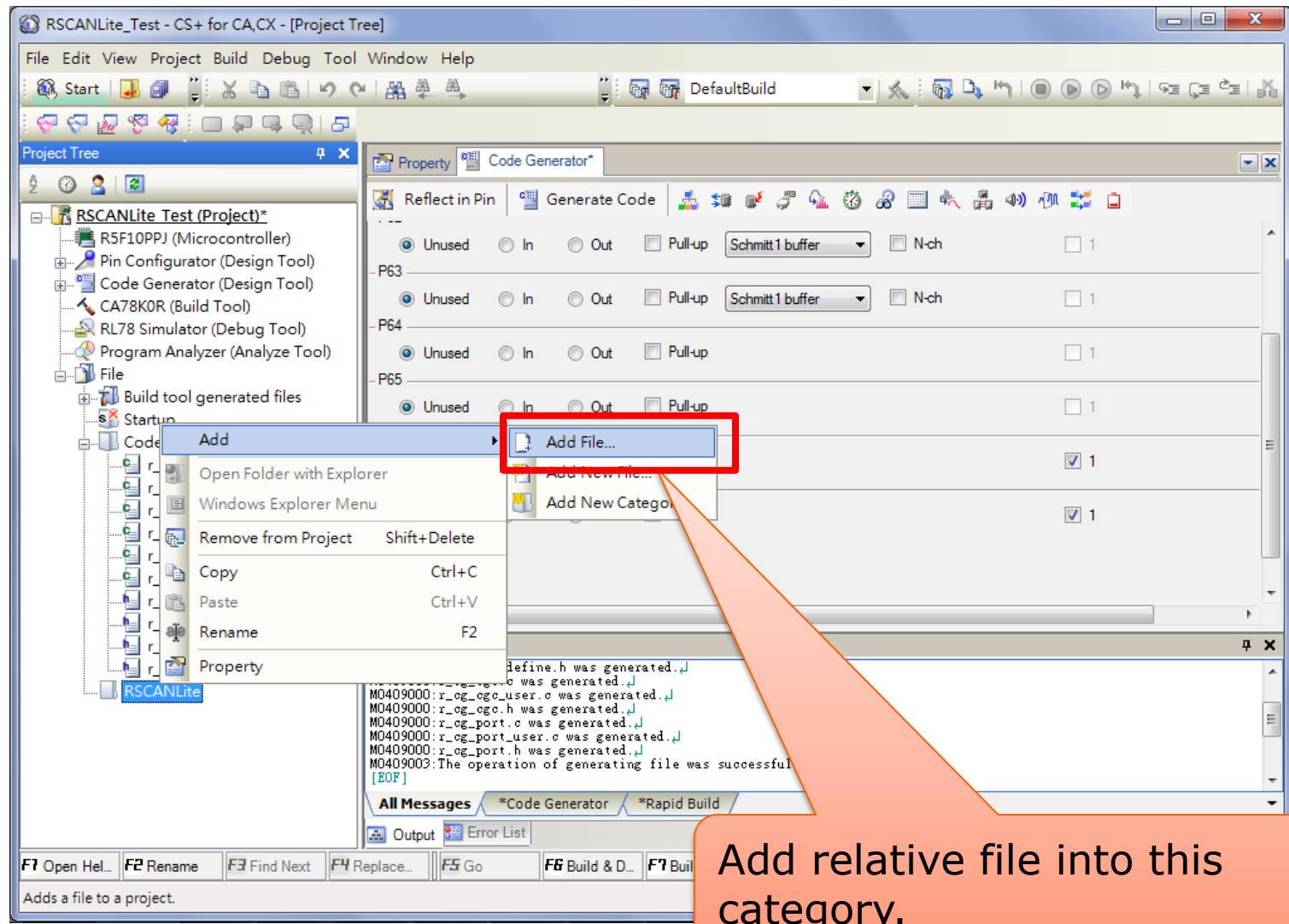
Integrated RS-CAN API into CS+ project

Integrated RS-CAN API into CS+ project

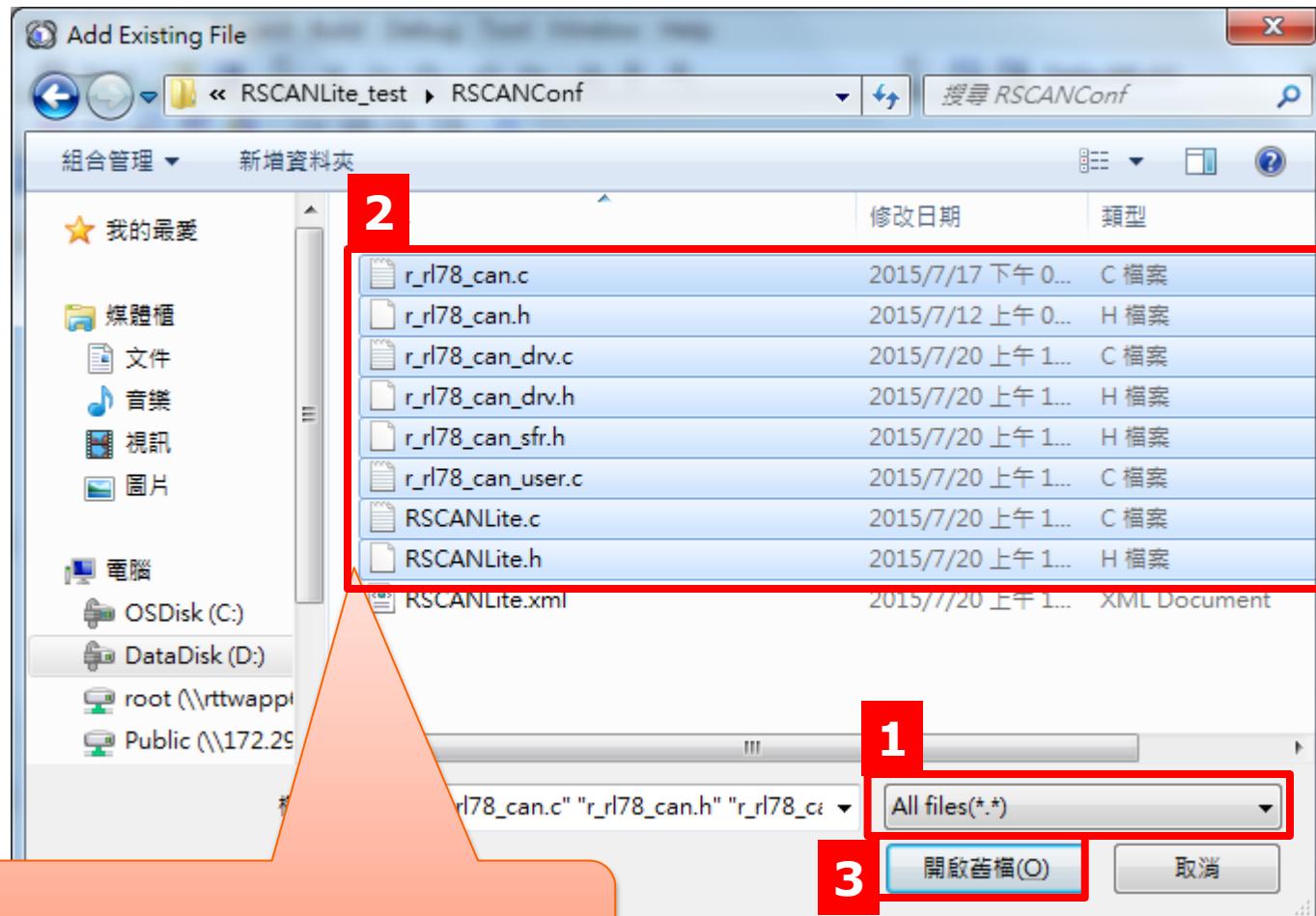


Create new category for RS-CAN API.

Integrated RS-CAN API into CS+ project



Integrated RS-CAN API into CS+ project



Add all .c and .h files.

Integrated RS-CAN API into CS+ project

The screenshot shows the Renesas CS+ IDE interface. The title bar reads "RSCANLite_Test - CS+ for CA,CX - [r_main.c]". The menu bar includes File, Edit, View, Project, Build, Debug, Tool, Window, Help. The toolbar has various icons for file operations. The "DefaultBuild" button is highlighted. The "Property" tab is selected in the top right of the main window. The "Code Generator" tab is also visible. The "r_main.c" file is open in the code editor. A red box highlights the file tab. Another red box highlights the code in the editor. The code editor shows the following snippet:

```
31  ****
32  /* Start user code for pragma. Do not edit comment generated here */
33  /* End user code. Do not edit comment generated here */
34
35  ****
36  Includes
37  ****
38  #include "r_cg_macrodriver.h"
39  #include "r_cg_cgc.h"
40  #include "r_cg_port.h"
41  /* Start user code for include. Do not edit comment generated here */
42  #include "r_rl78_can_drv.h"          /* Header file for CAN software driver
43  #include "r_rl78_can.h"
44  /* End user code. Do not edit comment generated here */
45  #include "r_cg_userdefine.h"
46
47  ****
48  Global variables and functions
        !!!
```

The status bar at the bottom shows "Line 46/86 Column 1 Insert 繁體中文 (Big5) DISCONNECT".

An orange callout bubble in the bottom left corner points to the code in the editor and contains the text: "Add "r_rl78_can_drv.h" & "r_rl78_can.h" into "r_main.c"

Integrated RS-CAN API into CS+ project

The screenshot shows the Renesas CS+ IDE interface with the project "RSCANLite_Test - CS+ for CA,CX - [r_main.c]" open. The Project Tree on the left lists various files and tool components. The main window displays the code editor for "r_main.c". A red box highlights the following code block:

```
void R_MAIN_UserInit(void)
{
    /* Start user code. Do not edit comment generated here */
    R_CAN_Create();
    EI();
    /* End user code. Do not edit comment generated here */
}
```

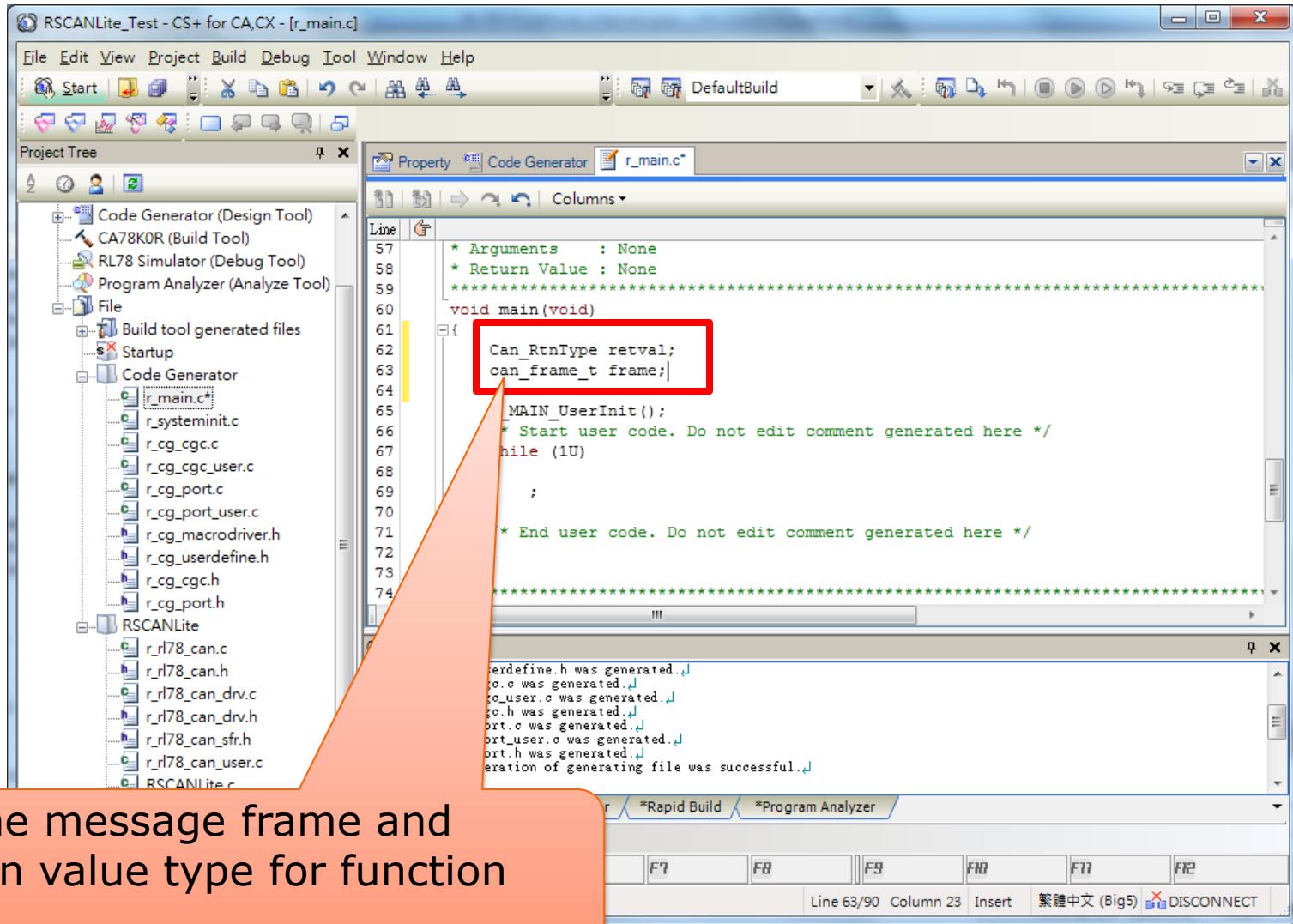
An orange callout points from this highlighted code to the following explanatory text:

Call R_CAN_Create() function to initial CAN module.

The Output window at the bottom shows build logs:

```
rdefine.h was generated.  
c was generated.  
user.c was generated.  
o.h was generated.  
rt.c was generated.  
rt_user.c was generated.  
rt.h was generated.  
eration of generating file was successful.
```

Integrated RS-CAN API into CS+ project



```
* Arguments      : None
* Return Value   : None
*****
void main(void)
{
    Can_RtnType retval;
    can_frame_t frame;

    _MAIN_UserInit();
    * Start user code. Do not edit comment generated here */
    while (1U)

    ;

    * End user code. Do not edit comment generated here */

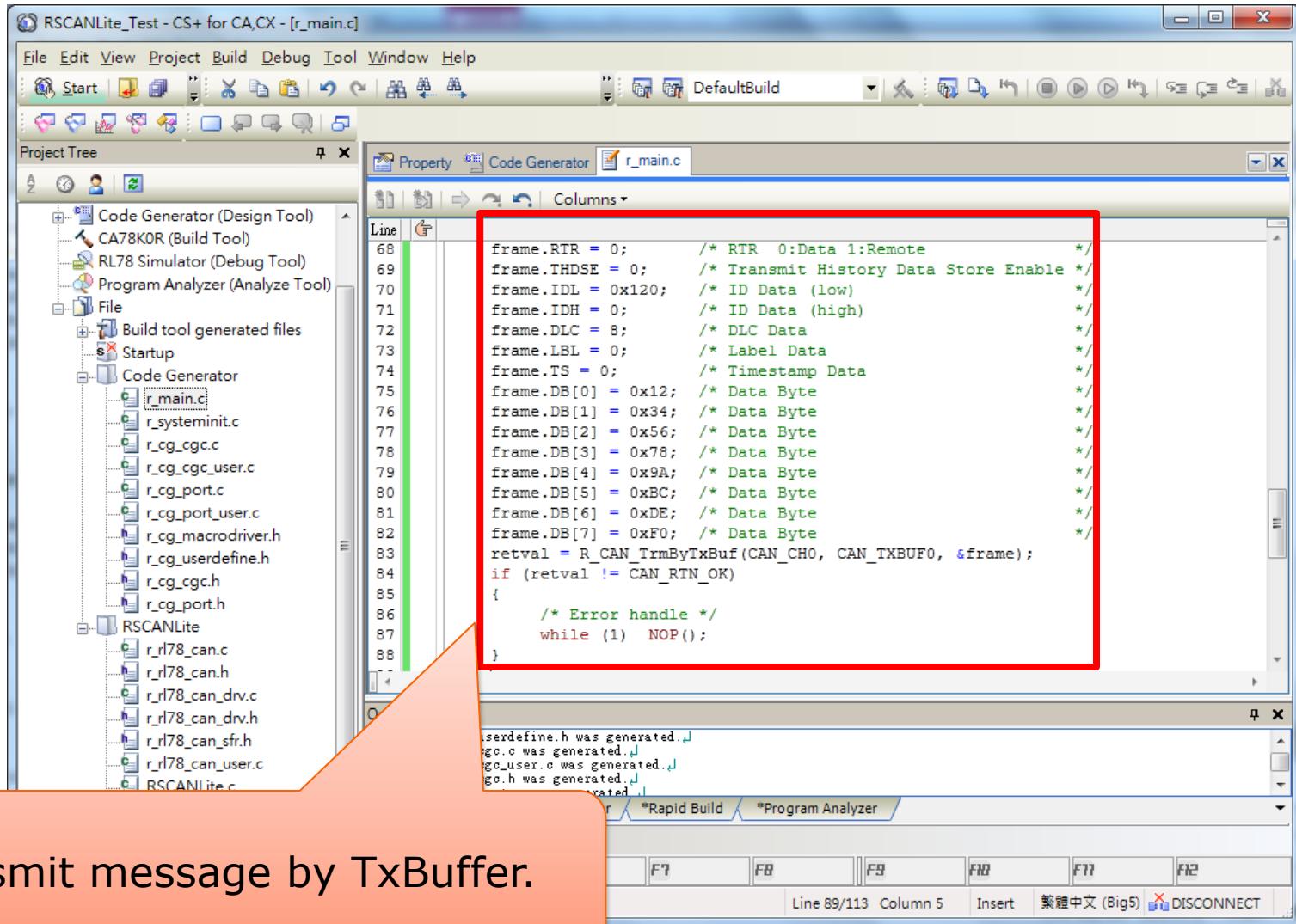
    ***
}

erdefine.h was generated.
io.c was generated.
io_user.c was generated.
io.h was generated.
prt.c was generated.
prt_user.c was generated.
prt.h was generated.

eration of generating file was successful.
```

Define message frame and
return value type for function
call.

Integrated RS-CAN API into CS+ project



The screenshot shows the Renesas CS+ IDE interface with the project "RSCANLite_Test - CS+ for CA,CX - [r_main.c]" open. The Project Tree on the left lists various tool components and generated files, including "Build tool generated files" like "Startup", "Code Generator" (containing "r_main.c", "r_systeminit.c", etc.), and "RSCANLite" (containing "r_rl78_can.c", "r_rl78_can.h", etc.). The main window displays the source code for "r_main.c". A red box highlights the following code segment:

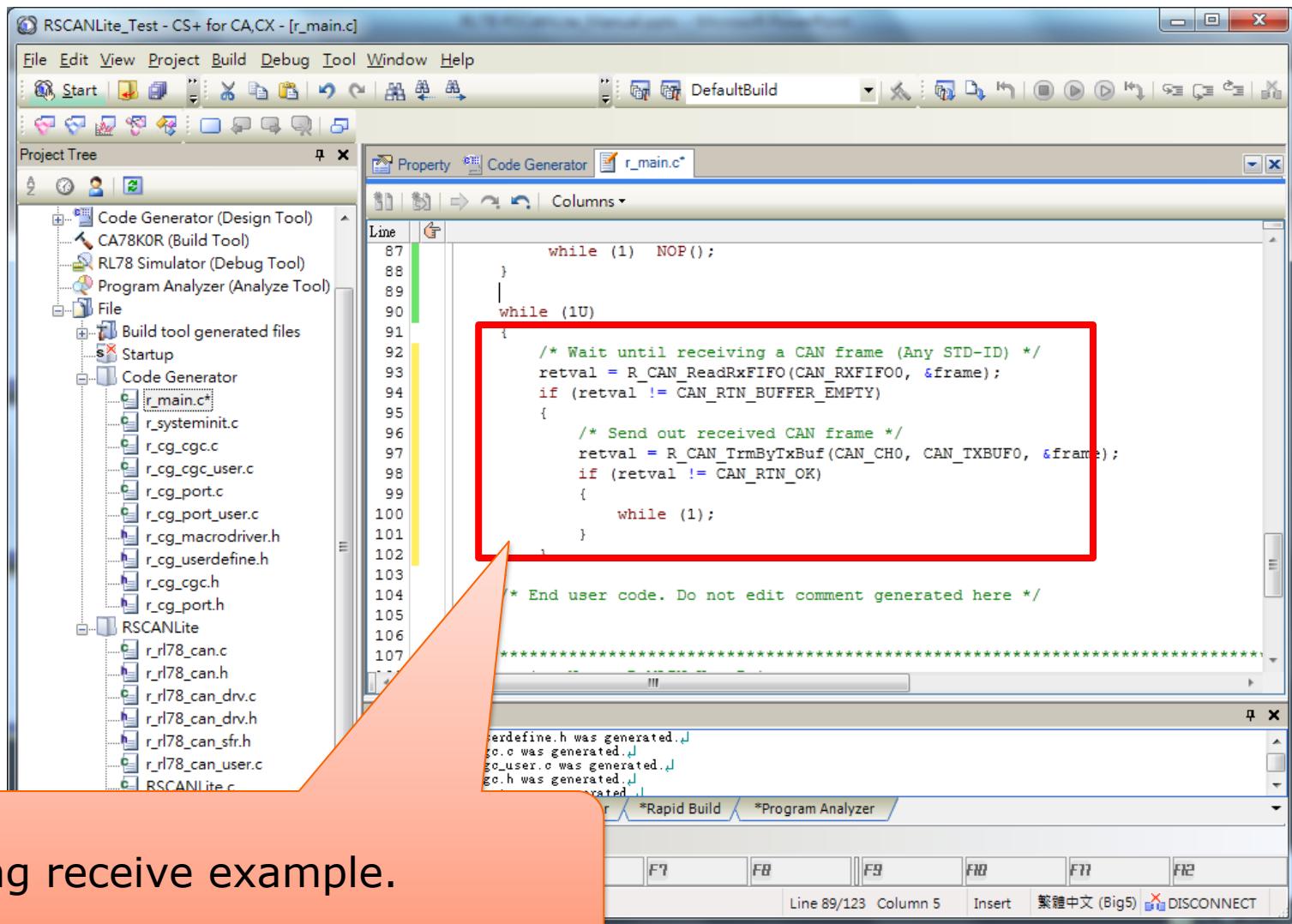
```
frame.RTR = 0; /* RTR 0:Data 1:Remote */  
frame.THDSE = 0; /* Transmit History Data Store Enable */  
frame.IDL = 0x120; /* ID Data (low) */  
frame.IDH = 0; /* ID Data (high) */  
frame.DLC = 8; /* DLC Data */  
frame.LBL = 0; /* Label Data */  
frame.IS = 0; /* Timestamp Data */  
frame.DB[0] = 0x12; /* Data Byte */  
frame.DB[1] = 0x34; /* Data Byte */  
frame.DB[2] = 0x56; /* Data Byte */  
frame.DB[3] = 0x78; /* Data Byte */  
frame.DB[4] = 0x9A; /* Data Byte */  
frame.DB[5] = 0xBC; /* Data Byte */  
frame.DB[6] = 0xDE; /* Data Byte */  
frame.DB[7] = 0xF0; /* Data Byte */  
retval = R_CAN_TrmByTxBuf(CAN_CHO, CAN_TXBUFO, &frame);  
if (retval != CAN_RTN_OK)  
{  
    /* Error handle */  
    while (1) NOP();  
}
```

Below the code, a message box shows the output of the build process:

```
userdefine.h was generated.  
go.c was generated.  
go_user.c was generated.  
go.h was generated.  
generated.
```

An orange callout bubble at the bottom left points to the highlighted code with the text "Transmit message by TxBuffer."

Integrated RS-CAN API into CS+ project



The screenshot shows the Renesas CS+ IDE interface with the project "RSCANLite_Test - CS+ for CA,CX - [r_main.c]" open. The Project Tree on the left shows the project structure, including files like r_main.c, r_systeminit.c, and various RCG-generated files. The main window displays the source code for r_main.c. A red box highlights a specific block of code:

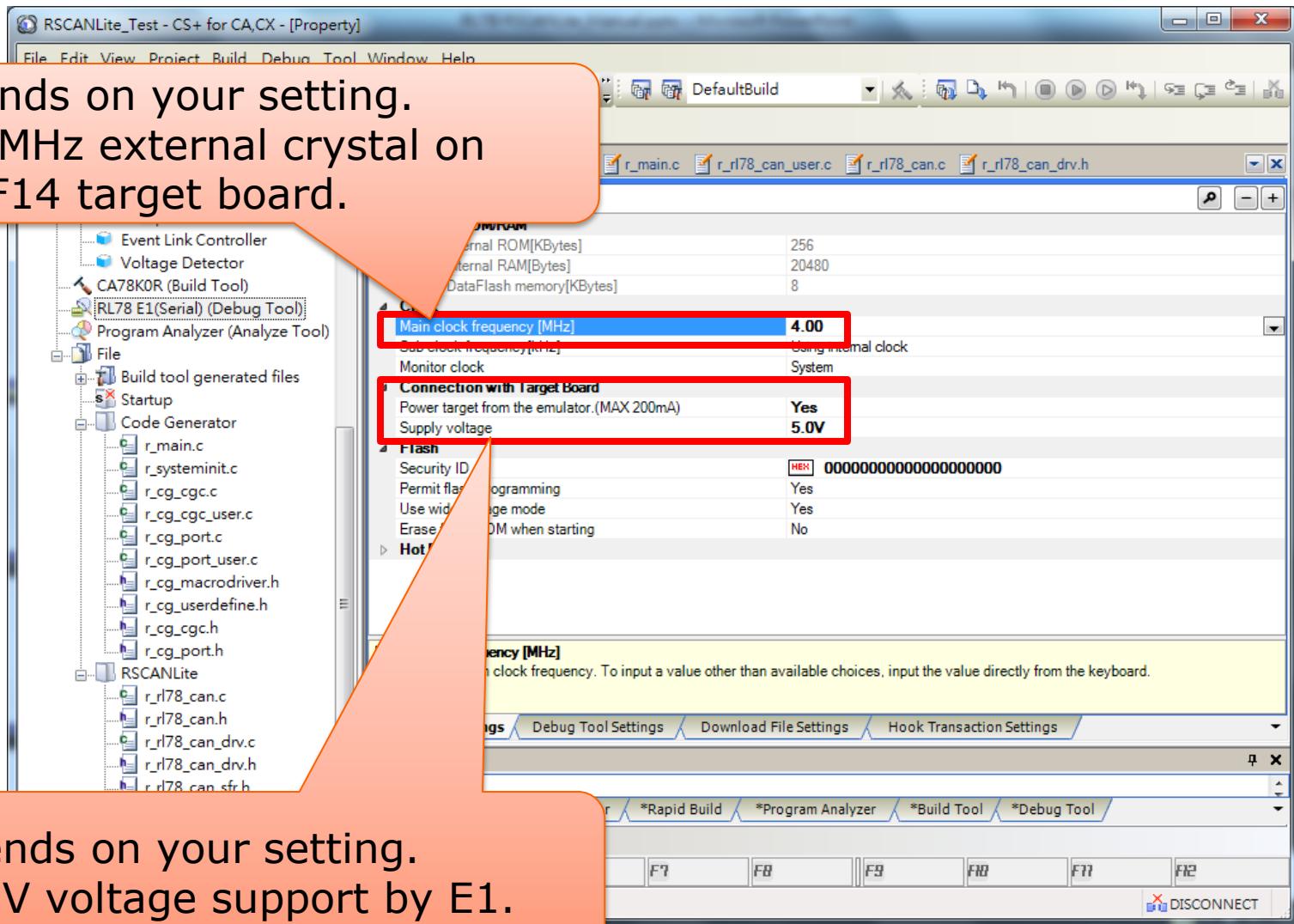
```
        while (1) NOP();  
    }  
  
    while (1U)  
    {  
        /* Wait until receiving a CAN frame (Any STD-ID) */  
        retval = R_CAN_ReadRxFIFO(CAN_RXFIFO0, &frame);  
        if (retval != CAN_RTN_BUFFER_EMPTY)  
        {  
            /* Send out received CAN frame */  
            retval = R_CAN_TrmByTxBuf(CAN_CH0, CAN_TXBUFO, &frame);  
            if (retval != CAN_RTN_OK)  
            {  
                while (1);  
            }  
        }  
  
        /* End user code. Do not edit comment generated here */  
  
*****  
  
r_cg_userdefine.h was generated.  
go.c was generated.  
go_user.c was generated.  
go.h was generated.  
generated.
```

An orange callout box points from the text "Polling receive example." to the highlighted code in the editor.

Polling receive example.

Setup debug environment

Setup debug environment



Depends on your setting.
Ex. 5V voltage support by E1.

RSCANLite_Test - RL78 E1(Serial) - CS+ for CA,CX - [r_rl78_can_user.c]

File Edit View Project Build Debug Tool Help

Start Disassemble1 r_main.c r_rl78_can_user.c r_rl78_can.c r_rl78_can_drv.h DefaultBuild

Project Tree

- Comparator
- Event Link Controller
- Voltage Detector
- CA78KOR (Build Tool)
- RL78 E1(Serial) (Debug Tool)
- Program Analyzer (Analyze)
- File
 - Build tool generated file
 - Startup
 - Code Generator
 - r_main.c
 - r_systeminit.c
 - r_cg_cgc.c
 - r_cg_cgc_user.c
 - r_cg_port.c
 - r_cg_port_user.c
 - r_cg_macrodriver.h
 - r_cg_userdefine.h
 - r_cg_cgc.h
 - r_cg_port.h
 - RSCANLite
 - r_rl78_can.c
 - r_rl78_can.h
 - r_rl78_can_drv.c
 - r_rl78_can_drv.h
 - r_rl78_can_sfr.h
 - r_rl78_can_user.c
 - DCCAN1

Property Disassemble1 r_main.c r_rl78_can_user.c r_rl78_can.c r_rl78_can_drv.h Columns

Line	Address	Content
27		
28		
29		*****
30		* Function Name: r_can0_g_receive_isr()
31		* Description : Receive FIFO interrupt service routine
32		*****
33	00185	_interrupt void r_can0_g_receive_isr(void)
34		{
35		uint16_t temp_status;
36		/* --- Check Rx FIFO0 --- */
37	00187	temp_status = RFSTS0;
38	0018a	if ((temp_status & (0x0001 << CAN_RFIF_BIT_POS))
39		{
40	00193	/* --- Clear Receive FIFO0 request flag --- */
41		RFSTS0 &= ~ (0x0001 << CAN_RFIF_BIT_POS);
42		/* [ISR] */
43	0019d	NOP();
44		}
45		/* --- Check Rx FIFO1 --- */
46		temp_status = RFSTS1;
47	0019e	if ((temp_status & (0x0001 << CAN_RFIF_BIT_POS))
48		{
49	001a1	/* --- Clear Receive FIFO1 request flag --- */
50		RFSTS1 &= ~ (0x0001 << CAN_RFIF_BIT_POS);
51		/* [ISR] */
52	001aa	NOP();
53		
54	001b4	
55		

Local Variables

Current

Name

- retval
- frame

Output [EOF]

F1 F2 F3 F4 F5 F6 F7 F8 F9 F10 F11 F12

Line 44/330 Column 15 Insert 繁體中文 (Big5) BREAK 0x00304 RL78 E1(Serial) 67.900 ms

RSCANLite_Test - RL78 E1(Serial) - CS+ for CA,CX - [r_rl78_can_user.c]

File Edit View Project Build Debug Tool Help

Start Disassemble1 r_main.c r_rl78_can_user.c r_rl78_can.c r_rl78_can_drv.h DefaultBuild

Project Tree

- Comparator
- Event Link Controller
- Voltage Detector
- CA78KOR (Build Tool)
- RL78 E1(Serial) (Debug Tool)
- Program Analyzer (Analyze)
- File
 - Build tool generated file
 - Startup
 - Code Generator
 - r_main.c
 - r_systeminit.c
 - r_cg_cgc.c
 - r_cg_cgc_user.c
 - r_cg_port.c
 - r_cg_port_user.c
 - r_cg_macrodriver.h
 - r_cg_userdefine.h
 - r_cg_cgc.h
 - r_cg_port.h
 - RSCANLite
 - r_rl78_can.c
 - r_rl78_can.h
 - r_rl78_can_drv.c
 - r_rl78_can_drv.h
 - r_rl78_can_sfr.h
 - r_rl78_can_user.c
 - DCAN1

Property Disassemble1 r_main.c r_rl78_can_user.c r_rl78_can.c r_rl78_can_drv.h Columns

Line	Address	Code
126		/* ***** */
127		* Function Name: r_can0_transmit_isr()
128		* Description : Transmit interrupt service routine for CAN0
129		*****
130		
131		_interrupt void r_can0_transmit_isr(void)
132	00228	{
133		uint16_t temp_status;
134		
135	0022b	/* --- Check TRFIFO status --- */
136	0022e	temp_status = CFSTS0; /* Read Common FIFO status */
137		if ((0x0001 & (temp_status >> CAN_CFTXIF_BIT_POS))
138		{
139		/* --- Clear transmit flag --- */
140	00239	CFSTS0 &= ~ (0x0001 << CAN_CFTXIF_BIT_POS);
141		
142	00243	/* [ISR] */
143		NOP();
144		}
145		
146	00244	/* --- Check TrmBuffer0 status --- */
147	0024c	temp_status = ((TMSTS0 & 0x0006) >> CAN_TMTRF_BIT_POS);
148		TMSTS0 &= ~ (0x0006UL);
149	00254	switch (temp_status)
150		{
151		case CAN_RTN_TRANSMITTING:
152		/* transmitting or idle */
153		/* [ISR] */
154		break;

F1 F2 F3 F4 F5 F6 F7 F8 F9 F10 F11 F12

Line 146/330 Column 28 Insert 繁體中文 (Big5) BREAK 0x00304 RL78 E1(Serial) 67.900 ms

Local Variables

Current

Name

- retval
- frame

Output

[EOF]

All

RSCANLite_Test - RL78 E1(Serial) - CS+ for CA,CX - [r_main.c]

File Edit View Project Build Debug Tool Help

Start Disassemble1 r_main.c r_rl78_can_user.c r_rl78_can.c r_rl78_can_drv.h DefaultBuild

Project Tree

- Comparator
- Event Link Controller
- Voltage Detector
- CA78KOR (Build Tool)
- RL78 E1(Serial) (Debug Tool)
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 - Build tool generated file
 - Startup
 - Code Generator
 - r_main.c
 - r_systeminit.c
 - r_cg_cgc.c
 - r_cg_cgc_user.c
 - r_cg_port.c
 - r_cg_port_user.c
 - r_cg_macrodriver.h
 - r_cg_userdefine.h
 - r_cg_cgc.h
 - r_cg_port.h
 - RSCANLite
 - r_rl78_can.c
 - r_rl78_can.h
 - r_rl78_can_drv.c
 - r_rl78_can_drv.h
 - r_rl78_can_sfr.h
 - r_rl78_can_user.c

Property Columns

Line	Address	Code
58		* Return Value : None
59		*****
60		*****
61	00304	void main(void)
62		{
63		Can_RtnType retval;
64		can_frame_t frame;
65	0030a	R_MAIN_UserInit();
66		/* Start user code. Do not edit comment generated h
67	0030e	frame.IDE = 1; /* IDE 0:Standard 1:Extend
68	00314	frame.RTR = 0; /* RTR 0:Data 1:Remote
69	00318	frame.THDSE = 0; /* Transmit History Data Store
70	0031c	frame.IDL = 0x120; /* ID Data (low)
71	00320	frame.IDH = 0; /* ID Data (high)
72	00327	frame.DLC = 8; /* DLC Data
73	0032f	frameLBL = 0; /* Label Data
74	00336	frameTS = 0; /* Timestamp Data
75	00339	frameDB[0] = 0x12; /* Data Byte
76	0033c	frameDB[1] = 0x34; /* Data Byte
77	0033f	frameDB[2] = 0x56; /* Data Byte
78	00342	frameDB[3] = 0x78; /* Data Byte
79	00345	frameDB[4] = 0x9A; /* Data Byte
80	00348	frameDB[5] = 0xBC; /* Data Byte
81	0034b	frameDB[6] = 0xDE; /* Data Byte
82	0034e	frameDB[7] = 0xF0; /* Data Byte
83	00351	retval = R_CAN_TrmByTxBuf(CAN_CHO, CAN_TXBUFO, &frame);
84	0035c	if (retval != CAN_RTN_OK)
85		/* Error handle */
86		

Local Variables

Current

Name

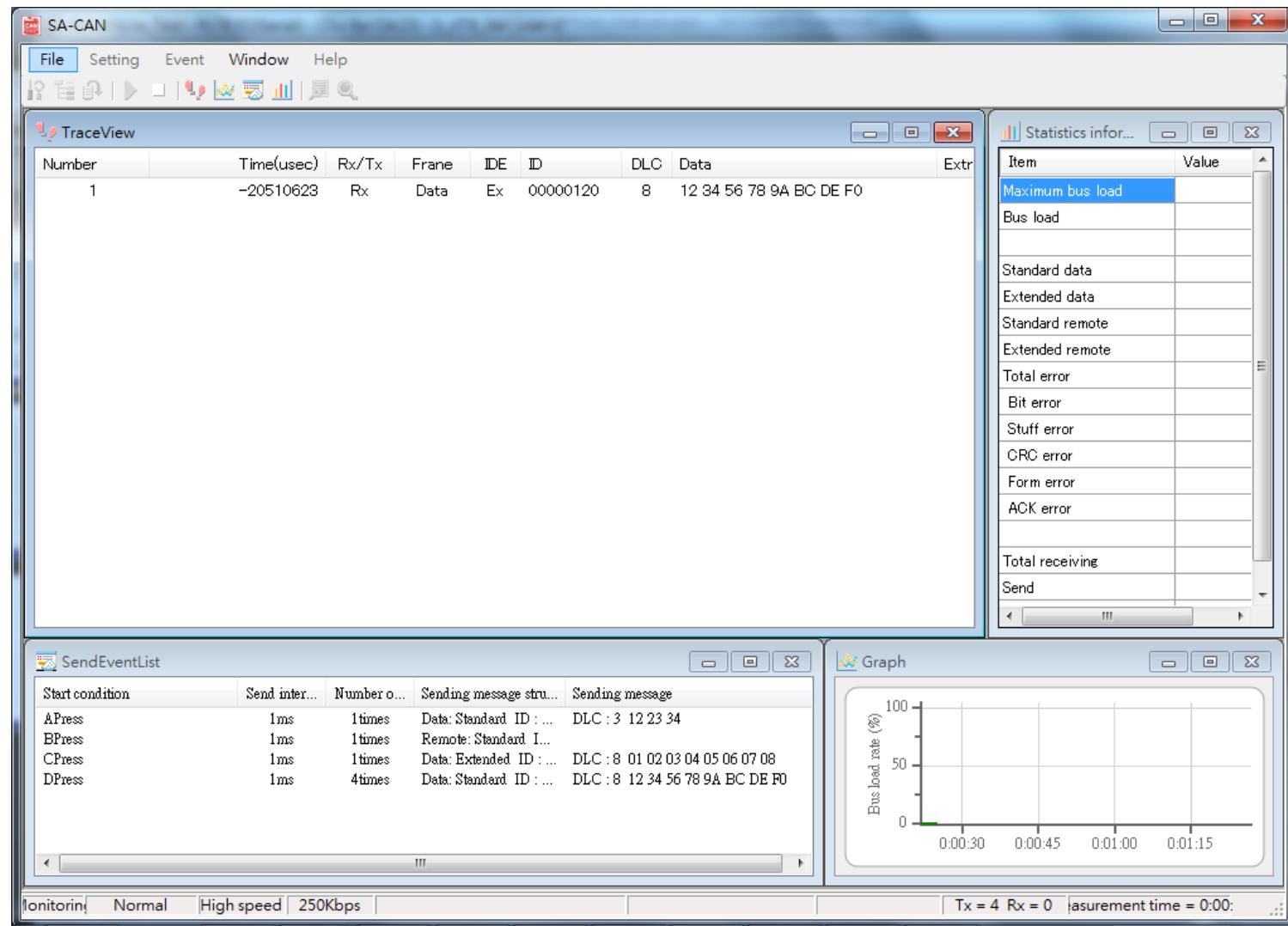
- retval
- frame

Output

[B0F]

F1 F2 F3 F4 F5 F6 F7 F8 F9 F10 F11 F12

Line 100/123 Column 14 Insert 繁體中文 (Big5) BREAK 0x00304 RL78 E1(Serial) 67.900 ms



RSCANLite_Test - RL78 E1(Serial) - CS+ for CA,CX - [r_rl78_can_user.c]

File Edit View Project Build Debug Tool Window Help

Start Disassemble1 r_main.c r_rl78_can_user.c r_rl78_can.c r_rl78_can_drv.h DefaultBuild

Project Tree

- Comparator
- Event Link Controller
- Voltage Detector
- CA78KOR (Build Tool)
- RL78 E1(Serial) (Debug Tool)
- Program Analyzer (Analyze)
- File
 - Build tool generated file
 - Startup
 - Code Generator
 - r_main.c
 - r_systeminit.c
 - r_cg_cgc.c
 - r_cg_cgc_user.c
 - r_cg_port.c
 - r_cg_port_user.c
 - r_cg_macerdriver.h
 - r_cg_userdefine.h
 - r_cg_cgc.h
 - r_cg_port.h
 - RSCANLite
 - r_rl78_can.c
 - r_rl78_can.h
 - r_rl78_can_drv.c
 - r_rl78_can_drv.h
 - r_rl78_can_sfr.h
 - r_rl78_can_user.c
 - DCAN1

Property Disassemble1 r_main.c r_rl78_can_user.c r_rl78_can.c r_rl78_can_drv.h Columns

Line	Address	Code
151		case CAN_RTN_TRANSMITTING:
152		/* transmitting or idle */
153		/* [ISR] */
154		break;
155		
156		case CAN_RTN_TX_ABORT_OVER:
157		/* Transmission aborted */
158		/* [ISR] */
159		break;
160		
161		case CAN_RTN_TX_END:
162		/* Transmission completed w/o abort request */
163		/* [ISR] */
164	00264	NOP();
165		break;
166		
167		case CAN_RTN_TX_END_WITH_ABORT_REQ:
168		/* Transmission completed w/ abort request */
169		/* [ISR] */
170		break;
171		
172		default:
173		break;
174		}
175		
176		/* --- Check TrmBuffer1 status --- */
177	00265	temp_status = ((TMSTS1 & 0x0006) >> CAN_TMRF_BIT_
178	0026d	TMSTS1 &= ~ (0x0006UL);
179	00275	switch (temp_status)

Local Variables

Current

Name: temp_status

Output

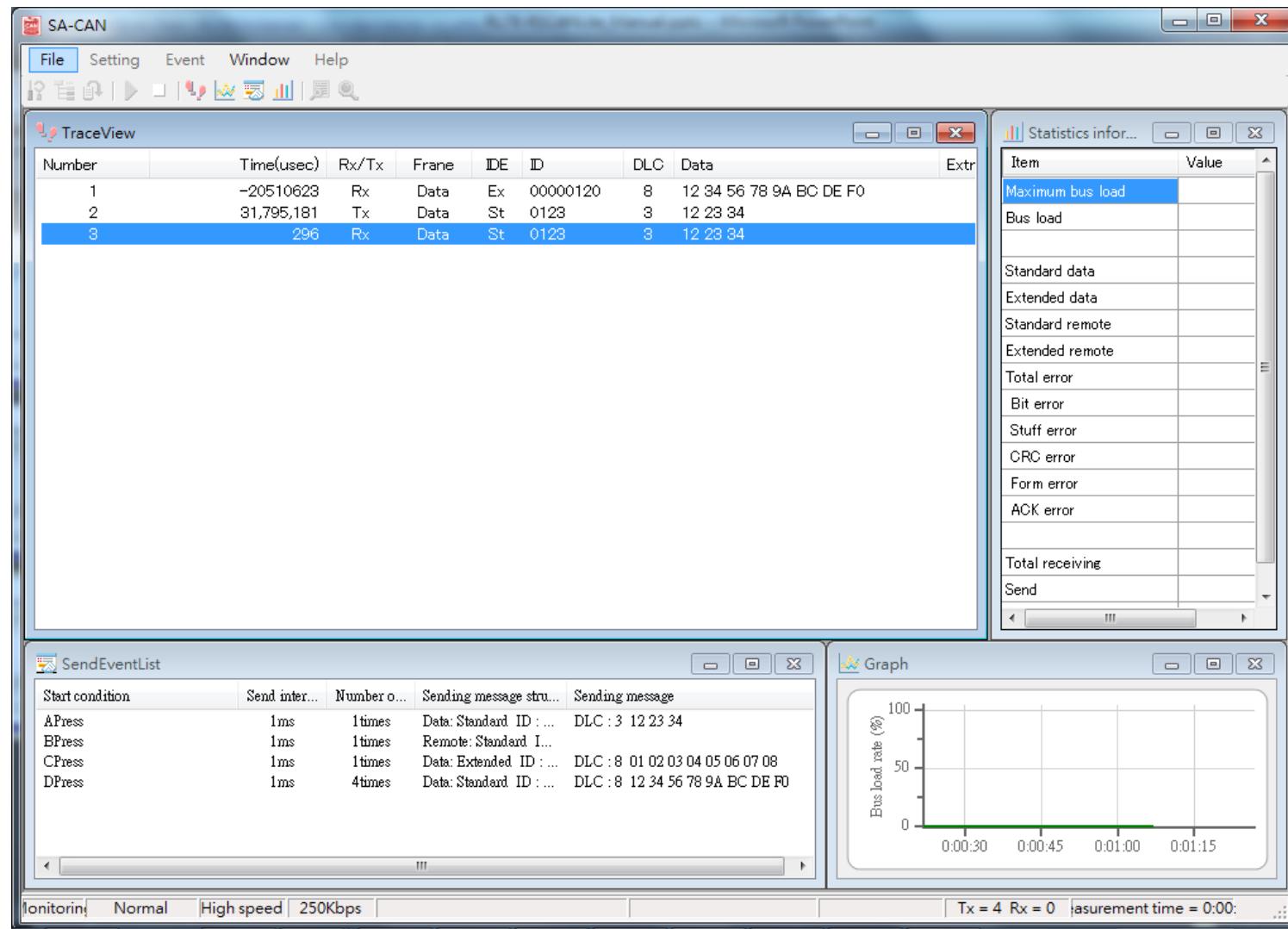
Stopped by Software Break.

Stopped by Software Break.

[EOF]

F1 F2 F3 F4 F5 F6 F7 F8 F9 F10 F11 F12

Line 164/330 Column 1 Insert 繁體中文 (Big5) BREAK 0x00264 RL78 E1(Serial) 600.000 μs



RSCANLite_Test - RL78 E1(Serial) - CS+ for CA,CX - [r_rl78_can_user.c]

File Edit View Project Build Debug Tool Window Help

Start Disassemble1 r_main.c r_rl78_can_user.c r_rl78_can.c r_rl78_can_drv.h DefaultBuild

Project Tree

- Comparator
- Event Link Controller
- Voltage Detector
- CA78KOR (Build Tool)
- RL78 E1(Serial) (Debug Tool)
- Program Analyzer (Analyze)
- File
 - Build tool generated file
 - Startup
 - Code Generator
 - r_main.c
 - r_systeminit.c
 - r_cg_cgc.c
 - r_cg_cgc_user.c
 - r_cg_port.c
 - r_cg_port_user.c
 - r_cg_macerdriver.h
 - r_cg_userdefine.h
 - r_cg_cgc.h
 - r_cg_port.h
 - RSCANLite
 - r_rl78_can.c
 - r_rl78_can.h
 - r_rl78_can_drv.c
 - r_rl78_can_drv.h
 - r_rl78_can_sfr.h
 - r_rl78_can_user.c
 - DCAN1

Property Disassemble1 r_main.c r_rl78_can_user.c r_rl78_can.c r_rl78_can_drv.h Columns

Line	Address	Code
151		case CAN_RTN_TRANSMITTING:
152		/* transmitting or idle */
153		/* [ISR] */
154		break;
155		
156		case CAN_RTN_TX_ABORT_OVER:
157		/* Transmission aborted */
158		/* [ISR] */
159		break;
160		
161		case CAN_RTN_TX_END:
162		/* Transmission completed w/o abort request */
163		/* [ISR] */
164	00264	NOP();
165		break;
166		
167		case CAN_RTN_TX_END_WITH_ABORT_REQ:
168		/* Transmission completed w/ abort request */
169		/* [ISR] */
170		break;
171		
172		default:
173		break;
174		}
175		
176		/* --- Check TrmBuffer1 status --- */
177	00265	temp_status = ((TMSTS1 & 0x0006) >> CAN_TMRF_BIT_
178	0026d	TMSTS1 &= ~ (0x0006UL);
179	00275	switch (temp_status)

Local Variables

Current

Name: temp_status

Output

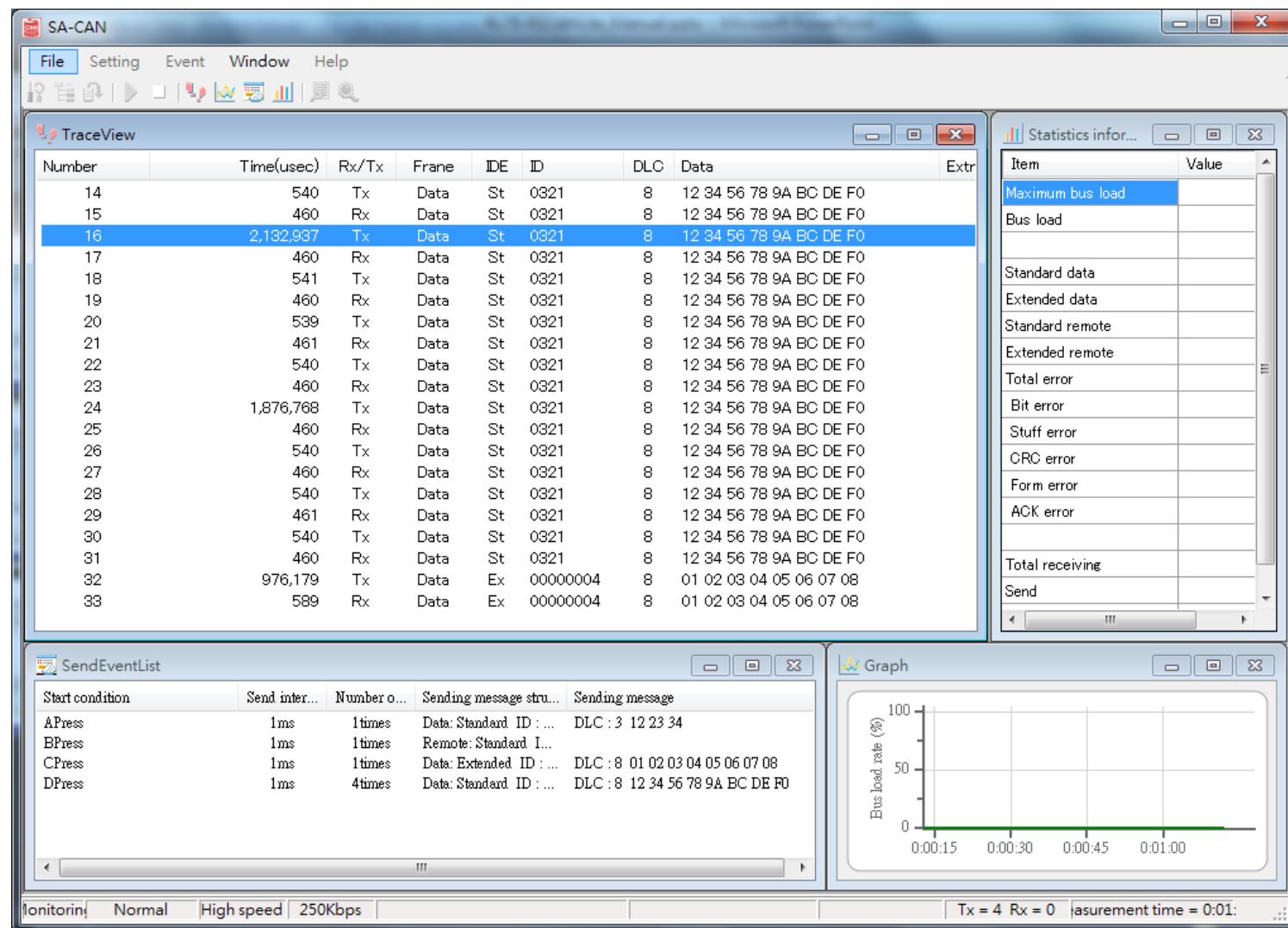
Stopped by Software Break

Stopped by Software Break

Stopped by Software Break [EOF]

F1 F2 F3 F4 F5 F6 F7 F8 F9 F10 F11 F12

Line 164/330 Column 1 Insert 繁體中文 (Big5) BREAK 0x00264 RL78 E1(Serial) 7.786 s





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