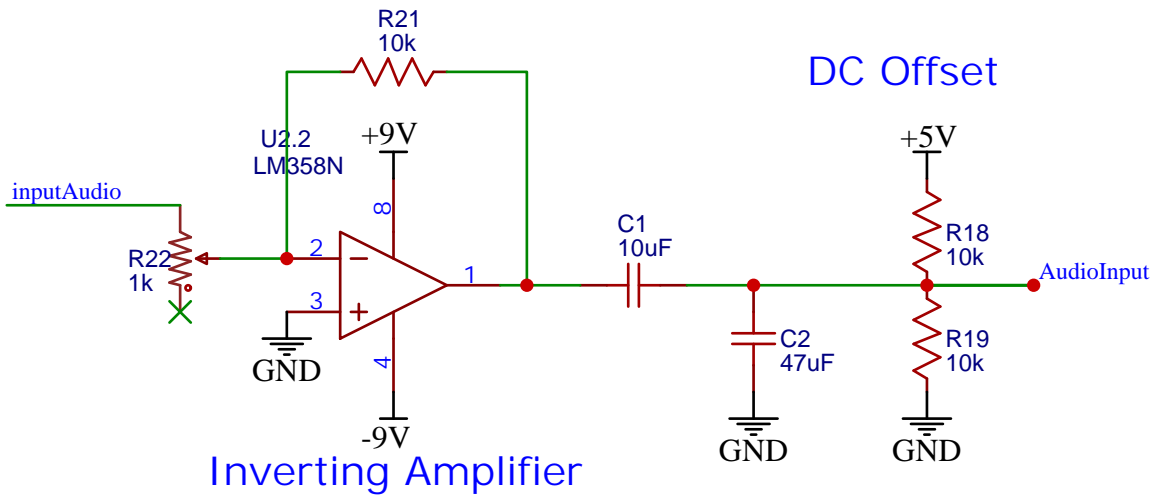
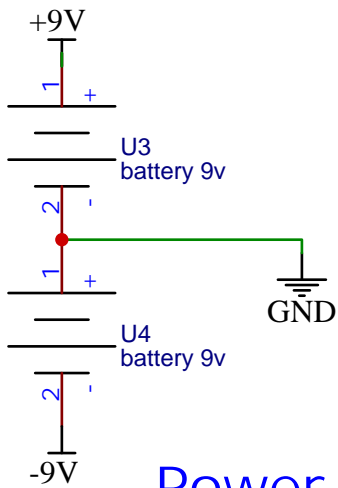


Input Stage

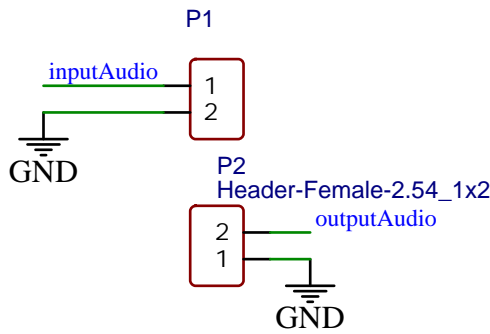


Inverting Amplifier

DC Offset

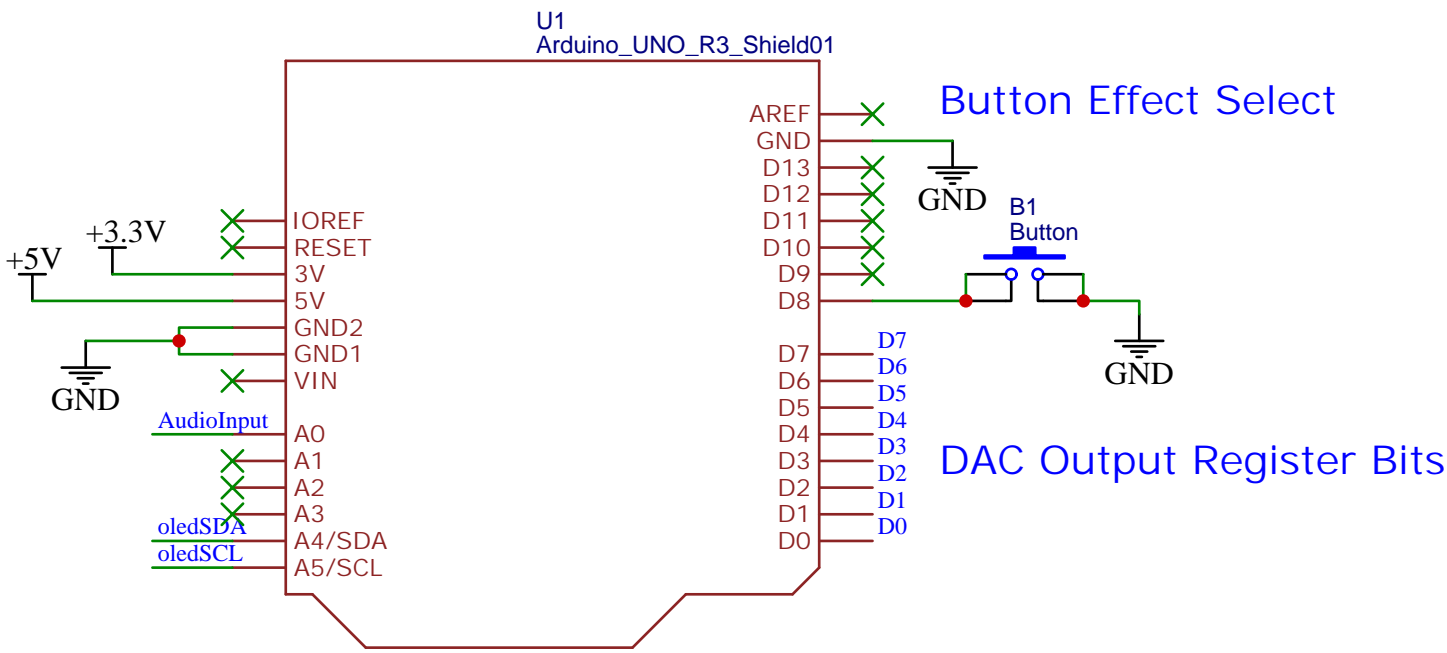


Power Supply



Input/Output

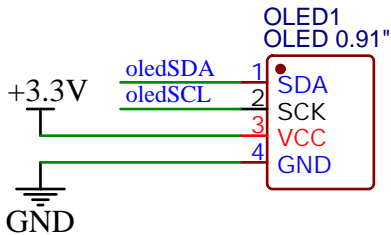
Micro-Controller



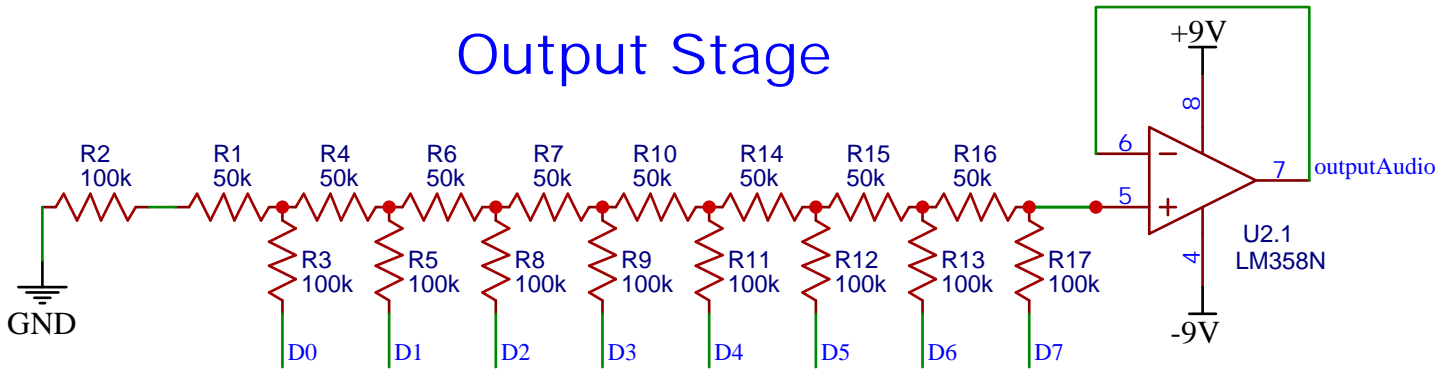
Button Effect Select

DAC Output Register Bits

OLED Screen



Output Stage



Voltage Buffer

R2R Resistor Ladder

TITLE: Main Schematic		REV: 1.0
EasyEDA	Company: Matthew Yong	Sheet: 1/1
	Date: 2020-02-14	Drawn By: matthewyfy