Make – Scripting Multi-File Builds

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- The building of applications requires many complex commands.
- This can become very unwieldy very quickly.
- We solve this by using some sort of build system.





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- We can do this by adding the -c option to g++





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- Compile each of the cpp files like this:
 - q++ -c soda-machine.cpp
 - g++ -c sodasim.cpp
- List the directory. Note the object files this created.
- Now we will link the executable with the following:

```
q++ soda-machine.o sodasim.o -o sodasim
```





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- Recipes are either implicit or explicitly.



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 make hello.o
- Pretty neat, huh?



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- For example, try the following:
 - **1** Change to the examples/19-Classy directory.
 - Create a file named Makefile with the following content: sodasim: sodasim.o soda-machine.o

```
g++ -o sodasim sodasim.o soda-machine.o
```

```
sodasim.o: sodasim.cpp soda-machine.h
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```
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 Remember that when indenting, you must use a literal tab character!



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- There are several pre-defined variables, the two most commonly used ones are:
 - \$@ The name of the target
 - \$^ − The list of all ingredients
- We could simplify the sodasim Makefile like so: sodasim: sodasim.o soda-machine.o

sodasim.o: sodasim.cpp soda-machine.h
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- You can also define your own variables: TARGETS=stock
- You refer to your own variables like this: \$ (TARGETS)
- This allows you to make compact makefiles.





Making The Program 5 Makefile

```
TARGETS=stock
#application builds
all: $(TARGETS)
stock: iofun.o main.o stock.o transaction.o portfolio.o
        q++ -o $@ $^
#object files
iofun.o: iofun.h iofun.cpp
main.o: main.cpp iofun.h stock.h transaction.h portfolio.h
stock.o: stock.h stock.cpp
transction.o: transaction.cpp transaction.h
portfolio.o: portfolio.cpp portfolio.h
#delete all binaries
clean:
        rm -f *.o $(TARGETS)
```

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• Run make to build the first recipe in the Makefile





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Building With Make

- Run make to build the first recipe in the Makefile
- Run make target to build any other target.
- For example make clean runs the clean target.





Lab Activity – Address Book

Using the Program 5 makefile as an example, build a makefile for the address book lab.

```
#application builds
all: $(TARGETS)
stock: iofun.o main.o stock.o transaction.o portfolio.o
        a++ -0 $@ $^
#object files
iofun.o: iofun.h iofun.cpp
main.o: main.cpp iofun.h stock.h transaction.h portfolio.h
stock.o: stock.h stock.cpp
transction.o: transaction.cpp transaction.h
portfolio.o: portfolio.cpp portfolio.h
#delete all binaries
clean:
        rm - f *.o $(TARGETS)
```