

Trustee Memory

core 1								
core 2								
core 3								
core 4								
core 5						Resp2		
core 6								
core 7								
core 8	Resp1							
core 9								

Client Memory

core 1							Resp1	
core 2								
core 3								
core 4								
core 5								
core 6								
core 7								
core 8				Resp2				
core 9								