PDE and Jitter modelling of SPAD Devices.

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Abstract

In this paper we present a full 3D simulation methodology to extract Photon Detection Probability (PDP) and Jitter of Single-Photon Avalanche Diode (SPAD) Devices. The simulation results are compared with measurements on devices and show good agreement with the experiments.

Keywords— single-photon avalanche diode (SPAD), photon detection probability (PDP), jitter, avalanche breakdown probability, breakdown voltage

1 Introduction

Single Photon Avalanche Diodes (SPAD) are key optoelectronic detectors for medical imaging, camera ranging and automotive laser imaging detection and ranging (LiDAR) applications. Currently, the device leading the market is a micrometric silicon (Si) PN junction associated to a proximity CMOS electronics biasing the system above the breakdown voltage. Si-SPADs present low noise and relatively high photon detection probability (PDP), but their sensitivity is limited to photon wavelengths lower than 1100 nm, while class 1 eye-safety devices would require wavelengths larger than 1400 nm.

2 Device structure and TCAD simulation

A variety of SPAD architectures were devised with pitch size ranging from five to ten microns. Each layout was devised to create a different electric field profile which can vary both laterally and vertically through the full volume of the device. These architectures were manufactured on silicon and characterized using a methodology which prevents contamination of results from any potential crosstalk.

The structures' doping profiles were modelled with a well-calibrated process simulator followed by electrostatic device simulation. The resulting electric field profiles (calculated as usual by the solution of the Poisson equation) were used as the starting point for a suite of in-house softwares developed to calcuate breakdown probability and jitter characteristics. The carrier density ρ and the recombination-generation processes were computed by resolving current continuity equations consistently with the drift-diffusion transport model.

In parallel, optical simulation with a well-known FDTD solver was performed on the architectures to obtain maps of optical absorption. The back end of the devices including the microlens and texturation was kept constant for each comparison.

3 Avalanche breakdown probability

The avalanche breakdown probability is computed by the means of the well known McIntyre model [1]. We briefly recall the model derivation: Let $P_e(x)$ be the probability that an electron starting at x in the depletion layer triggers an avalanche and $P_h(x)$ the same probability for an hole starting at x. Straightforwardly, the probability that neither an hole nor an electron starting at x trigger an avalanche is given by $(1-P_e(x))(1-P_e(x))$

 $P_h(x)$). Thus, the probability that either the hole or the electron trigger an avalanche, noted P_{pair} is :

$$P_{pair}(x) = 1 - (1 - P_e(x)) (1 - P_h(x))$$

= $P_e + P_h - P_e P_h$

Now, the probability that an electron starting at x + dx triggers an avalanche is: The probability that the electron reaches the position x and triggers an avalanche in x plus the probability that it triggers an avalanche between x and x + dx less the probability of the intersection of the two previous events. It writes:

$$\begin{split} P_{e}(x+dx) &= P_{e}(x) + \alpha_{e}(x) dx P_{pair}(x) - P_{e}(x) \alpha_{e} dx P_{pair}(x) \\ &= P_{e}(x) + \alpha_{e}(x) dx (P_{e}(x) + P_{h}(x) - P_{e}(x) P_{h}(x)) \\ &- P_{e}(x) \alpha_{e}(x) dx (P_{e}(x) + P_{h}(x) - P_{e}(x) P_{h}(x)) \\ &= P_{e}(x) + dx \alpha_{e}(x) (P_{e}(x) + P_{h}(x) - P_{e}(x) P_{h}(x)) (1 - P_{e}(x)) \end{split}$$

Where α_e is the electron linear ionization rate: the probability by length that an electron create an impact ionization event. One can rearrange the terms to obtain:

$$\frac{P_e(x+dx) - P_e(x)}{dx} = \alpha_e(x)(P_e(x) + P_h(x) - P_e(x)P_h(x))(1 - P_e(x))$$

Which leads to the first ordinary differential equation:

$$\frac{dP_e}{dx} = (1 - P_e)\alpha_e(P_e + P_h - P_eP_h)$$

The same reasoning applies to the probability that an hole starting at x-dx triggers an avalanche. Which leads to the second ordinary differential equation :

$$\frac{dP_h}{dx} = -(1 - P_h)\alpha_h(P_e + P_h - P_eP_h)$$

Therefore we can draw up the McIntyre system :

$$\begin{cases} \frac{dP_e}{dx} = (1 - P_e)\alpha_e(P_e + P_h - P_eP_h) \\ \frac{dP_h}{dx} = -(1 - P_h)\alpha_h(P_e + P_h - P_eP_h) \end{cases}$$
 (2)

for $0 \le x \le W$.

Adding the couple of boundary value conditions:

$$\begin{cases} P_e(x=0) = 0 & (3) \\ P_h(x=W) = 0 & (4) \end{cases}$$

we have a full 1D coupled and non-linear boundary value problem. Since we have to extract this value at a large number of points, we use a self-made solver, embedded in a C++ program. This solver uses finite difference method coupled with a Newton's method to care of the non-linearity of the problem [2].

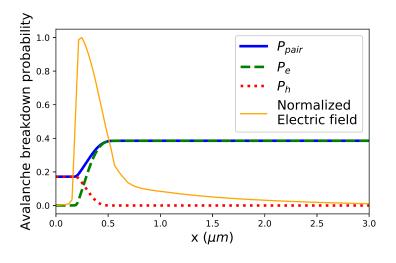


Figure 1: Typical solution of the McIntyre model for a given electric field curve

3.1 Application to field lines

The McIntyre model is a fully 1D model where the electron and holes path are assumed to be a straight line, often took from the bottom to the top of the device, see for example [3]. In this work we wish to have accurate values of breakdown probability in all the device volume. To this purpose we use electric field streamline to model the carriers transport inside the device. The field lines are computed straightforwardly using simple Euler scheme with adaptive step to ensure a good distribution of points along the line.

$$\frac{dX(s)}{ds} = \vec{F}_{electric} \tag{5}$$

Then the streamline is defined by the following set of points:

$$\{X(s) \text{ for } s \in [s_0, s_f]\}\$$

Our Euler method then reads:

$$\frac{X(s+ds) - X(s)}{ds} = \vec{F}_{electric}(X(s))$$
 (6)

$$\frac{X(s+ds)-X(s)}{ds} = \vec{F}_{electric}(X(s))$$

$$\implies X(s+ds) = X(s) + \underbrace{ds\vec{F}_{electric}(X(s))}_{dX}$$
(6)

This operation is performed both forward (hole motion) and backward (electron motion). We then interpolate the electric field on the resulting streamline. We can now obtain a function E(x) where x is the distance from the beginning of the line and E(x) is the norm of the electric field at this point. The streamlines and this function are respectively represented on figures 2 and 1

We can now compute the impact ionization coefficients, required to compute the McIntyre's model, in this work we choose the local coefficient from Van Overstraeten and De Man [4].

We can compute the breakdown probability over multiple streamlines starting from multiple points inside the device and plot them to have an idea of the breakdown probability inside the device, see figure.

Jitter modeling

Drif-Diffusion model

The jitter in SPAD devices is made of multiple phenomena such as depth of photon absorption, carrier transport timing, avalanche buildup, quench circuit statistics etc.

In the present work, we focus on the modeling of the carrier transport part which is responsible for the queue of the jitter distribution. To do so, we couple the streamlines and the one dimensional advectiondiffusion equation with variable velocity and diffusion. Let us consider that a photon absorption at a given point x_0 leads to the creation of an electron-hole pair, we want to simulate the timing for the electron

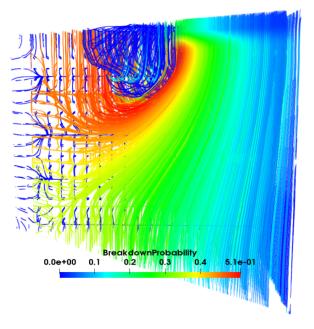


Figure 2: Breakdown Probability computed over multiple streamlines

to reach the avalanche zone, we assume that this zone is represented by the location of the maximum electric field, denoted $x_{E_{max}}$. Let $f:(x,t)\mapsto f(x,t)$ the probability distribution of an electron presence. We assume that the electron will drift and diffuse along the electric field streamline. At time t = 0, f is a Dirac distribution, in order to be able to compute a solution numerically, we start a time $t_0 = \delta t$ where δt is as small as possible. We then take the assumption that $D(x) = D(x_0)$ and $u(x) = u(x_0)$, and that the analytical solution applies for a very small time interval δt .

Hence, f verify the following equation:

$$\forall x \in [x_s, x_{max}] \text{ and } t \in [t_0, T]$$

$$\frac{\partial f}{\partial t}(x,t) = -\frac{\partial (u \cdot f)}{\partial x}(x,t) + \frac{\partial}{\partial x} \left(D \cdot \frac{\partial f}{\partial x} \right)(x,t) \tag{8}$$

With the following initial condition:

$$f(x, t = t_0) = \frac{1}{\sqrt{4\pi D(x_0)t_0}} exp(-\frac{(x - v(x_0)t_0)^2}{4D(x_0)t_0}$$
(9)

And setting an absorbing boundary condition at $x_s = x_{E_{max}}$:

$$\frac{\partial f}{\partial t}(x_s, t) = \frac{\partial}{\partial x} \left(D \cdot \frac{\partial (f)}{\partial x} \right) (x_s, t) \tag{10}$$

The velocity is computed throw a high field saturation model and the diffusion throw the Einstein relation $D = \frac{\mu k_B T}{a}$, these quantities are represented in figure 3. Let T_e the time for the electron to reach the avalanche region. It is straightforward that the probability that T is less than t is:

$$P_r(t < T) = F_{P_r}(t) = 1 - \int_{x_0}^{x_{max}} f(x, t) dx$$

From this cumulative distribution function, we can find back the distribution of T:

$$f_T(t) = \frac{F_{Pr}(t)}{dt} \tag{11}$$

The equation is solved by the mean of the finite difference method, more precisely we use a modified Crank-Nicholson method that takes into account the variable velocity and diffusion.

Results and comparisons with experiments

To validate our models, the simulation results were compared with experimental measurements performed on the wide range of architecture

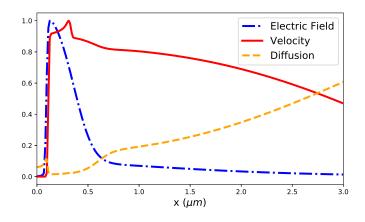


Figure 3: Normalized Electric Field, Velocity and Diffusion along a streamline

variants described in section 2. Where appropriate measurements are repeated on several SPADs to reduce the variability of the results and the values given are the median.

We first present the PDE results derived from the avalanche breakdown probability simulations described in section **??**. A key visualisation of the result is the 2D BrP colour map, which is generated for two different architectures with varying junction size in figure 4.

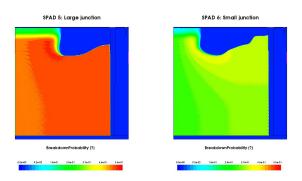


Figure 4: 2D colour maps of BrP from two design variations with a larger and a smaller avalanche region. BrP is shown at BV+4V and at 333K

To verify that the prediction of higher PDE for a smaller junction size is physical, we can extract the integrated PDE for a range of voltages. The resulting curve is shown for the three architectures with junctions of various widths overlaid by the appropriate experimental results in figure 5. The PDE values are obtained by multiplying the breakdown avalanche probability by an average optical absorption, found to be 26% for this architecture.

The comparison was extended to a large set of fifteen different architectures with electric field profiles selected for their wide range of electrostatic characteristics. As shown in figure 6, which presents the correlation between simulation and experiment at 4V excess voltage, the model was able to predict a significant range of PDE correctly.

An additional strength of the avalanche breakdown probability methodology is that the breakdown voltage (BV) can be extracted from the PDE vs voltage curve. We define that the device has broken down when a threshold PDE of 1e-5 is reached, as shown in figure 7. The extraction was performed on the same fifteen diodes and the results, shown in figure 8, show the resulting correlation. In particular, the extreme breakdown voltages engineered for SPADs 3 and 4 are predicted by the model with ease.

Finally a comparison between characterisation and simulation was made for the jitter model. Figure 9 shows two diodes engineered to exhibit varying jitter profiles. In order to verify that jitter model is valid for a wide range of architectures, the pitch size and a variety of layout parameters were varied. The experimental results for the large diode show a strong initial peak and a rapid decrease in jitter. In contrast, the

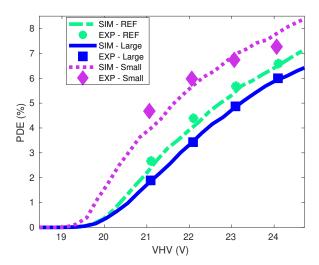


Figure 5: Comparison of PDE measured and simulated on three architecture variations

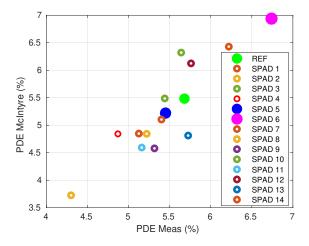


Figure 6: Correlation between simulation and experiment for PDE at BV+4V at 333K for 15 diodes of varying architecture

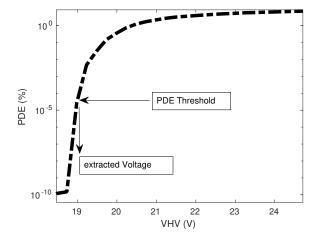


Figure 7: Method for BV extraction; the threshold defining breakdown is defined at a PDE of 1e-5

smaller diode has a much weaker initial peak and a second smaller peak leading to a longer jitter tail. These characteristics are well represented in the simulation results, which shows a second peak for the smaller diode and longer tail. The crossing point between the two curves is also clearly indicated.

The good predictivity shown by both the BrP and jitter models in pre-

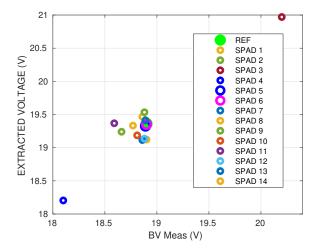


Figure 8: Correlation between simulation and measurements for BV at 333K for 15 diodes of varying architecture

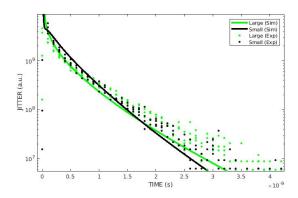


Figure 9: Comparison between characterisation and simulation for jitter on a 10um and 5um diode at BV+4V

dicting PDE, BV and jitter characteristics is essential to confirm a clear understanding of the device physics. Furthermore, it allows for many iterations of design optimization without the need for silicon.

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