Sheet: Power_Supply

POWER SUPPLY

File: Power_Supply.sch

Sheet: Microcontroller

MICROCONTROLLER

File: Microcontroller.sch

IO-LINK drivers

Sheet: IO_LINK_0

IO-LINK TRANSCEIVER 0

File: IO_LINK_0.sch

Sheet: IO_LINK_1

IO-LINK TRANSCEIVER 1

File: IO_LINK_1.sch

Sheet: IO_LINK_2

IO-LINK TRANSCEIVER 2

File: IO_LINK_2.sch

Sheet: IO_LINK_3

IO-LINK TRANSCEIVER 3

File: IO_LINK_3.sch

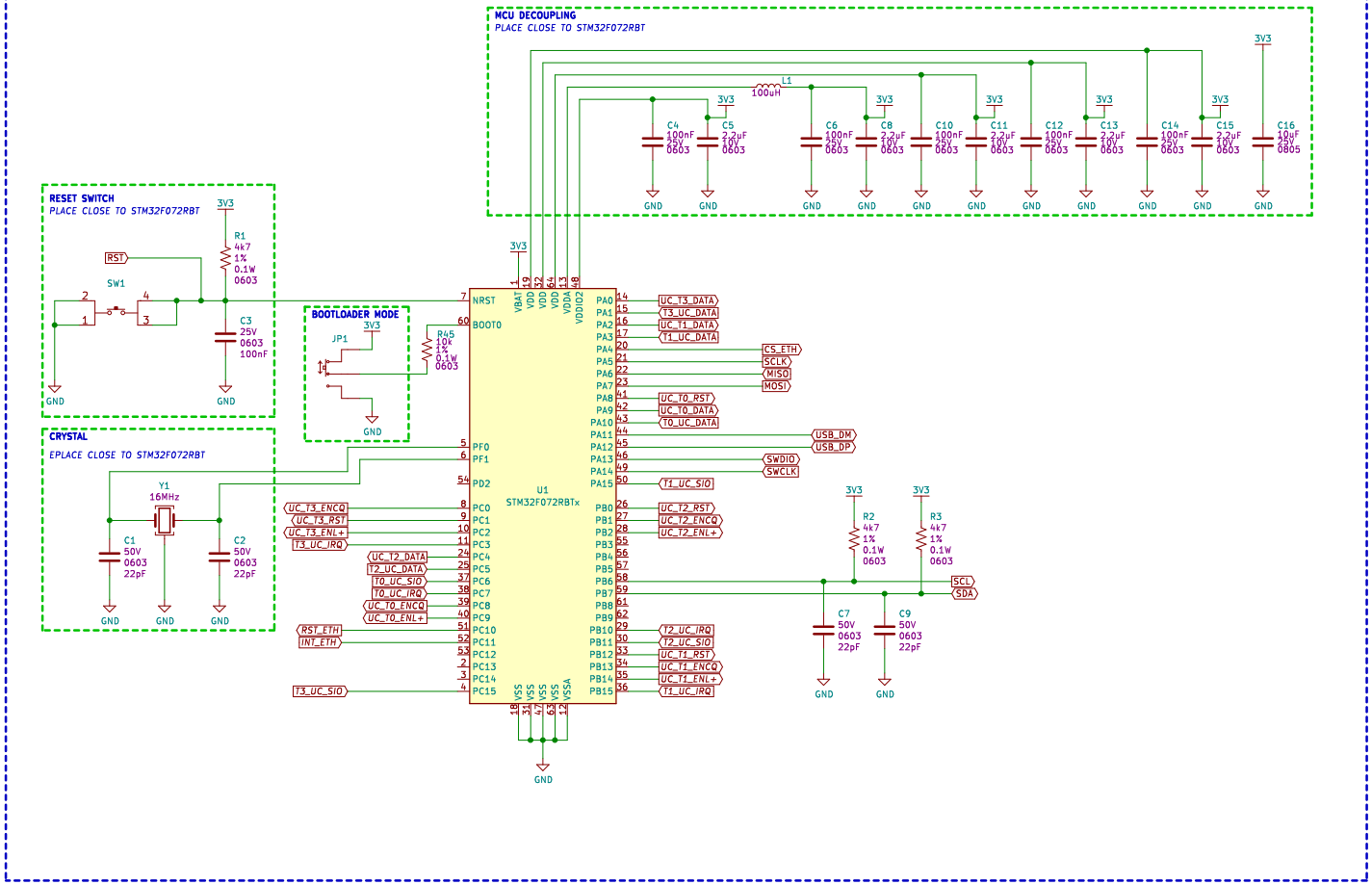
H1 H2 H3 H4

Sheet: /
File: IO_LINK.sch

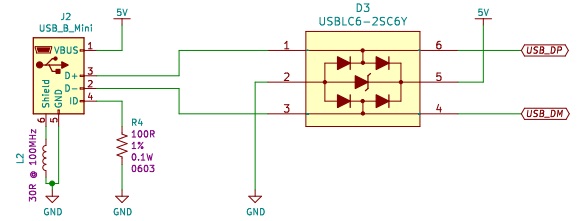
Title: *IO-LINK MASTER board*

Size: A4	Date: *22.01.2021*	**Rev:**
KiCad E.D.A. kicad (5.1.9)-1		Id: 1/7

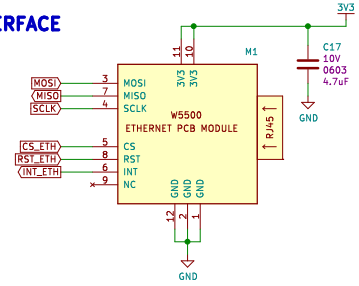
MICROCONTROLLER



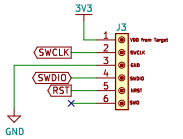
USB INTERFACE



ETHERNET INTERFACE

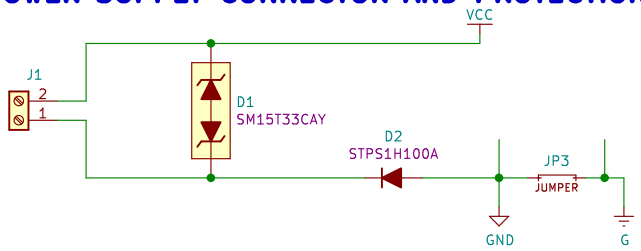


PROGRAMING INTERFACE

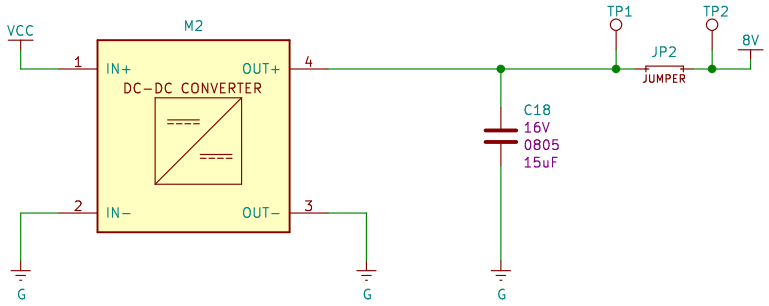


Sheet: /Microcontroller/	
File: Microcontroller.sch	
Title: IO-LINK MASTER board / microcontroller block	
Size: User	Date: 22.02.2021
KiCad E.D.A. kicad (5.1.9)-1	Id: 2/7
	Rev:

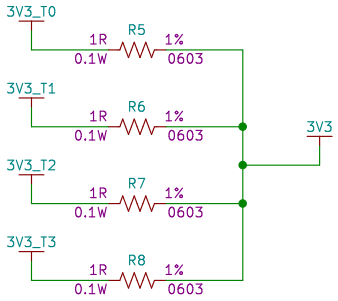
POWER SUPPLY CONNECTOR AND PROTECTION CIRCUIT



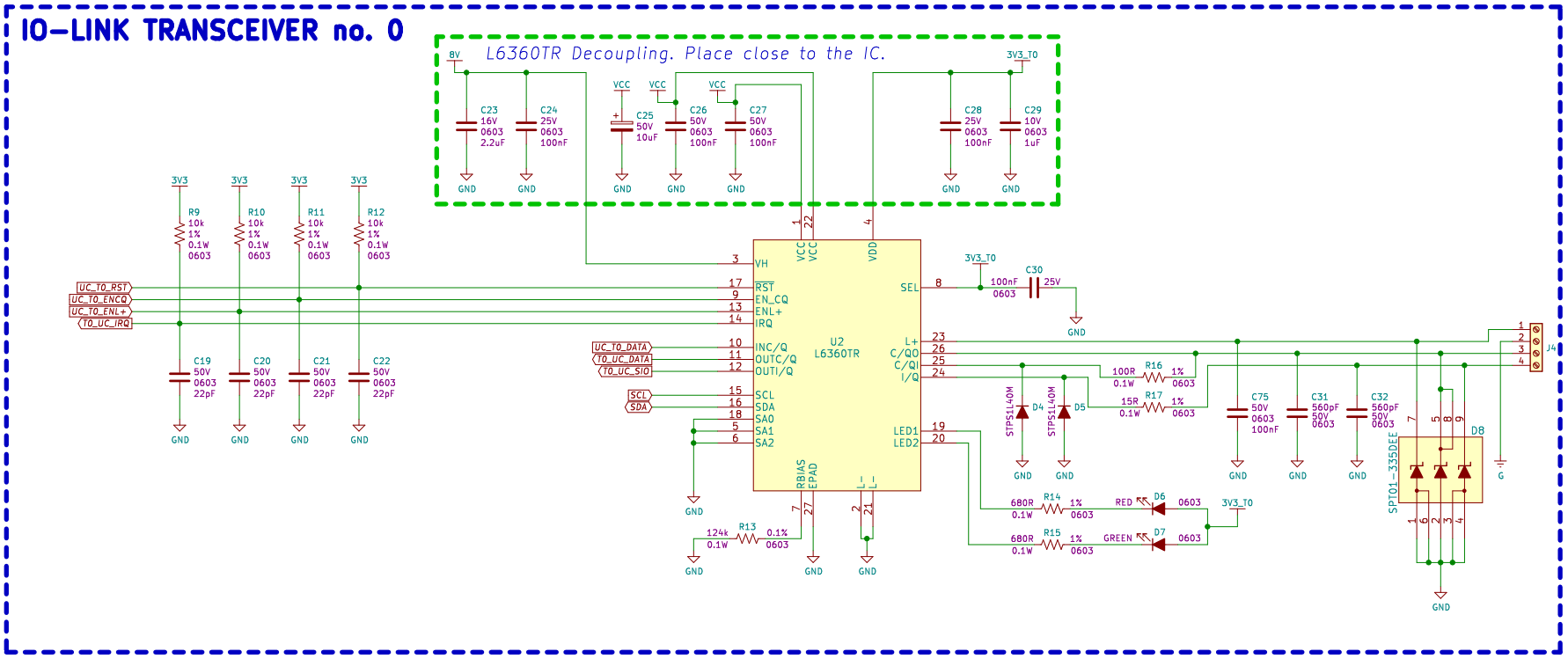
DC–DC SMPS POWER SUPPLY SECTION



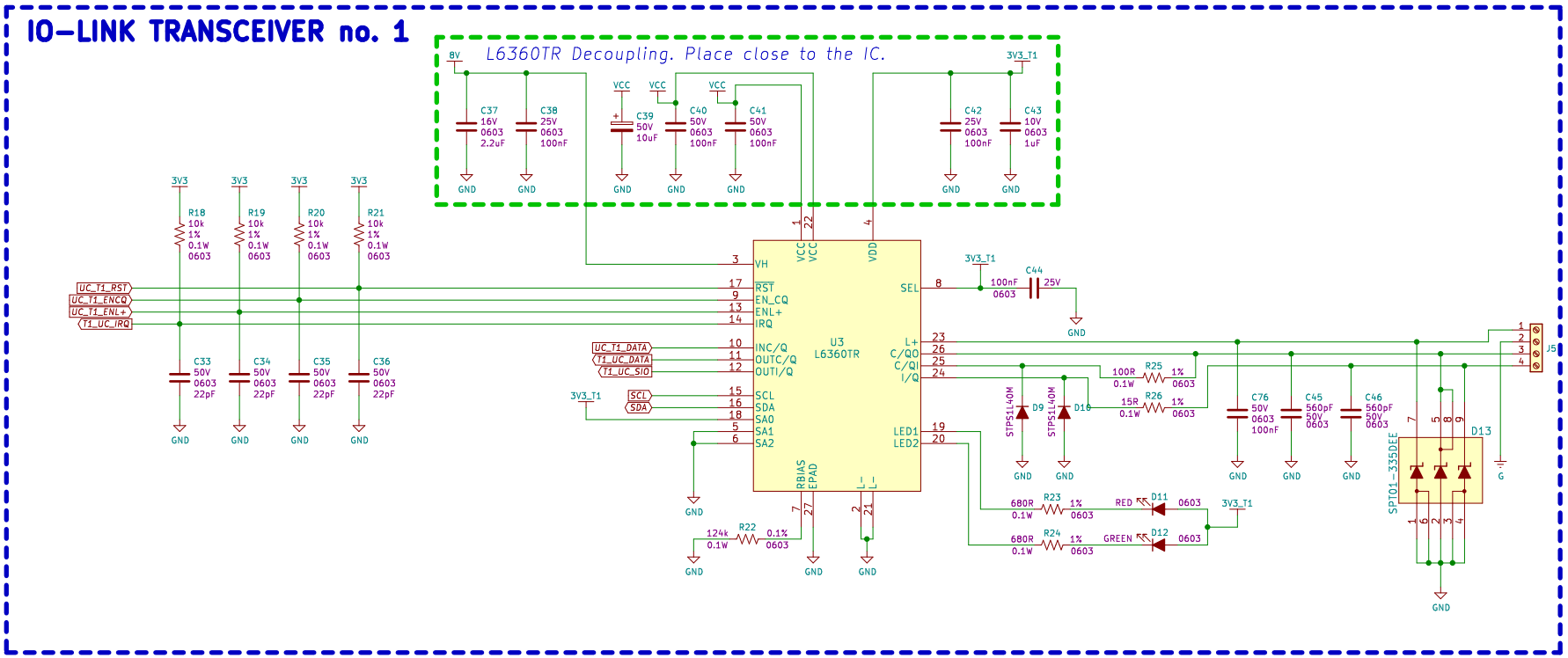
Each IO_Link transceiver contain its own LDO and their outputs are parallelized:



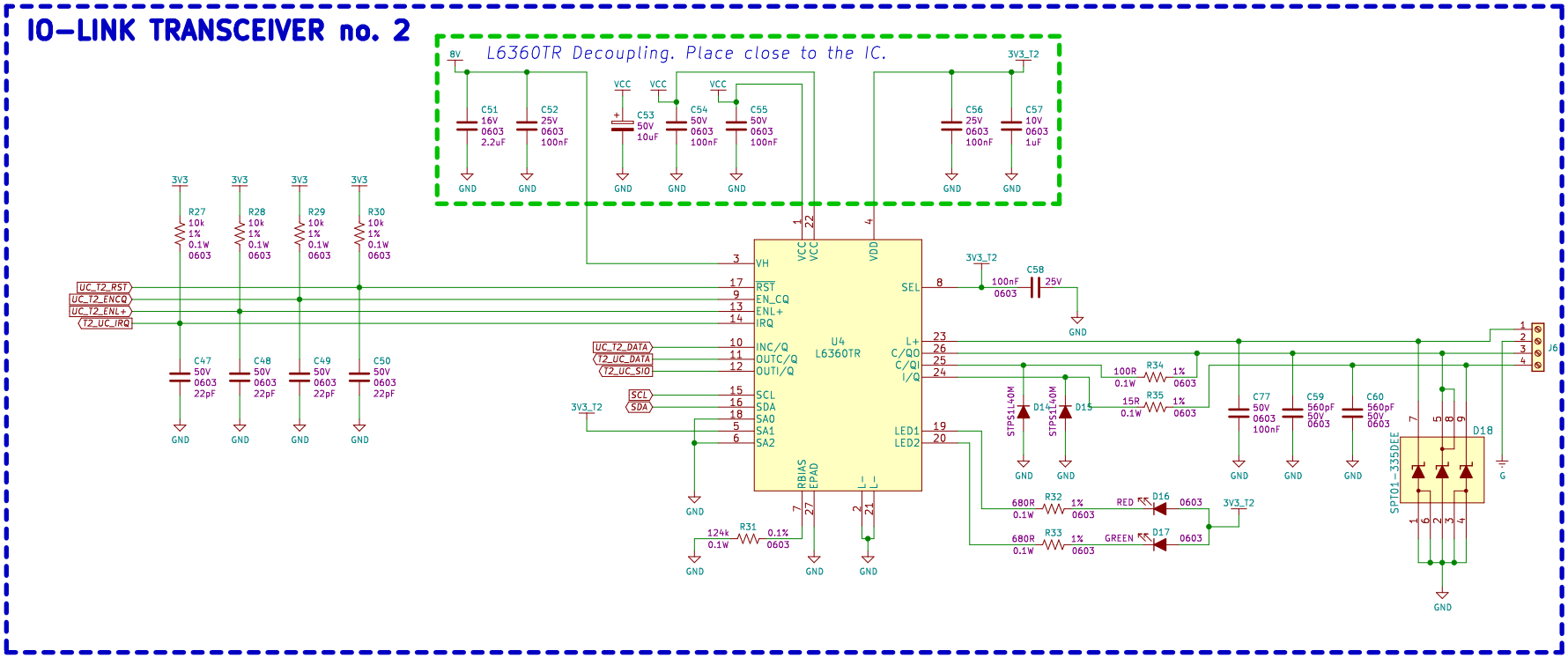
Sheet: /Power_Supply/ File: Power_Supply.sch		
Title: *IO–LINK MASTER board / power supply and protection circuit*		
Size: A4	Date: *19.02.2021*	**Rev:**
KiCad E.D.A. kicad (5.1.9)–1		Id: 3/7



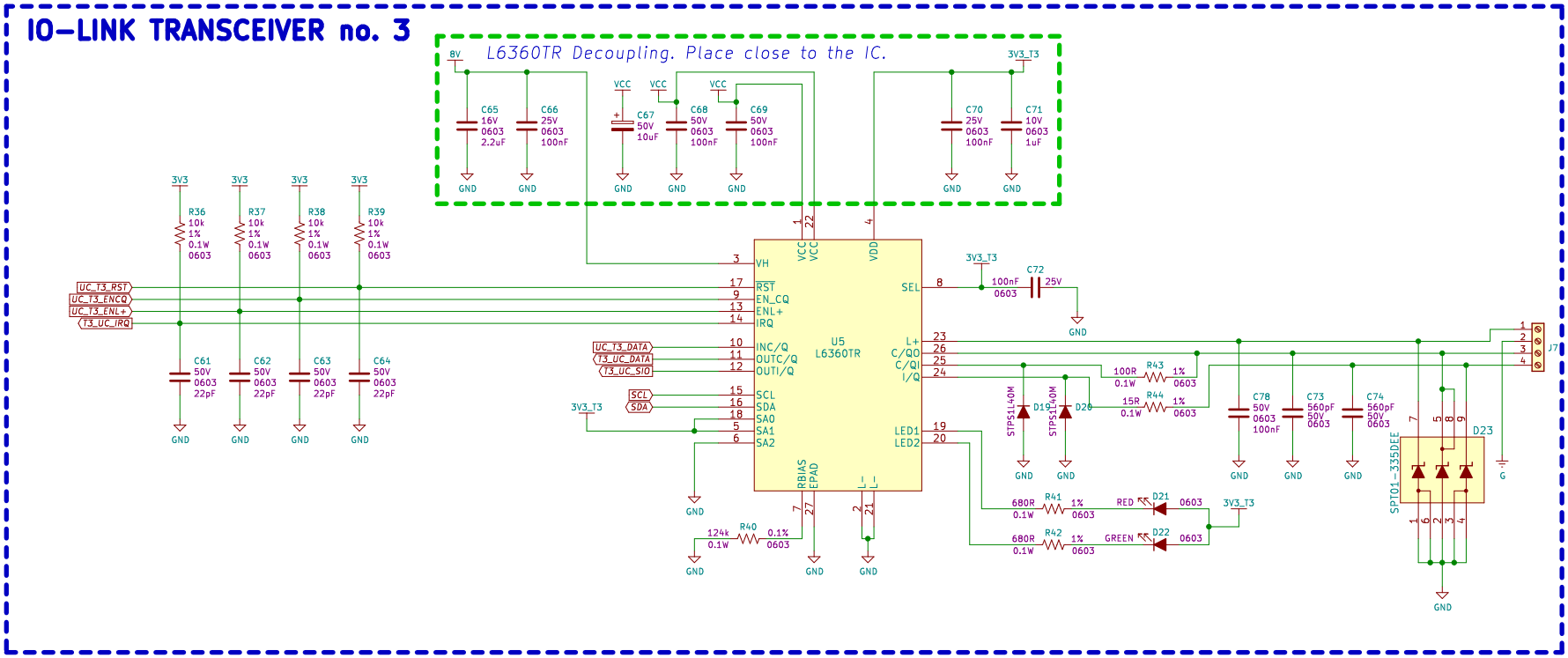
Sheet: /IO_LINK_0/		
File: IO_LINK_0.sch		
Title: IO-LINK MASTER board / IO-Link driver no. 0		
Size: User	Date: 22.02.2021	**Rev:**
KiCad E.D.A. kicad (5.1.9)-1		Id: 4/7



Sheet: /IO_LINK_1/		
File: IO_LINK_1.sch		
Title: IO-LINK MASTER board / IO-Link driver no. 1		
Size: User	Date: 22.02.2021	**Rev:**
KiCad E.D.A. kicad (5.1.9)-1		Id: 5/7



Sheet: /IO_LINK_2/		
File: IO_LINK_2.sch		
Title: IO-LINK MASTER board / IO-Link driver no. 2		
Size: User	Date: 22.02.2021	**Rev:**
KiCad E.D.A. kicad (5.1.9)-1		Id: 6/7



Sheet: /IO_LINK_3/		
File: IO_LINK_3.sch		
Title: IO-LINK MASTER board / IO-Link driver no. 3		
Size: User	Date: 22.02.2021	**Rev:**
KiCad E.D.A. kicad (5.1.9)-1		Id: 7/7