








Renan LARRIEU

Electrical Engineering Student

 [linkedin.com/in/renanlarrieu](https://www.linkedin.com/in/renanlarrieu)  github.com/renanlarrieu
 +55 21 991202579  renanlarrieu@gmail.com
 Rua francisco bernardino 51, AP301F
 Brazilian, live in Rio de Janeiro, 20 years  CGPA : 2.844

Passionate researcher. Experience with datascience (Python), PCB designs (EAGLE), electric/electronics system's simulation (Proteus), Embedded systems and Arduino (C++), and FPGA hardware designs (Xilinx/VIVADO).
















SKILLS

Programming	Python , C++ , Verilog
Software experience	Proteus, Eagle, Arduino IDE, Vivado
Operating systems	Windows 7/8/10, Linux Mint/Ubuntu

EXTRACURRICULAR ACTIVITIES

Current August 2018	Avionics Manager, UERJ, Rio de Janeiro > Avionics manager in a Extra-curricular activity that is a rocket team. I've been managing the avionics subsystem, designing and improving electronics technology of our rocket.
Current February 2020	FPGA researcher , UERJ-LEPAT, Rio de Janeiro > Real time simulation of Power Electronics. I design hardware to simulate power electronics semiconductors/phenomena to understand its behavior in a real time way.

LANGUAGES

Portuguese	    
English	    
Spanish	    



FORCES

- > Hardworking
- > Quicklearner
- > Leadership team

FORMATION

2018-Current B.Sc. Electrical Engineering at State University of Rio de Janeiro (5º Semester)

REFERENCES

Luís Fernando Corrêa Monteiro
Department Chief, DETEL-UERJ
 lmonteiro@eng.uerj.br
 +55 21 982846405

Gil Roberto Vieira Pinheiro
Department Teacher, DETEL-UERJ
 gilp@uerj.br
 +55 21 997957525