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(0x08000200-0x08007A50)
                           RTT active
1 Bugs in the book's *.jdeb
                           Debug.SetConnectMode (CM DOWNLOAD RESET);
2 Problems in running Ozo
                           Debug.Start();
 2.1 Debug-session does
                          Device "STM32F767ZI" selected.
 2.2 Ozone does not exit
                          Found SW-DP with ID 0x9BA02477
3 Troubleshooting debugge
                           Found SW-DP with ID 0x9BA02477
 3.1 Console output for a
                          DPv0 detected
                           Scanning AP map to find all available APs
                           AP[1]: Stopped AP scan as end of AP map has been reached
                           AP[0]: AHB-AP (IDR: 0x74770001)
                           Iterating through AP map to find AHB-AP to use
                           AP[0]: Core found
                           AP[0]: AHB-AP ROM base: 0xE00FD000
                           CPUID register: 0x411FC270. Implementer code: 0x41 (ARM)
                           Found Cortex-M7 r1p0, Little endian.
                           FPUnit: 8 code (BP) slots and 0 literal slots
                           CoreSight components:
                           ROMTb1[0] @ E00FD000
                           ROMTb1[0][0]: E00FE000, CID: B105100D, PID: 000BB4C8 ROM Table
                           ROMTb1[1] @ E00FE000
                          ROMTb1[1][0]: E00FF000, CID: B105100D, PID: 000BB4C7 ROM Table
                           ROMTb1[2] @ E00FF000
                           ROMTb1[2][0]: E000E000, CID: B105E00D, PID: 000BB00C SCS-M7
                           ROMTb1[2][1]: E0001000, CID: B105E00D, PID: 000BB002 DWT
                           ROMTb1[2][2]: E0002000, CID: B105E00D, PID: 000BB00E FPB-M7
                           ROMTb1[2][3]: E0000000, CID: B105E00D, PID: 000BB001 ITM
                           ROMTb1[1][1]: E0041000, CID: B105900D, PID: 001BB975 ETM-M7
                          ROMTb1[0][1]: E0040000, CID: B105900D, PID: 000BB9A9 TPIU-M7
                           Cache: Separate I- and D-cache.
                           I-Cache L1: 16 KB, 256 Sets, 32 Bytes/Line, 2-Way
                           D-Cache L1: 16 KB, 128 Sets, 32 Bytes/Line, 4-Way
                           Connected to target device.
                           Reset: Halt core after reset via DEMCR.VC CORERESET.
                           Reset: Reset device via AIRCR.SYSRESETREQ.
                          Elf.GetBaseAddr(); // returns 0x8000000
                           Target.ReadU32 (0x08000000); // returns 0x20080000
                           Target.SetReg ("SP", 0x20080000);
                           Target.ReadU32 (0x08000004); // returns 0x8007959
                           Target.SetReg ("PC", 0x8007959);
                           J-Link: Flash download: Bank 0 @ 0x08000000: Skipped. Contents already match
                          Elf.GetBaseAddr(); // returns 0x8000000
                           Target.ReadU32 (0x08000000); // returns 0x20080000
                           Target.SetReg ("SP", 0x20080000);
                           Target.ReadU32 (0x08000004); // returns 0x8007959
                           Target.SetReg ("PC", 0x8007959);
                           Startup complete (PC=0x080076C8)
                           Executed J-Link command "SetRTTAddr 0x2000535c"
                           Debug.Continue();
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