Product Update

Errata for Z8430/Z84C30 NMOS/CMOS Z80 CTC Counter/Timer Circuit Devices

UP010001-0607

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This Errata provides information on the 44-lead QFP which has been replaced by the 44-lead LQFP.

Affected Documents

Table 1 provides the list of documents affected by the replacement of 44-lead QFP with 44-lead LQFP.

Table 1. Documents Affected by the Replacement of 44-Lead QFP with 44-Lead LQFP

Devices	Title	Document Number
Z8430/Z84C30	Z8430/Z84C30 NMOS/CMOS Z80 CTC Counter/Timer Circuit Product Specification	PS0181

44-Lead LQFP

The Z8430/Z84C30 NMOS/CMOS Z80 CTC Counter/Timer Circuit Devices are now available in 44-lead LQFP. Figure 1 displays the pin diagram and Table 2 provides the pin description of the 44-lead LQFP.
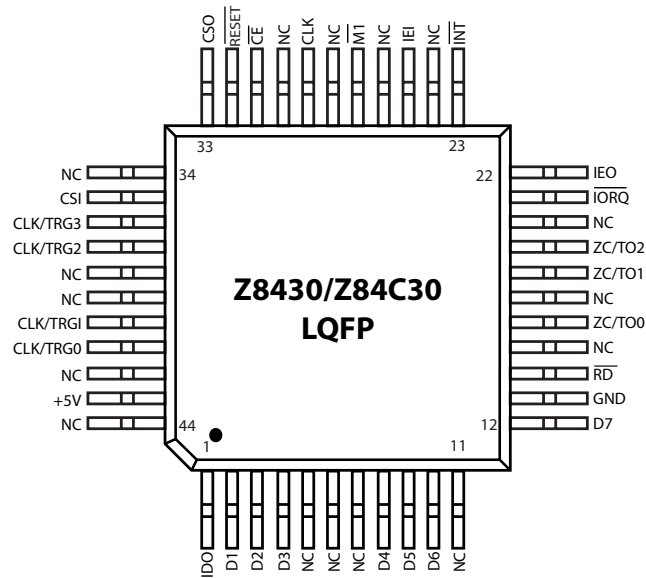

Figure 1. 44-Lead Z8430/Z84C30 LQFP

Table 2. 44-Lead Z8430/Z84C30 LQFP Description

Pin No	Symbol	Function	Direction
1–4	D0–D3	Data Bus 0,1,2,3	Input/Output
5–7	NC	Not Connected	N/A
8–10	D4–D6	Data Bus 4, 5, 6	Input/Output
11	NC	Not Connected	N/A
12	D7	Data Bus 7	Input/Output
13	GND	Ground	Input
14	$\overline{\text{RD}}$	Read Cycle Status	Input
15	NC	Not Connected	N/A
16	ZC/TO0	Zero Count TimeOut Pulse	Output
17	NC	Not Connected	N/A
18–19	ZC/TO1–ZC/TO2	Zero Count TimeOut Pulse	Output
20	NC	Not Connected	N/A
21	$\overline{\text{IORQ}}$	Input/Output Request	Input
22	IEO	Interrupt Enable Out	Output
23	$\overline{\text{INT}}$	Interrupt Request	Output
24	NC	Not Connected	N/A
25	IEI	Interrupt Enable In	Input
26	NC	Not Connected	N/A
27	$\overline{\text{M1}}$	Machine Cycle 1	Input
28	NC	Not Connected	N/A
29	CLK	Clock	Input
30	NC	Not Connected	N/A
31	$\overline{\text{CE}}$	Chip Enable	Input
32	$\overline{\text{RESET}}$	Reset	Input
33	CS0	Chip Select 0	Input
34	NC	Not Connected	N/A
35	CS1	Chip Select 1	Input
36–37	CLK/TRG3–CLK/TRG2	Clock Trigger 3, 2	Input
38–39	NC	Not Connected	N/A
40–41	CLK/TRG1–CLK/TRG0	Clock Trigger 1, 0	Input
42	NC	Not Connected	N/A
43	+5V	Power Supply	Input
44	NC	Not Connected	N/A

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