

CD4030B Types

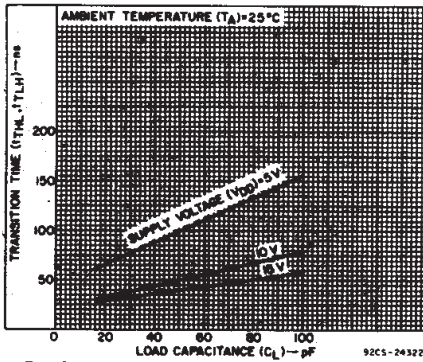


Fig. 6 - Typical transition time as a function of load capacitance.

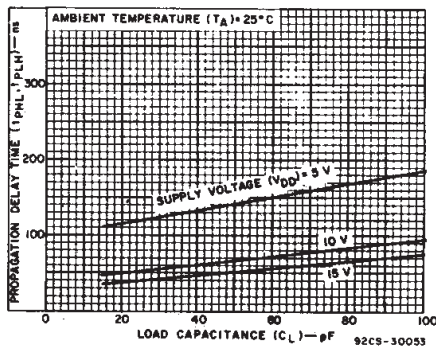


Fig. 7 - Typical propagation delay time as a function of load capacitance.

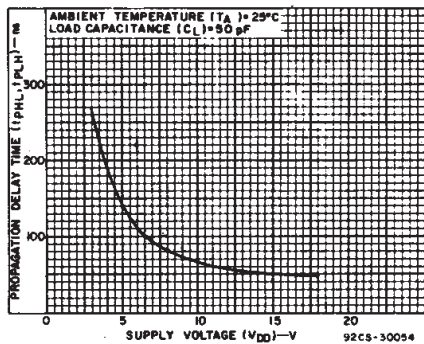


Fig. 8 - Typical propagation delay time as a function of supply voltage.

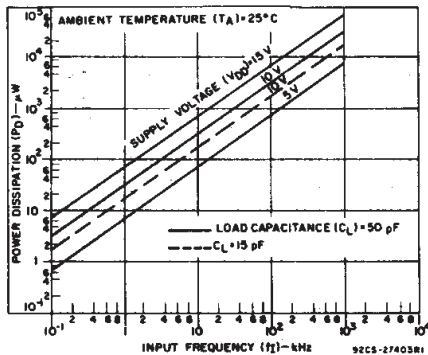


Fig. 9 - Typical dynamic power dissipation as a function of input frequency.

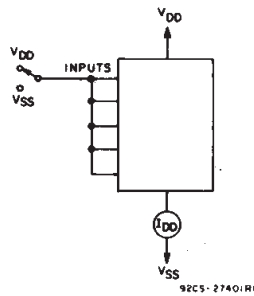


Fig. 10 - Quiescent-device current test circuit.

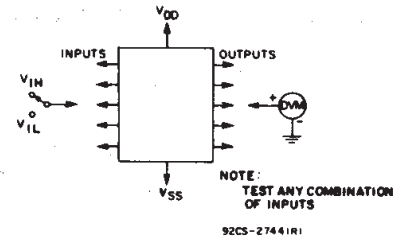


Fig. 11 - Input-voltage test circuit.

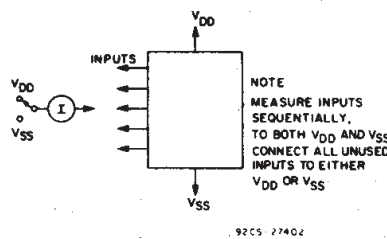


Fig. 12 - Input-current test circuit.

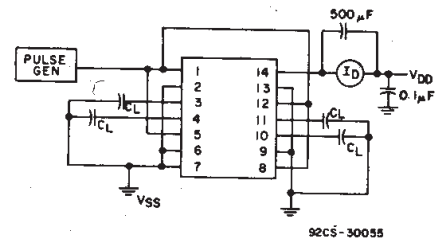
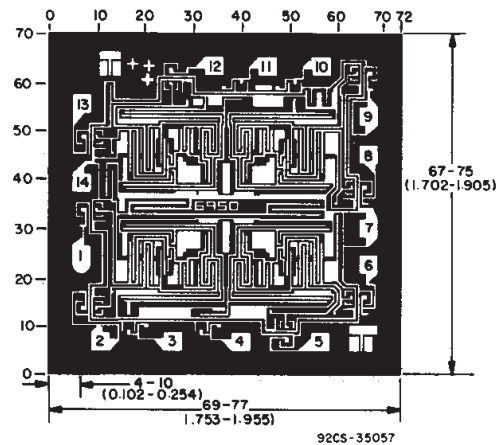


Fig. 13 - Dynamic power dissipation test circuit.



Dimensions and pad layout for CD4030BH.

Dimensions in parentheses are in millimeters and are derived from the basic inch dimensions as indicated. Grid graduations are in mils (10^{-3} inch).