RZ/A2M Group

RZ/A2M MMU Driver

R01AN4498EG0100 Rev.1.0 Sept 20, 2018

Introduction

This application note describes the operation of the software MMU Driver for the RZ/A2 device on the RZ/A2M CPU Board.

It provides a comprehensive overview of the driver. For further details please refer to the software driver itself.

The user is assumed to have knowledge of e² studio and to be equipped with an RZ/A2M CPU Board.

Target Device

RZ/A2M Group

Driver Dependencies

This driver has no other driver dependencies.

Referenced Documents

Document Type	Document Name	Document No.
User's Manual	RZ/A2M Hardware Manual	R01UH0746EJ

List of Abbreviations and Acronyms

Abbreviation	Full Form
API	Application Programming Interface
ARM	Advanced RISC Machines
CPU	Central Processing Unit
IDE	Integrated Development Environment
LLD	Low Layer Driver
MMU	Memory Management Unit
TLB	Translation Lookaside Buffer

Table 1-1 List of Abbreviations and Acronyms

Contents

1. (Outline of Software Driver	3
2. [Description of the Software Driver	4
	Structure	
	Description of each file	
	Low Layer Driver	
3. E	Example of Use	7
3.1	Initialise MMU Driver	7
3.2	Write to the Translation Table	7
3.3	MMU Enable	7
4. (OS Support	8
5. H	How to Import the Driver	9
5.1	e ² studio	g
Webs	site and Support	10

1. Outline of Software Driver

The MMU (Memory Management Unit) driver is an abstraction layer between the application and the memory management facilities within the ARM Cortex-A9 microcontroller core.

For more information regarding the Cortex-A9 memory management unit, please refer to the **ARM Architecture Reference Manual** issued by ARM Ltd.

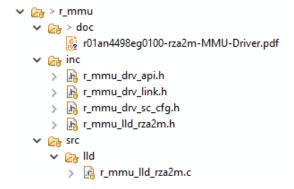
2. Description of the Software Driver

The key features of the driver include:

- writing entries in the address translation table
- reading entries from the address translation table
- provides a means to call TLB maintenance operations
- enable and disable the MMU

2.1 Structure

Unlike many of the other drivers, the MMU driver currently consists of a single layer: the Low Layer Driver (LLD). This includes all the hardware specific functions and provides the API to the application.



2.2 Description of each file

Each file's description can be seen in the following table.

Filename	Usage	Description	
Application-Facing Driver API			
r_mmu_drv_api.h Application ** FOR FUTURE USE **			
	High L	Layer to Low Layer API	
r_mmu_lld_xxxx.h	r_mmu_lld_xxxx.h API header file Low Layer Driver (LLD) header file (where "xxxx" is a device and board-specific identification).		
		This is the header file to include in application code.	
Abstraction Link between High and Low Layer Drivers (HLD/LLD Link)			
r_mmu_drv_link.h	Private (HLD/LLD only)	** FOR FUTURE USE **	
Low Layer Driver (LLD) Source			
r_mmu_lld_xxxx.c	Private (LLD only)	(Where "xxxx" is a device and board specific identification). Provides the definitions for the Low Layer Driver interface.	
Smart Configurator			
r_mmu_drv_sc_cfg.h	Private (LLD only)	This file is intended to be used by Smart Configurator to pass setup information to the driver. This is not for application use	

2.3 Low Layer Driver

The Low Layer Driver provides the functions to configure the hardware.

Return Type	Function	Description	Arguments	Return
void	R_MMU_Init(void)	Initialise the MMU driver	None	None
e_mmu_err_t	R_MMU_WriteTbl(uint 32_t vaddress, uint32_t paddress, uint32_t size, uint32_t entry)	Write MMU translation table entry	vaddress: [in] virtual address to be mapped paddress: [in] physical address to be mapped size: [in] length in megabytes entry: [in] table entry value	MMU_SUCCESS or MMU_ERR_OVER FLOW
uint32_t	R_MMU_ReadTbl(uint 32_t address)	Read MMU translation table entry	address: [in] virtual address to read	MMU table entry value
void	R_MMU_Disable(void)	Disable the MMU	None	None
void	R_MMU_Enable(void)	Enable the MMU	None	None
e_mmu_err_t	R_MMU_VAtoPA(uint 32_t vaddress, uint32_t * paddress)	Convert a virtual address to a physical address	vaddress: [in] virtual address paddress: [out] physical address	MMU_SUCCESS or MMU_ERR_TRAN SLATION
void	R_MMU_TLBIALL(vo id)	Executes the Cortex-A9 TLBIALL TLB maintenance operation	None	None
void	R_MMU_TLBIMVAA(uint32_t vaddress)	Executes the Cortex-A9 TLBIMVAA TLB maintenance operation	vaddress: [in] virtual address	None

3. Example of Use

This section gives simple examples for initialising the driver, writing a value to the translation table, and enabling the MMU.

3.1 Initialise MMU Driver

```
result = R_MMU_Init();
```

3.2 Write to the Translation Table

```
mmu_err_t result;
uint32_t vaddress = 0x80000000uL;
uint32_t paddress = 0x80000000uL;
uint32_t size = 64;
uint32_t entry = MMU_ATTR_NORMAL_L1CACHE | MMU_ATTR_DOMAIN(15);
result = R_MMU_WriteTbl(vaddress, paddress, size, entry);
```

3.3 MMU Enable

```
R_MMU_Enable();
```

4. OS Support

This driver supports any OS.

5. How to Import the Driver

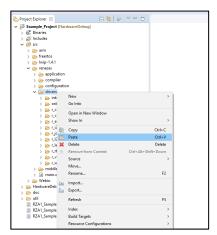
This section describes how to import the driver into your project. Generally, there are two steps in any IDE:

- 1) Copy the software driver to the location in the source tree that you require for your project.
- 2) Add the include path of the driver to the compiler.

5.1 e² studio

To import the driver into your project please follow the instructions below.

- In Windows Explorer, right-click on the r_mmu folder, and click Copy.
- 2) In e² studio Project Explorer view, select the folder where you wish the driver project to be located; right-click and click **Paste**.
- 3) Right-click on the parent project folder (in this case 'Example_Project') and click **Properties ...**
- 4) In 'C/C++ Build → Settings → Cross ARM Compiler → Includes', add the include folder of the newly added driver, e.g.
 '\${ProjDirPath}\src\renesas\drivers\r_mmu\inc'



Website and Support

Renesas Electronics website https://www.renesas.com/

Inquiries

https://www.renesas.com/contact/

All trademarks and registered trademarks are the property of their respective owners.

Revision History

Description

Rev.	Date	Page	Summary	
1.00	Sept 20, 2018	All	Created document.	

General Precautions in the Handling of Microprocessing Unit and Microcontroller Unit Products

The following usage notes are applicable to all Microprocessing unit and Microcontroller unit products from Renesas. For detailed usage notes on the products covered by this document, refer to the relevant sections of the document as well as any technical updates that have been issued for the products.

1. Handling of Unused Pins

Handle unused pins in accordance with the directions given under Handling of Unused Pins in the manual

34 The input pins of CMOS products are generally in the high-impedance state. In operation with an unused pin in the open-circuit state, extra electromagnetic noise is induced in the vicinity of LSI, an associated shoot-through current flows internally, and malfunctions occur due to the false recognition of the pin state as an input signal become possible. Unused pins should be handled as described under Handling of Unused Pins in the manual.

2. Processing at Power-on

The state of the product is undefined at the moment when power is supplied.

- 3/4 The states of internal circuits in the LSI are indeterminate and the states of register settings and pins are undefined at the moment when power is supplied.
 In a finished product where the reset signal is applied to the external reset pin, the states of pins are not guaranteed from the moment when power is supplied until the reset process is completed.
 In a similar way, the states of pins in a product that is reset by an on-chip power-on reset function are not guaranteed from the moment when power is supplied until the power reaches the level at which resetting has been specified.
- 3. Prohibition of Access to Reserved Addresses

Access to reserved addresses is prohibited.

3/4 The reserved addresses are provided for the possible future expansion of functions. Do not access these addresses; the correct operation of LSI is not guaranteed if they are accessed.

4. Clock Signals

After applying a reset, only release the reset line after the operating clock signal has become stable. When switching the clock signal during program execution, wait until the target clock signal has stabilized.

When the clock signal is generated with an external resonator (or from an external oscillator) during a reset, ensure that the reset line is only released after full stabilization of the clock signal. Moreover, when switching to a clock signal produced with an external resonator (or by an external oscillator) while program execution is in progress, wait until the target clock signal is stable.

5. Differences between Products

Before changing from one product to another, i.e. to a product with a different part number, confirm that the change will not lead to problems.

34 The characteristics of Microprocessing unit or Microcontroller unit products in the same group but having a different part number may differ in terms of the internal memory capacity, layout pattern, and other factors, which can affect the ranges of electrical characteristics, such as characteristic values, operating margins, immunity to noise, and amount of radiated noise. When changing to a product with a different part number, implement a system-evaluation test for the given product.

Notice

- criptions of circuits, software and other related information in this document are provided only to illustrate the operation of semiconductor products and application examples. You are fully resp the incorporation or any other use of the circuits, software, and information in the design of your product or system. Renesas Electronics disclaims any and all liability for any losses and damages incurred by you or third parties arising from the use of these circuits, software, or information
- 2. Renesas Electronics hereby expressly disclaims any warranties against and liability for infringement or any other claims involving patents, copyrights, or other intellectual property rights of third parties, by or arising from the use of Renesas Electronics products or technical information described in this document, including but not limited to, the product data, drawings, charts, programs, algorithms, and application
- 3. No license, express, implied or otherwise, is granted hereby under any patents, copyrights or other intellectual property rights of Renesas Electronics or others.
- 4. You shall not alter, modify, copy, or reverse engineer any Renesas Electronics product, whether in whole or in part. Renesas Electronics disclaims any and all liability for any losses or damages incurred by you or third parties arising from such alteration, modification, copying or reverse engineering.
- 5. Renesas Electronics products are classified according to the following two quality grades: "Standard" and "High Quality". The intended applications for each Renesas Electronics product depends on the product's quality grade, as indicated below.
 - Computers; office equipment; communications equipment; test and measurement equipment; audio and visual equipment; home electronic appliances; machine tools; personal electronic equipment: industrial robots: etc.

"High Quality": Transportation equipment (automobiles, trains, ships, etc.); traffic control (traffic lights); large-scale communication equipment; key financial terminal systems; safety control equipment; etc. Unless expressly designated as a high reliability product or a product for harsh environments in a Renesas Electronics data sheet or other Renesas Electronics document, Renesas Electronics products are not intended or authorized for use in products or systems that may pose a direct threat to human life or bodily injury (artificial life support devices or systems; surgical implantations; etc.), or may cause serious property damage (space system; undersea repeaters; nuclear power control systems; aircraft control systems; key plant systems; military equipment; etc.). Renesas Electronics disclaims any and all liability for any damages or losses incurred by you or any third parties arising from the use of any Renesas Electronics product that is inconsistent with any Renesas Electronics data sheet, user's manual or other Renesas Electronics document.

- 6. When using Renesas Electronics products, refer to the latest product information (data sheets, user's manuals, application notes, "General Notes for Handling and Using Semiconductor Devices" in the reliability handbook, etc.), and ensure that usage conditions are within the ranges specified by Renesas Electronics with respect to maximum ratings, operating power supply voltage range, heat dissipation characteristics, installation, etc. Renesas Electronics disclaims any and all liability for any malfunctions, failure or accident arising out of the use of Renesas Electronics products outside of such specified
- 7. Although Renesas Electronics endeavors to improve the quality and reliability of Renesas Electronics products, semiconductor products have specific characteristics, such as the occurrence of failure at a certain rate and malfunctions under certain use conditions. Unless designated as a high reliability product or a product for harsh environments in a Renesas Electronics data sheet or other Renesas Electronics document, Renesas Electronics products are not subject to radiation resistance design. You are responsible for implementing safety measures to guard against the possibility of bodily injury, injury or damage caused by fire, and/or danger to the public in the event of a failure or malfunction of Renesas Electronics products, such as safety design for hardware and software, including but not limited to redundancy, fire control and malfunction prevention, appropriate treatment for aging degradation or any other appropriate measures. Because the evaluation of microcomputer software alone is very difficult and impractical, you are responsible for evaluating the safety of the final products or systems manufactured by you.
- 8. Please contact a Renesas Electronics sales office for details as to environmental matters such as the environmental compatibility of each Renesas Electronics product. You are responsible for carefully and sufficiently investigating applicable laws and regulations that regulate the inclusion or use of controlled substances, including without limitation, the EU RoHS Directive, and using Renesas Electronics products in compliance with all these applicable laws and regulations. Renesas Electronics disclaims any and all liability for damages or losses occurring as a result of your noncompliance with applicable aws and regulations
- 9. Renesas Electronics products and technologies shall not be used for or incorporated into any products or systems whose manufacture, use, or sale is prohibited under any applicable domestic or foreign laws or regulations. You shall comply with any applicable export control laws and regulations promulgated and administered by the governments of any countries asserting jurisdiction over the parties or transactions.
- 10. It is the responsibility of the buyer or distributor of Renesas Electronics products, or any other party who distributes, disposes of, or otherwise sells or transfers the product to a third party, to notify such third earty in advance of the contents and conditions set forth in this document
- 11. This document shall not be reprinted, reproduced or duplicated in any form, in whole or in part, without prior written consent of Renesas Electronics
- 12. Please contact a Renesas Electronics sales office if you have any questions regarding the information contained in this document or Renesas Electronics products
- (Note 1) "Renesas Electronics" as used in this document means Renesas Electronics Corporation and also includes its directly or indirectly controlled subsidiaries.

(Note 2) "Renesas Electronics product(s)" means any product developed or manufactured by or for Renesas Electronics

(Rev.4.0-1 November 2017)



SALES OFFICES

Renesas Electronics Corporation

http://www.renesas.com

Refer to "http://www.renesas.com/" for the latest and detailed information.

Renesas Electronics Corporation TOYOSU FORESIA, 3-2-24 Toyosu, Koto-ku, Tokyo 135-0061, Japan

Renesas Electronics America Inc. 1001 Murphy Ranch Road, Milpitas, CA 95035, U.S.A. Tel: +1-408-432-8888, Fax: +1-408-434-5351

Renesas Electronics Canada Limited 9251 Yonge Street, Suite 8309 Richmond Hill, Ontario Canada L4C 9T3 Tel: +1-905-237-2004

Renesas Electronics Europe Limited
Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K
Tel: +44-1628-651-700

Renesas Electronics Europe GmbH

Arcadiastrasse 10, 40472 Düsseldorf, Germany Tel: +49-211-6503-0, Fax: +49-211-6503-1327

Renesas Electronics (China) Co., Ltd.
Room 1709 Quantum Plaza, No.27 ZhichunLu, Haidian District, Beijing, 100191 P. R. China Tel: +86-10-8235-1155, Fax: +86-10-8235-7679

Renesas Electronics (Shanghai) Co., Ltd.

Unit 301, Tower A, Central Towers, 555 Langae Road, Putuo District, Shanghai, 200333 P. R. China Tel: +86-21-2226-0888, Fax: +86-21-2226-0999

Renesas Electronics Hong Kong Limited

Unit 1601-1611, 16IF., Tower 2, Grand Century Place, 193 Prince Edward Road West, Mongkok, Kowloon, Hong Kong Tel: +852-2265-6688, Fax: +852 2886-9022

Renesas Electronics Taiwan Co., Ltd. 13F, No. 363, Fu Shing North Road, Taipei 10543, Taiwan Tel: +886-2-8175-9600, Fax: +886 2-8175-9670

Renesas Electronics Singapore Pte. Ltd.
80 Bendemeer Road, Unit #06-02 Hyflux Innovation Centre, Singapore 339949
Tel: +65-6213-0200, Fax: +65-6213-0300

Renesas Electronics Malaysia Sdn.Bhd.
Unit 1207, Block B, Menara Amcorp, Amcorp Trade Centre, No. 18, Jln Persiaran Barat, 46050 Petaling Jaya, Selangor Darul Ehsan, Malaysia Tel: +60-3-7955-9390, Fax: +60-3-7955-9510

Renesas Electronics India Pvt. Ltd. No.777C, 100 Feet Road, HAL 2nd Stage, Ind Tel: +91-80-67208700, Fax: +91-80-67208777 Indiranagar, Bangalore 560 038, India

Renesas Electronics Korea Co., Ltd. 17F, KAMCO Yangjae Tower, 262, Gangnam-daero, Gangnam-gu, Seoul, 06265 Korea Tel: +82-2-558-3737, Fax: +82-2-558-5338